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(54) **UNIVERSAL SERIAL BUS (USB) MEMORY DEVICE**

6,829,672 B1 12/2004 Deng et al.  
6,854,984 B1 \* 2/2005 Lee ..... G06K 19/07732  
439/607.41

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Miaoli (TW)

7,052,287 B1 5/2006 Ni et al.  
7,069,370 B2 6/2006 Sukegawa et al.  
7,074,052 B1 7/2006 Ni et al.  
7,165,998 B2 1/2007 Lee et al.

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(Continued)

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CN 2142631 Y 9/1993  
CN 1632987 A 6/2005

FOREIGN PATENT DOCUMENTS

(Continued)

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OTHER PUBLICATIONS

**Related U.S. Patent Documents**

Compaq Computer Corp., et al, "Universal Serial Bus Specification, Revision 2.0", Apr. 27, 2000, Chapter 6, pp. 85-118.

Reissue of:

(Continued)

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**H05K 5/02** (2006.01)  
**H01R 13/6598** (2011.01)

(57) **ABSTRACT**

(52) **U.S. Cl.**  
CPC ..... **H05K 5/0278** (2013.01); **H01R 13/6598**  
(2013.01)

A universal serial bus (USB) memory is disclosed. The USB memory includes a housing having a plurality of orientated indentations and a plurality of concave props, wherein the plurality of orientated indentation facilitates the USB memory to be connected while the USB memory is inserted into a female USB socket; a print circuit board assembly (PCBA) disposed in the housing, wherein the PCBA is fixed by means of pressing of the plurality of concave props; and a LED module having a LED indicator disposed in the housing and a LED module controller disposed on the PCBA, wherein a space is formed between the housing and the PCBA for disposing the LED module.

(58) **Field of Classification Search**  
CPC ..... H05K 5/0278; H01R 13/6598  
USPC ..... 361/737; 174/255; 439/351-353, 638,  
439/607

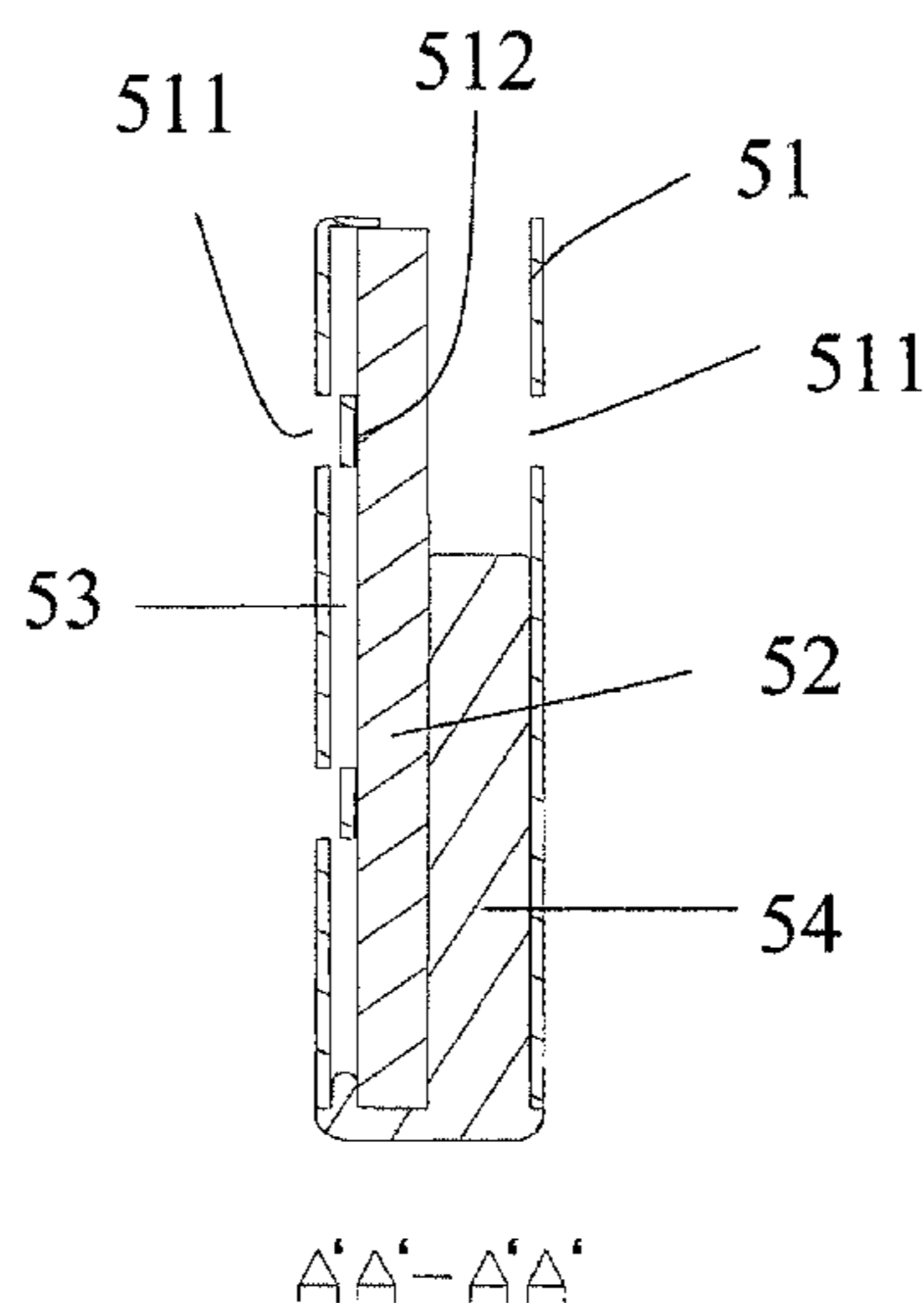
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

6,671,808 B1 \* 12/2003 Abbott ..... G06F 21/32  
705/55  
6,746,282 B2 6/2004 Meister

**15 Claims, 5 Drawing Sheets**



(56)

References Cited

U.S. PATENT DOCUMENTS

|              |     |         |                    |         |
|--------------|-----|---------|--------------------|---------|
| 7,352,601    | B1  | 4/2008  | Minneman et al.    |         |
| 7,507,119    | B2  | 3/2009  | Ni et al.          |         |
| 8,162,696    | B2  | 4/2012  | Elbaz et al.       |         |
| 8,176,276    | B2  | 5/2012  | Galipeau et al.    |         |
| 2004/0027809 | A1  | 2/2004  | Takahashi et al.   |         |
| 2004/0087213 | A1* | 5/2004  | Kao .....          | 439/638 |
| 2004/0259423 | A1  | 12/2004 | Elbaz et al.       |         |
| 2005/0059273 | A1  | 3/2005  | Chiou et al.       |         |
| 2005/0085129 | A1  | 4/2005  | Chiou et al.       |         |
| 2006/0002096 | A1  | 1/2006  | Wang et al.        |         |
| 2007/0066119 | A1* | 3/2007  | Wu et al. ....     | 439/378 |
| 2007/0127223 | A1* | 6/2007  | Mitsubishi .....   | 361/752 |
| 2007/0178769 | A1* | 8/2007  | Ni .....           | 439/660 |
| 2008/0064272 | A1* | 3/2008  | Miller et al. .... | 439/892 |
| 2008/0076280 | A1* | 3/2008  | Chi et al. ....    | 439/131 |

FOREIGN PATENT DOCUMENTS

|    |            |   |         |
|----|------------|---|---------|
| CN | 2731910    | Y | 10/2005 |
| JP | 2003067709 |   | 3/2003  |

|    |             |         |
|----|-------------|---------|
| JP | 2003331249  | 11/2003 |
| JP | 2004-103907 | 2/2004  |
| JP | 3099411     | 4/2004  |
| JP | 2005504380  | 2/2005  |
| JP | 2005158712  | 6/2005  |
| TW | 200306653   | 11/2003 |

OTHER PUBLICATIONS

“PNY’s flash media cards”, Advertisement from El Dorado Trading Group, 2004.

“Five USB Flash-Drives Roundup” <http://www.xbitlabs.com/articles/storage/display/usb-flash-roundup.html>, Jan. 28, 2003.

“DataTraveler Family—USB Flash Drives”, [http://web.archive.org/web/20050204053526/http://www.kingston.com/digitalmedia/datatr . . .](http://web.archive.org/web/20050204053526/http://www.kingston.com/digitalmedia/datatr...), Kingston Technology Company, Feb. 4, 2005.

“Kingston Data Traveler”, Kingston Technology Company, 2004.

“Data Traveler Elite White Paper”, Kingston Technology Company, pp. 1-11.

\* cited by examiner

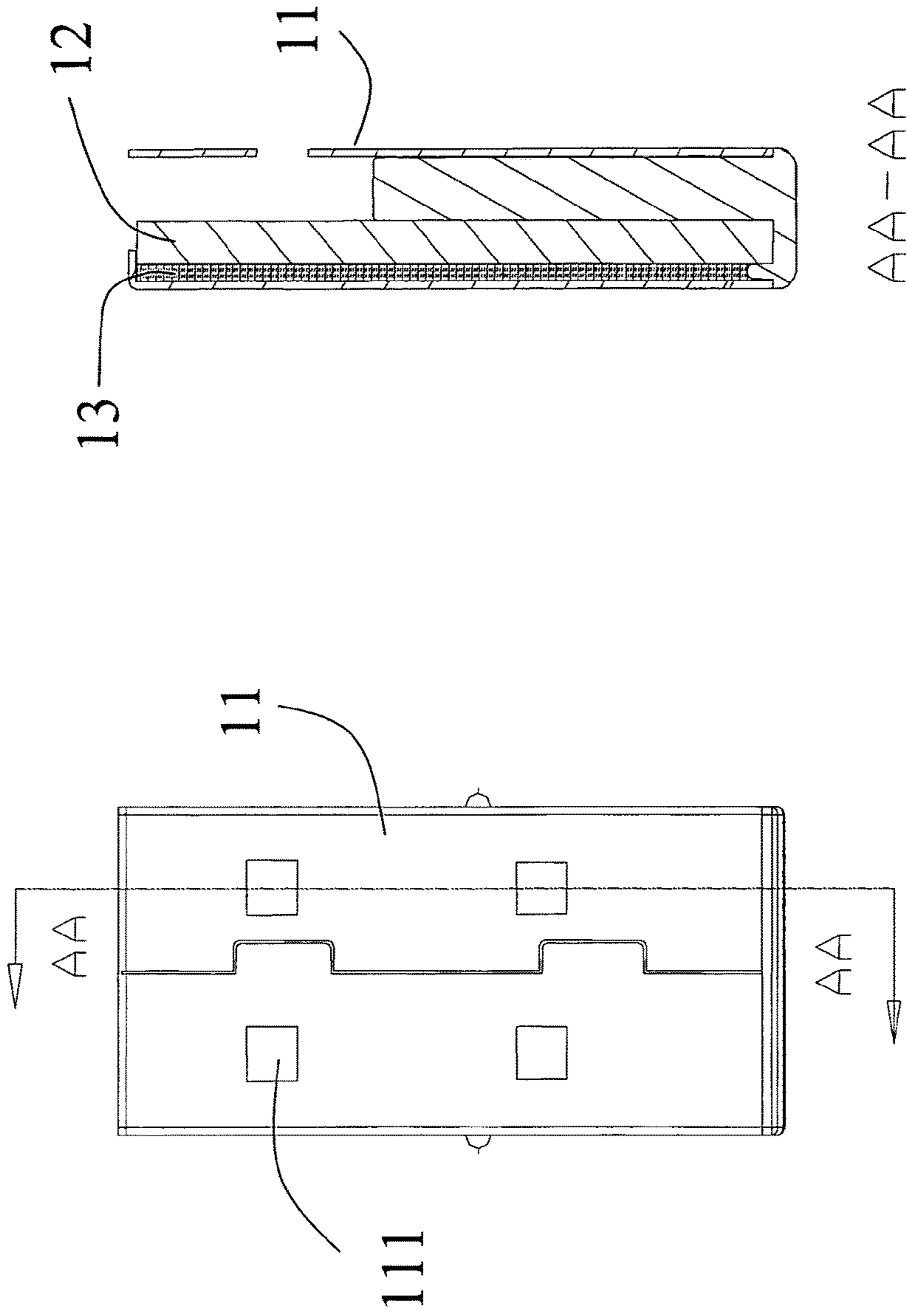


Fig. 2  
(Prior Art)

Fig. 1  
(Prior Art)

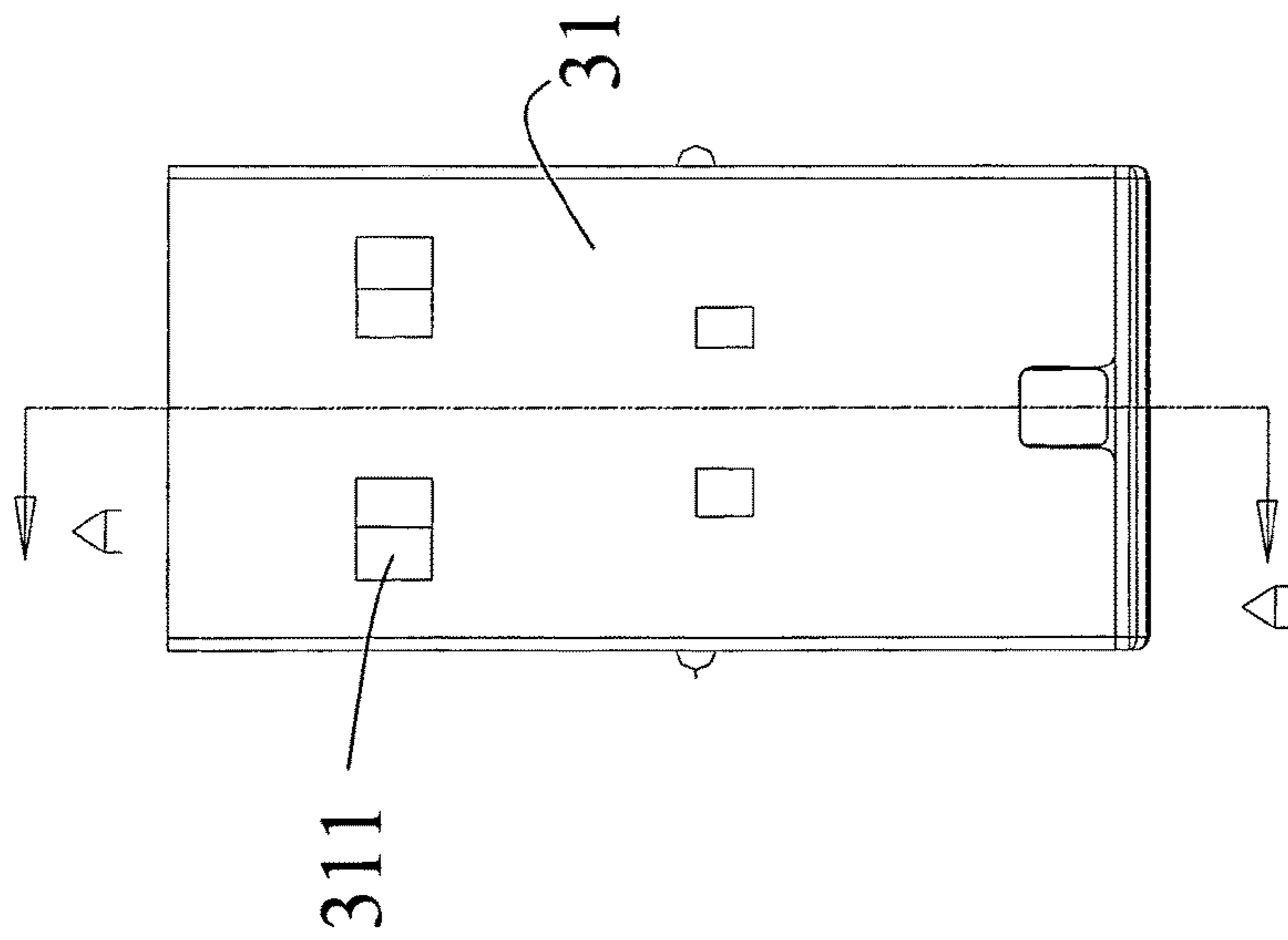


Fig. 3  
(Prior Art)

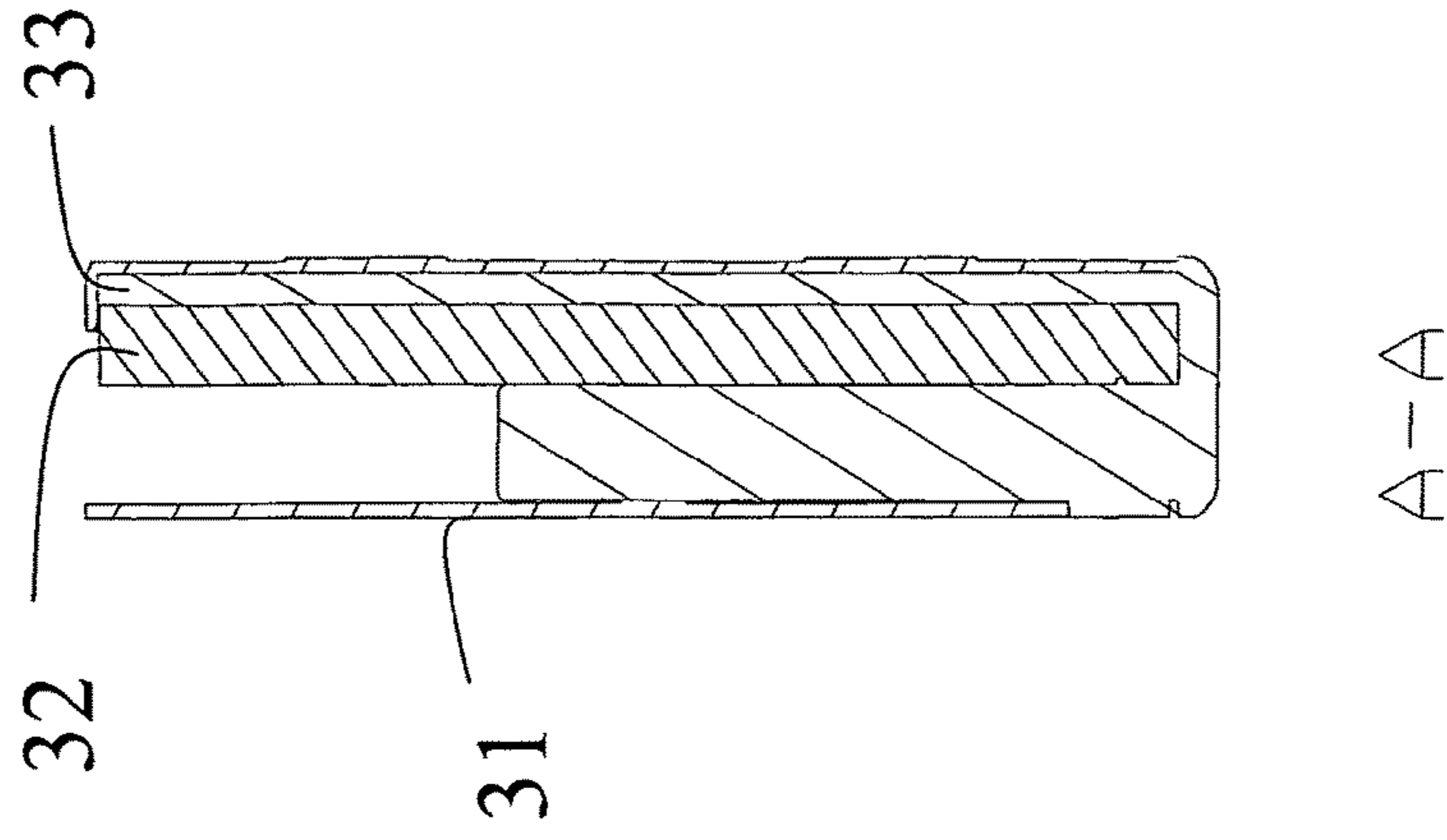


Fig. 4  
(Prior Art)

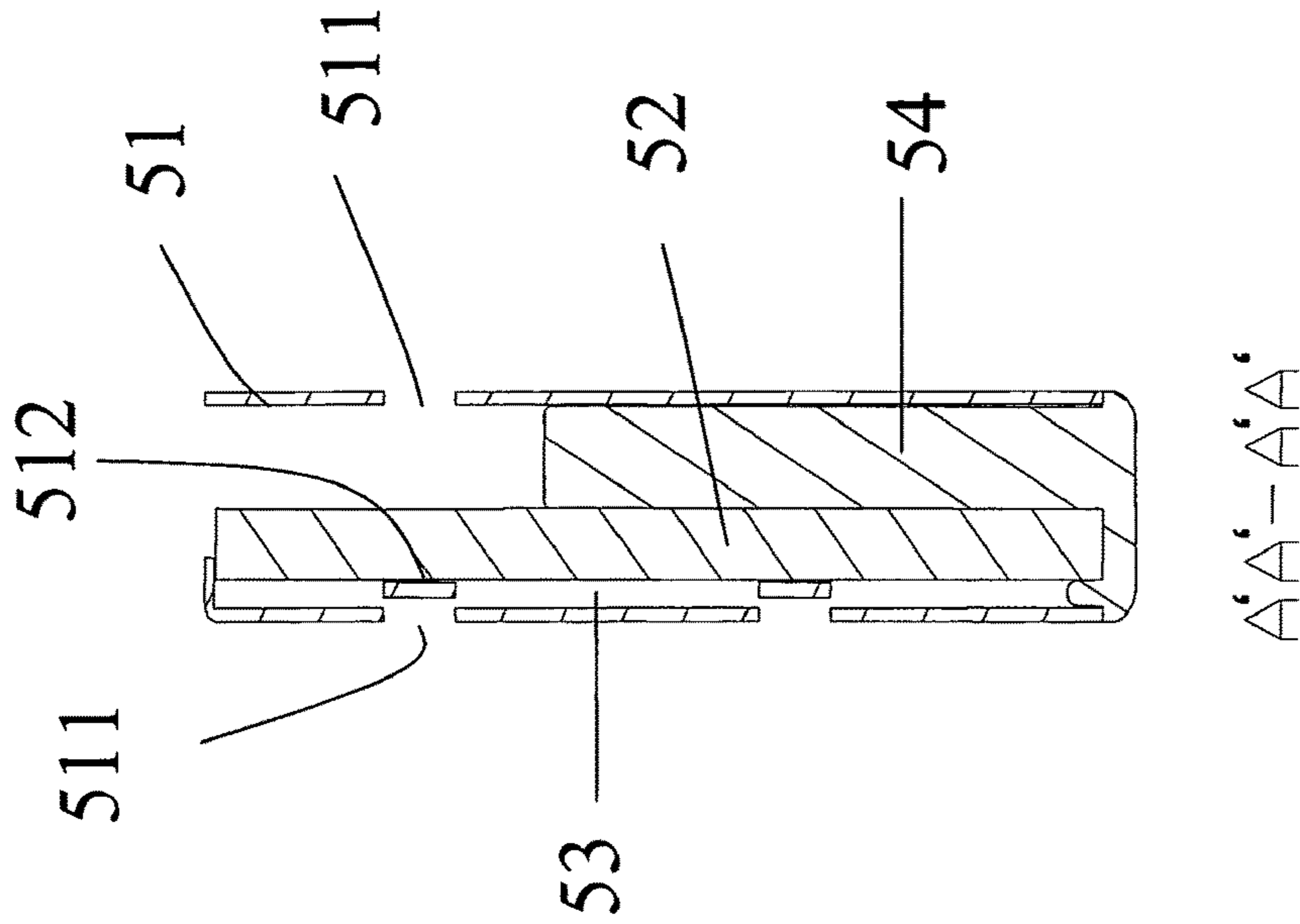


Fig. 5

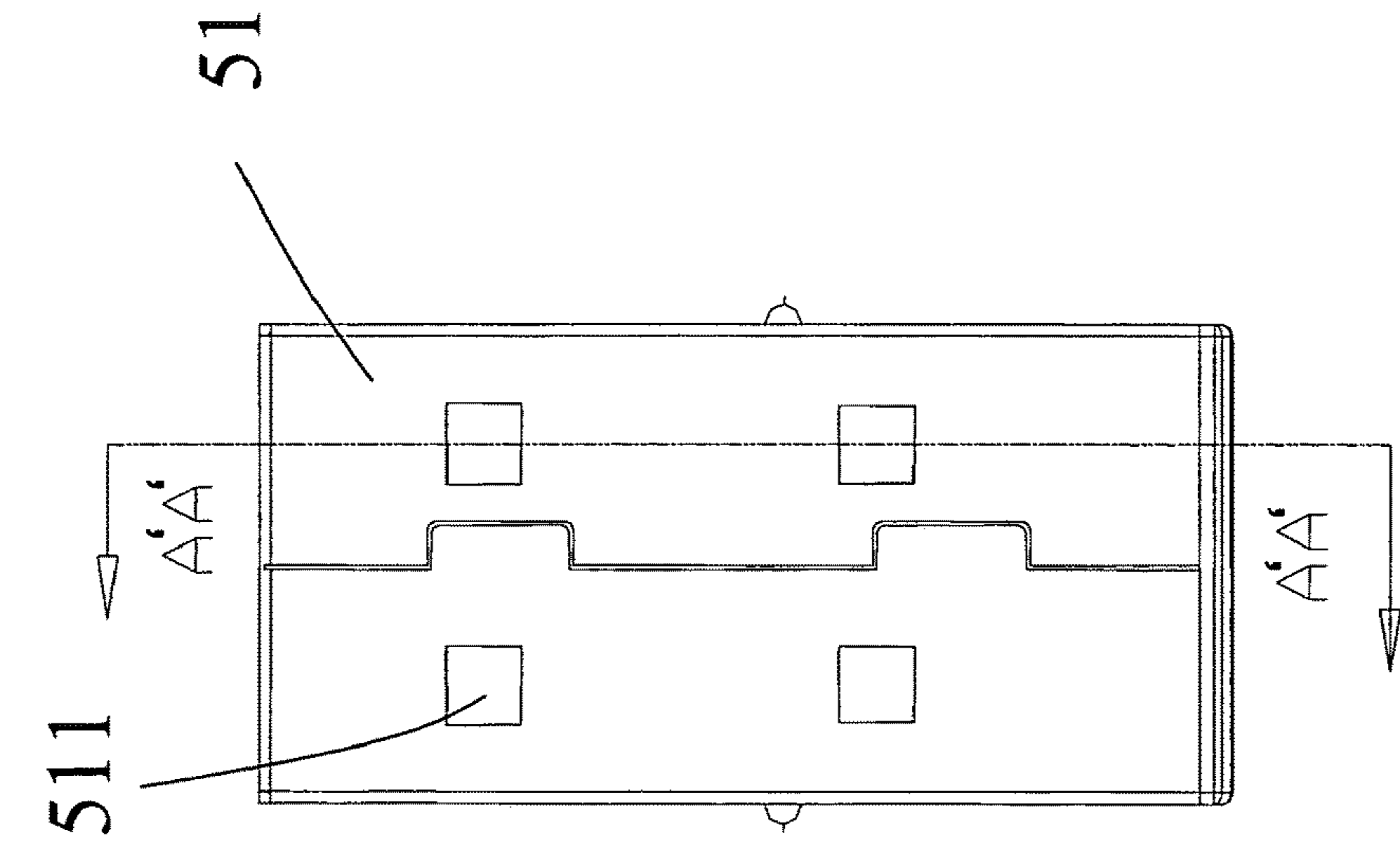


Fig. 6



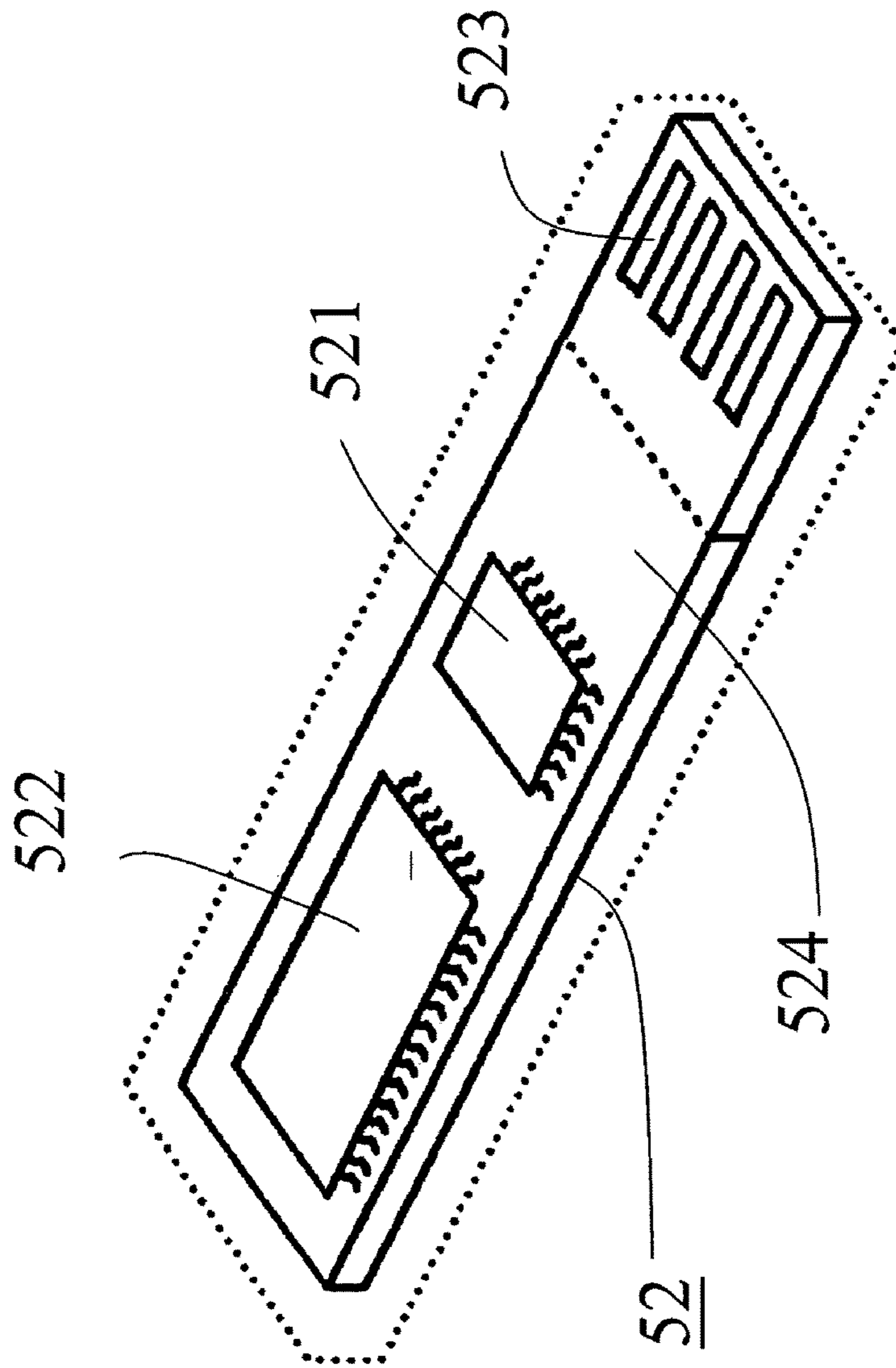


Fig. 7

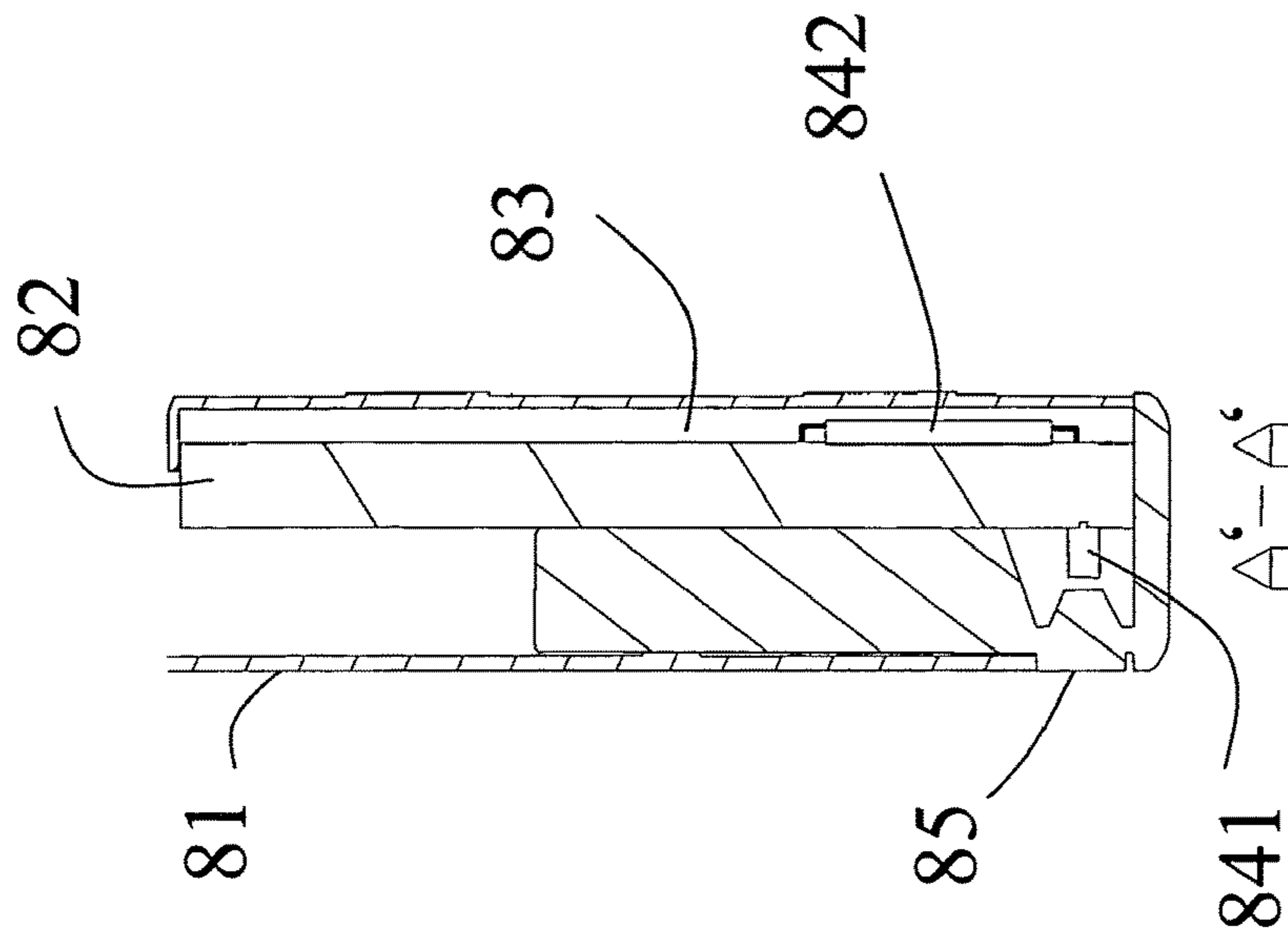


Fig. 9

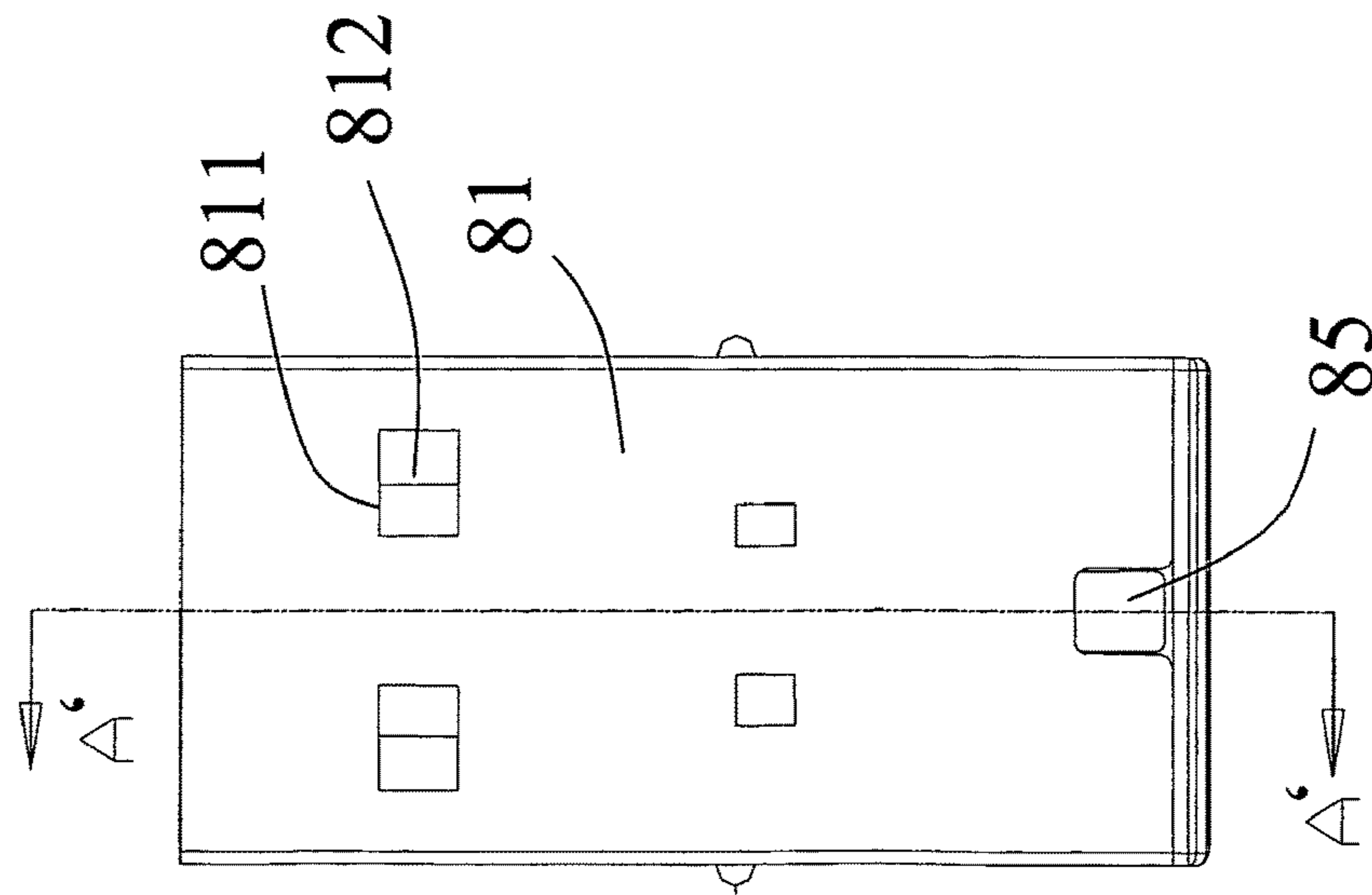


Fig. 8



## UNIVERSAL SERIAL BUS (USB) MEMORY DEVICE

**Matter enclosed in heavy brackets [ ] appears in the original patent but forms no part of this reissue specification; matter printed in italics indicates the additions made by reissue; a claim printed with strikethrough indicates that the claim was canceled, disclaimed, or held invalid by a prior post-patent action or proceeding.**

### CROSS-REFERENCE TO RELATED APPLICATION

*This application is a Reissue Application of U.S. Pat. No. 7,518,879 issued on Apr. 14, 2009, application Ser. No. 11/384,371, filed on Mar. 21, 2006. The entirety of each of the above-mentioned patent applications is hereby incorporated by reference herein and made a part of this specification.*

### FIELD OF THE INVENTION

The present invention relates to a Universal Serial Bus (USB) memory apparatus, and more particularly, to a miniaturized Universal Serial Bus (USB) memory apparatus.

### BACKGROUND OF THE INVENTION

The capabilities of a computer are maximized by utilizing a variety of external peripheral devices that are connected to the computer. This may involve any number of peripherals. In response to computers becoming more modular, a Universal Serial Bus (USB) interface standard was jointly developed. The USB standard defines a high-speed serial data interface between a computer and add-on devices, such as keyboards, printers, scanners and secondary storage devices, including floppy disk drives, hard disk drives, and solid-state secondary storage drives. In computers that have USB compliant ports, such devices can be added or removed while the computer is powered up and running.

With the growing popularity of portable computing devices having USB compliant ports, such as notebook, tablet computers, and personal digital assistants (PDA), the USB ports afford on-the-go flexibility to a user of the portable computing device. That is, the user of a notebook can carry multiple external peripheral devices, and simply plug the needed external peripheral device into the portable computing device when needed.

Recently, flash memory cards are also being sold that contain a USB connector. Such USB-flash memory cards do not require a specialized reader but can be plugged into a USB connector on a personal computer (PC) or other hosting device. These USB-flash memory cards can be used in place of floppy disks and are known as USB key drives, USB thumb drives, and a variety of other names. These USB-flash cards can have a capacity of more than ten floppy disks in an area not much larger than a large postage stamp.

FIG. 1 shows an assembly of a male slim USB connector that is integrated with a circuit-board substrate of a flash memory card according to the prior art; and FIG. 2 shows a cross-sectional view of AA-AA in FIG. 1. As shown in FIGS. 1 and 2, the miniaturized USB Pen Driver includes a metallic conductive housing 11 having a plurality of orientated indentation 111, and a print circuit board assembly (PCBA) 12 for providing storing memory. Furthermore, the thickness of the print circuit board assembly (PCBA) 12 is

usually insufficient, thereby making data reading and storing difficult. Therefore, in order to overcome this drawback, the height needs to be increased with the aid of another object.

Consequently, the miniaturized USB Pen Driver also includes a flake spacer 13 disposed between the metallic conductive housing 11 and the PCBA 12 for fitting the PCBA 12 in the metallic conductive housing 11 tightly, wherein the flake spacer 13 could be made from a metallic slice or a plastic (Mylar) slice. The flake spacer 13 is used to fill up the space between the metallic conductive housing and the PCBA 12. However, in practice, the flake spacer 13 is not easy to be fabricated into the space between the metallic conductive housing 11 and the PCBA 12. Therefore, the USB Pen Driver of the prior art could not be miniaturized effectively.

FIG. 3 shows an assembly of USB memory apparatus according to the prior art, and FIG. 4 shows a cross-sectional view of A-A in FIG. 3. As shown in FIGS. 3 and 4, the USB memory apparatus includes a metallic conductive housing 31 having a plurality of orientated indentation 311, and a print circuit board assembly (PCBA) 32 for providing storing memory. Similarly, the miniaturized USB memory apparatus also includes a flake spacer 33 disposed between the metallic conductive housing 31 and the PCBA 32 for fitting the PCBA 32 in the metallic conductive housing 31 tightly. Due to the flake spacer 33, the inner space of the metallic conductive housing 31 is further decreased. In practice, there is no enough space for introducing a LED indicator. If a LED indicator should be introduced into the USB memory apparatus, the prior art has to add extra space for containing the module of the LED indicator. Therefore, the USB memory apparatus of the prior art can not introduce a LED indicator without increasing extra space, and can not be miniaturized effectively.

Although such multi-application USB memory apparatuses are technically feasible, in practice they are very difficult to implement, as demonstrated by numerous pioneering attempts ever since the invention of the miniaturized USB memory apparatus itself. It needs to provide a miniaturized Universal Serial Bus (USB) memory apparatus, which decreases the cost, simplifies the manufacturing process, is capable of introducing extra module, such as a LED indicator into the USB memory apparatus without increasing entirety volume thereof, and can rectify those drawbacks of the prior art and solve the above problems.

### SUMMARY OF THE INVENTION

This paragraph extracts and compiles some features of the present invention; other features will be disclosed in the follow-up paragraph. It is intended to cover various modifications and similar arrangements included within the spirit and scope of the appended claims, and this paragraph also is considered to refer.

Accordingly, the prior art is limited by the above problems. It is an object of the present invention to provide a Universal Serial Bus (USB) memory apparatus, and in particular a USB memory plug, which decreases the cost, simplifies the manufacturing process, and is capable of introducing an extra module, such as a LED indicator, into the USB memory apparatus without increasing entirely the volume thereof.

In accordance with an aspect of the present invention, the USB memory apparatus includes a housing having a plurality of orientated indentations and a plurality of concave props, wherein the plurality of orientated indentation facilitates the USB memory apparatus to be connected while the



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USB memory apparatus is inserted into a female USB socket; and a print circuit board assembly (PCBA) disposed in the housing, wherein the PCBA is fixed by means of pressing of the plurality of concave props, and a space is formed between the housing and the PCBA.

Preferably, the housing is made from a metallic conductive material.

Certainly, the plurality of orientated indentations and the plurality of concave props are integrally formed by means of punching on the housing.

Preferably, the PCBA further includes a memory controller; a storage memory in communication with the memory controller; a USB interface circuit in communication with the memory controller; and an integrated circuit package for accommodating the memory controller, the storage memory, and the USB interface together within physical dimensions of the USB memory apparatus.

Preferably, the storage memory is one of a flash memory and an Electrically Erasable Programmable Read Only Memory (EEPROM).

Certainly, further includes a LED indicator for indicating operation of the USB memory apparatus.

Preferably, the PCBA further includes a LED module controller for controlling the LED indicator.

Preferably, the housing further includes an opening for positioning the LED indicator.

According to the present invention, the USB memory apparatus includes a print circuit board assembly (PCBA); and a housing having a plurality of concave props, wherein the PCBA is disposed in the housing; the plurality of concave props protrude inward to fix the PCBA, and a space is formed between the housing and the PCBA.

Preferably, the housing is made from a metallic conductive material.

Preferably, the plurality of concave props are formed by means of punching on the housing.

Preferably, the PCBA further includes a memory controller; a storage memory in communication with the memory controller; a USB interface circuit in communication with the memory controller; and an integrated circuit package for accommodating the memory controller, the storage memory, and the USB interface together within physical dimensions of the USB memory apparatus.

Preferably, the storage memory is one of a flash memory and an Electrically Erasable Programmable Read Only Memory (EEPROM).

Certainly, further includes a LED indicator for indicating operation of the USB memory apparatus.

Preferably, the PCBA further includes a LED module controller for controlling the LED indicator.

Preferably, the housing further includes an opening for positioning the LED indicator.

In accordance with another aspect of the present invention, the USB memory apparatus includes a housing having a plurality of orientated indentations and a plurality of concave props, wherein the plurality of orientated indentation facilitates the USB memory apparatus to be connected while the USB memory apparatus is inserted into a female USB socket; an print circuit board assembly (PCBA) disposed in the housing wherein the PCBA is fixed by means of pressing of the plurality of concave props; and a LED module having a LED indicator disposed in the housing and a LED module controller disposed on the PCBA, wherein a space is formed between the housing and the PCBA for disposing the LED module.

Preferably, the housing further includes an opening for positioning the LED indicator.

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The above objects and advantages of the present invention will become more readily apparent to those ordinarily skilled in the art after reviewing the following detailed description and accompanying drawings, in which:

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 illustrates an assembly of a male slim USB connector that is integrated with a circuit-board substrate of a flash memory card according to the prior art.

FIG. 2 illustrates a cross-sectional view of AA-AA in FIG. 1.

FIG. 3 illustrates an assembly of USB memory apparatus according to the prior art.

FIG. 4 illustrates a cross-sectional view of A-A in FIG. 3

FIG. 5 illustrates a preferred embodiment of a USB memory apparatus according to the present invention.

FIG. 6 illustrates a cross-sectional view of A'A'-A'A' in FIG. 5.

FIG. 7 illustrates a PCBA according to the present invention.

FIG. 8 illustrates another preferred embodiment of a USB memory apparatus according to the present invention.

FIG. 9 illustrates a cross-sectional view of A'-A' in FIG. 8.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

The present invention discloses a Universal Serial Bus (USB) memory apparatus, and the objects and advantages of the present invention will become more readily apparent to those ordinarily skilled in the art after reviewing the following detailed description. The present invention needs not be limited to the following embodiment.

Please refer to FIG. 5 and FIG. 6. Meanwhile, FIG. 5 illustrates a preferred embodiment of a USB memory apparatus according to the present invention, and FIG. 6 illustrates a cross-sectional view of A'A'-A'A' in FIG. 5. As shown in FIGS. 5 and 6, the USB memory apparatus includes a housing 51 having a plurality of orientated indentations 511 and a plurality of concave props 512, wherein the plurality of orientated indentation 511 facilitates the USB memory apparatus to be connected while the USB memory apparatus is inserted into a female USB socket (not shown); and a print circuit board assembly (PCBA) 52 disposed in the housing 51, wherein the PCBA 52 is fixed by means of pressing of the plurality of concave props 512; and there is a space 53 between the housing 51 and the PCBA 52.

In practice, the housing 51 is usually made from a metallic conductive material, wherein the plurality of orientated indentations 511 and the plurality of concave props 512 can be formed simultaneously by means of punching on the housing 51. In this embodiment, one end of the housing 51 is also regarded as a USB male connector, and then the height of the insertion end of the housing 51 is the same as a standard height of a standard USB male connector. Meanwhile, the plurality of orientated indentations 511 are located to engage metal springs on the USB socket (not shown), whereby the plurality of orientated indentations provide a secure fit into the USB socket when inserted. Moreover, the PCBA 52 is combined with an end base 54. Usually, the PCBA 52 should include several components. Please refer to FIG. 7. It illustrates a PCBA according to the present invention, wherein the PCBA 52 further includes a memory controller 521; a storage memory 522 in communication with the memory controller 521; a USB interface circuit 523



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in communication with the memory controller 521; and an integrated circuit package 524 for maintaining the memory controller 521, the storage memory 522, and the USB interface 523 together within physical dimensions of the USB memory apparatus. Meanwhile, the storage memory 522 is one of a flash memory and an Electrically Erasable Programmable Read Only Memory (EEPROM). Certainly, the PCBA 52 could introduce any electronic component according to the requirement. Specially, the USB memory apparatus of the present invention could introduce more components by means of using to the space 53 between the housing 51 and the PCBA 52.

Please refer to FIG. 8 and FIG. 9. Meanwhile, FIG. 8 illustrates another preferred embodiment of a USB memory apparatus according to the present invention, and FIG. 9 illustrates a cross-sectional view of A'-A' in FIG. 8. As shown in FIGS. 8 and 9, the USB memory apparatus includes a housing 81 having a plurality of orientated indentations 811 and a plurality of concave props 812, wherein the plurality of orientated indentation 811 (which is located to engage metal springs on the female USB socket) facilitates the USB memory apparatus to be connected while the USB memory apparatus is inserted into a female USB socket; a print circuit board assembly (PCBA) 82 disposed in the housing 81, wherein the PCBA 82 is fixed by means of pressing of the plurality of concave prop 812 (Please refer to FIG. 6, marked as 512); and a LED module having a LED indicator 841 disposed in the housing 81 and a LED module controller 842 disposed on the PCBA 82, wherein there is a space 83 between the housing 81 and the PCBA 82 for facilitating the LED module to be disposed.

In practice, the housing 81 could further include an opening 85 for facilitating the LED indicator 841 to emit light. The housing 81 is usually made from a metallic conductive material, wherein the plurality of orientated indentations 811, the plurality of concave props 812, and the opening 85 are unitary. Similar to the above descriptions, one end of the housing 81 is also regarded as a USB male connector, and then the height of the insertion end of the housing 81 is the same as a standard height of a standard USB male connector. Meanwhile, the plurality of orientated indentations 811 provide a secure fit into the USB socket when inserted. Actually, the housing 81 could also be regarded as an entire case of the USB memory apparatus, thereby decreasing the entire volume of the USB memory apparatus. Similarly, the PCBA 82, equal to FIG. 7, marked 52, could include a memory controller 521; a storage memory 522 in communication with the memory controller 521; a USB interface circuit 523 in communication with the memory controller 521; and an integrated circuit package 524 for maintaining the memory controller 521, the storage memory 522, and the USB interface 523 together within physical dimensions of the USB memory apparatus, wherein the storage memory 522 is a flash memory or an Electrically Erasable Programmable Read Only Memory (EEPROM). Certainly, the PCBA 82 could introduce any electronic component according to the requirement. In this embodiment, the USB memory apparatus further introduces a LED module without increasing entire volume thereof. In accordance with the aspect of the present invention, the USB memory apparatus of the present invention could introduce more components by means of using to the space 83 between the housing 81 and the PCBA 82.

In conclusion, the present invention provides a Universal Serial Bus (USB) memory apparatus, which decreases the cost and increases the yield because the flake spacer is not introduced. Moreover, it simplifies the manufacturing pro-

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cess because the PCBA could be propped up by punched concave props instead of uncontrollable flake spacer. Additionally, the USB memory apparatus of the present invention is capable of introducing extra module, such as a LED indicator into the USB memory apparatus without increasing entire volume thereof by means of using to the space between the housing and the PCBA. On the other hand, the housing could also be regarded as an entire case of the USB memory apparatus directly. Thus, the USB memory apparatus of the present invention can be miniaturized effectively. Certainly, the USB memory apparatus of the present invention could further include a decorated case covering the housing according to the multi-type design. Meanwhile the prior art fail to disclose that. Accordingly, the present invention possesses many outstanding characteristics, effectively improves upon the drawbacks associated with the prior art in practice and application, produces practical and reliable products, bears novelty, and adds to economical utility value. Therefore, the present invention exhibits a great industrial value.

While the invention has been described in terms of what is presently considered to be the most practical and preferred embodiments, it is to be understood that the invention needs not be limited to the disclosed embodiment. On the contrary, it is intended to cover various modifications and similar arrangements included within the spirit and scope of the appended claims, which are to be accorded with the broadest interpretation so as to encompass all such modifications and similar structures.

What is claimed is:

**[1. A Universal Serial Bus (USB) memory plug, comprising:**

a housing having a plurality of orientated indentations and a plurality of concave props, wherein said plurality of orientated indentation facilitates said USB memory plug to be connected while said USB memory plug is inserted into a female USB socket; and a print circuit board assembly (PCBA) disposed in said housing, wherein said PCBA is fixed by means of pressing of said plurality of concave props, and a space is formed between said housing and said PCBA.]

**[2. The USB memory plug according to claim 1, wherein said housing is made from a metallic conductive material.]**

**[3. The USB memory plug according to claim 1, wherein said PCBA further comprises:**

a memory controller;  
a storage memory in communication with said memory controller;  
a USB interface circuit in communication with said memory controller; and  
an integrated circuit package for accommodating said memory controller, said storage memory, and said USB interface circuit together within physical dimensions of said USB memory plug.]

**[4. The USB memory plug according to claim 3, wherein said storage memory is one of a flash memory and an Electrically Erasable Programmable Read Only Memory (EEPROM).]**

**[5. The USB memory plug according to claim 1, further comprising a LED indicator for indicating operation of said USB memory plug.]**

**[6. The USB memory plug according to claim 5, wherein said PCBA further comprises a LED module controller for controlling said LED indicator.]**

**[7. The USB memory plug according to claim 5, wherein said housing further comprises an opening for positioning said LED indicator.]**



**[8.** The USB memory plug according to claim 1, wherein the plurality of concave props are used to support said PCBA so that a space is formed between said housing and said PCBA.]

**[9.** A USB memory plug, comprising:  
a print circuit board assembly (PCBA); and  
a housing having a plurality of concave props, wherein said PCBA is disposed in said housing; said plurality of concave props protrude inward to fix said PCBA, and a space is formed between said housing and said PCBA.]

**[10.** The USB memory plug according to claim 9, wherein said housing is made from a metallic conductive material.]

**[11.** The USB memory plug according to claim 9, wherein said PCBA further comprises:

a memory controller;  
a storage memory in communication with said memory controller;  
a USB interface circuit in communication with said memory controller; and  
an integrated circuit package for accommodating said memory controller, said storage memory, and said USB interface circuit together within physical dimensions of said USB memory plug.]

**[12.** The USB memory plug according to claim 11, wherein said storage memory is one of a flash memory and an Electrically Erasable Programmable Read Only Memory (EEPROM).]

**[13.** The USB memory plug according to claim 9, further comprising a LED indicator for indicating operation of said USB memory plug.]

**[14.** The USB memory plug according to claim 13, wherein said PCBA further comprises a LED module controller for controlling said LED indicator.]

**[15.** The USB memory plug according to claim 13, wherein said housing further comprises an opening for positioning said LED indicator.]

**[16.** The USB memory plug according to claim 9, wherein the plurality of concave props are used to support said PCBA so that said PCBA is not in direct contact with said housing, and a space is formed between said housing and said PCBA.]

**[17.** A USB memory plug, comprising:  
a housing having a plurality of orientated indentations and a plurality of concave props, wherein said plurality of orientated indentation facilitates said USB memory plug to be connected while said USB memory plug is inserted into a female USB socket;

a print circuit board assembly (PCBA) disposed in said housing, wherein said PCBA is fixed by means of pressing of said plurality of concave props; and  
a LED module having a LED indicator disposed in said housing and a LED module controller disposed on said PCBA, wherein a space is formed between said housing and said PCBA for disposing said LED module.]

**[18.** The USB memory plug according to claim 17, wherein said housing further comprises an opening for positioning said LED indicator.]

**[19.** The USB memory plug according to claim 17, wherein the plurality of concave props are used to support said PCBA so that said PCBA is not in direct contact with said housing, and a space is formed between said housing and said PCBA.]

**[20.** The USB memory plug according to claim 17, wherein said PCBA further comprises:  
a memory controller;

a storage memory in communication with said memory controller;

a USB interface circuit in communication with said memory controller; and

an integrated circuit package for accommodating said memory controller, said storage memory, and said USB interface circuit together within physical dimensions of said USB memory plug.]

**[21.** The USB memory plug according to claim 20, wherein the LED module projects from the integrated circuit package.]

*22. A Universal Serial Bus (USB) memory device, comprising:*

*a housing having a plurality of orientated indentations of a side of said housing, a first plurality of concave props concaved inwards into an interior of the side of said housing, and a second plurality of concave props concaved inwards into the interior of the side of said housing, wherein said first plurality of concave props coincide with a portion of said plurality of orientated indentations on an insertion end of said housing, said second plurality of concave props are away from said insertion of said housing; and*

*a print circuit board assembly (PCBA) disposed in the interior of said housing, wherein said first and second plurality of concave props support said PCBA apart from said housing to form a space between said housing and said PCBA.*

*23. The USB memory device according to claim 22, wherein said housing is made from a metallic conductive material.*

*24. The USB memory device according to claim 22, wherein said PCBA further comprises:*

*a memory controller;  
a storage memory in communication with said memory controller;  
a USB interface circuit in communication with said memory controller; and*

*an integrated circuit package for accommodating said memory controller, said storage memory, and said USB interface circuit together within physical dimensions of said USB memory device.*

*25. The USB memory device according to claim 24, wherein said storage memory is one of a flash memory and an Electrically Erasable Programmable Read Only Memory (EEPROM).*

*26. The USB memory device according to claim 22, further comprising a LED indicator inside of said housing for indicating operation of said USB memory device.*

*27. The USB memory device according to claim 26, wherein said PCBA further comprises a LED module controller for controlling said LED indicator.*

*28. The USB memory device according to claim 26, wherein said housing further comprises an opening for positioning said LED indicator.*

*29. The USB memory device according to claim 22, further comprising an electronic component disposed on said PCBA and located in said space formed between said housing and said PCBA.*

*30. The USB memory device according to claim 29, wherein said electronic component is projected from said PCBA.*

*31. A Universal Serial Bus (USB) memory device adapted to be inserted into a female USB socket comprising a plurality of springs, said USB memory device comprising:  
a housing comprising:*



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a first plurality of concave props located on a first side of said housing, wherein said first plurality of concave props are concaved inwards into an interior of the first side of said housing to form recesses on an insertion end of said housing;

a first plurality of orientated indentations located on a second side of said housing opposite to said first side in corresponding to said first plurality of concave props, wherein said first plurality of orientated indentations and said recesses formed by said first plurality of concave props are located to engage said plurality of springs while said USB memory device is inserted into said female USB socket;

a second plurality of concave props located on the first side of said housing, wherein said second plurality of concave props are concaved inwards into the interior of the first side of said housing, said second plurality of concave props are away from said insertion end of said housing; and

a print circuit board assembly (PCBA) disposed in the interior of said housing, wherein said first and second plurality of concave props support said PCBA apart from said housing, and a space is formed between said housing and said PCBA and nearby said first side of said housing.

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32. The USB memory device according to claim 31, wherein said housing is made from a metallic conductive material.

33. The USB memory device according to claim 31, wherein said PCBA further comprises:  
 5 a memory controller;  
 a storage memory in communication with said memory controller;  
 a USB interface circuit in communication with said memory controller; and  
 10 an integrated circuit package for accommodating said memory controller, said storage memory, and said USB interface circuit together within physical dimensions of said USB memory device.

34. The USB memory device according to claim 31, wherein an orthogonal projection area of said PCBA on said first side of said housing is located within and smaller than an area of said first side of said housing.

35. The USB memory device according to claim 33, wherein said USB interface circuit comprises a plurality of contacts exposed by said integrated circuit package and electrically connected to said memory controller.

36. The USB memory device according to claim 31, wherein said space formed between said housing and said PCBA and between said first plurality of concave props and said second plurality of concave props is continuous space.

\* \* \* \* \*



UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : RE46,871 E  
APPLICATION NO. : 14/672246  
DATED : May 22, 2018  
INVENTOR(S) : Chung et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification

At Column 1, under the heading "*CROSS-REFERENCE TO RELATED APPLICATION*," replace Lines 15-16 (approx.) and the beginning portion of Line 17 (approx.) that includes "*11/384,371, filed Mar. 21, 2006.*" with the following:

*--NOTICE: More than one reissue application has been filed for the reissue of U.S. Patent No. 7,518,879 B2. The reissue applications are U.S. Reissue Patent Application Serial No. 15/853,981, filed on December 26, 2017, now U.S. Reissue Patent No. RE48,179 E, issued August 25, 2020, which is a continuation reissue application of U.S. Reissue Patent Application Serial No. 14/672,246 (the present application), filed on March 30, 2015, now U.S. Reissue Patent No. RE46,871 E, issued May 22, 2018, which is a reissue application of U.S. Patent Application Serial No. 11/384,371, filed on March 21, 2006, now U.S. Patent No. 7,518,879 B2, issued April 14, 2009.--*

Signed and Sealed this  
Twenty-third Day of March, 2021



Drew Hirshfeld  
*Performing the Functions and Duties of the  
Under Secretary of Commerce for Intellectual Property and  
Director of the United States Patent and Trademark Office*