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(19) United States

(12) Reissued Patent Nakano

(10) Patent Number: US RE40,094 E

(45) Date of Reissued Patent: Feb. 26, 2008

(54)	COMPUT	ER		
(75)	Inventor:	Rumi Nakano, Tokyo (JP)		
(73)	Assignee:	Sony Corporation, Tokyo (JP)		
(21)	Appl. No.:	29/218,399		
(22)	Filed:	Dec. 2, 2004		
Related U.S. Patent Documents				
Reiss	ue of:			
(64)	Patent No.	: Des. 466,499		
` ′	Issued:	Dec. 3, 2002		

(64)	Patent No.:	Des. 466,499
	Issued:	Dec. 3, 2002
	Appl. No.:	29/145,270
	Filed:	Jul. 20, 2001

(51)	LOC (8) Cl
(52)	U.S. Cl. D14/328
(58)	Field of Classification Search
	D14/322, 327, 328; D18/1, 2, 7, 11; 235/145 A,
	235/145 R; 341/22, 23; 345/104, 156, 168,
	345/169, 173; 361/680–686
	Cas application file for complete goods history

See application file for complete search history.

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PLLC

Primary Examiner—Freda S. Nunn (74) Attorney, Agent, or Firm—Rader, Fishman & Grauer,

(57) CLAIM

The ornamental design for a computer, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a computer showing my new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a rear elevational view thereof;

FIG. 4 is a left side elevational view thereof;

FIG. 5 is a right side elevational view thereof;

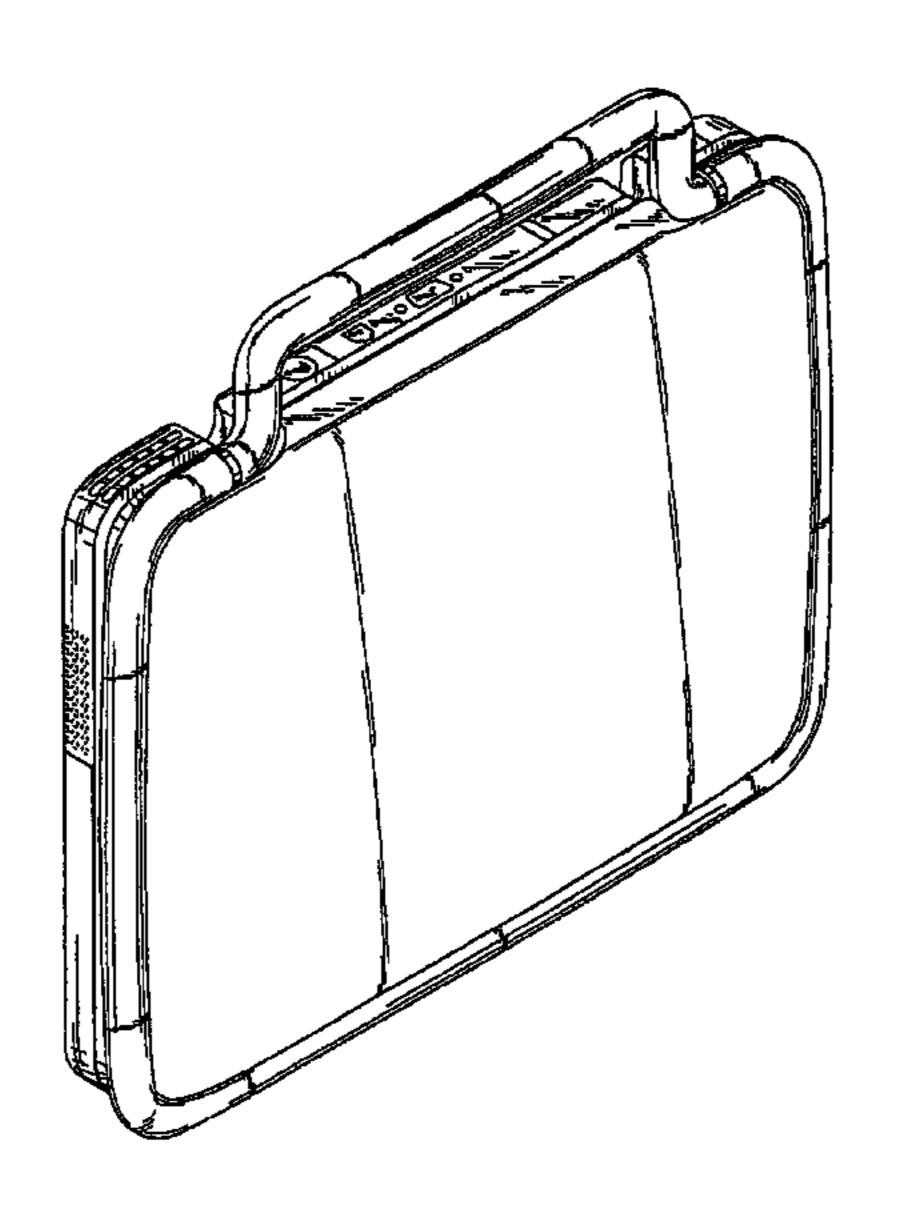
FIG. 6 is a top plan view thereof; and,

FIG. 7 is a bottom plan view thereof.

Portions of FIGS. 1 and 4 that are shown as broken lines are for illustrative purpose only and form no part of the claimed design.

1 Claim, 4 Drawing Sheets

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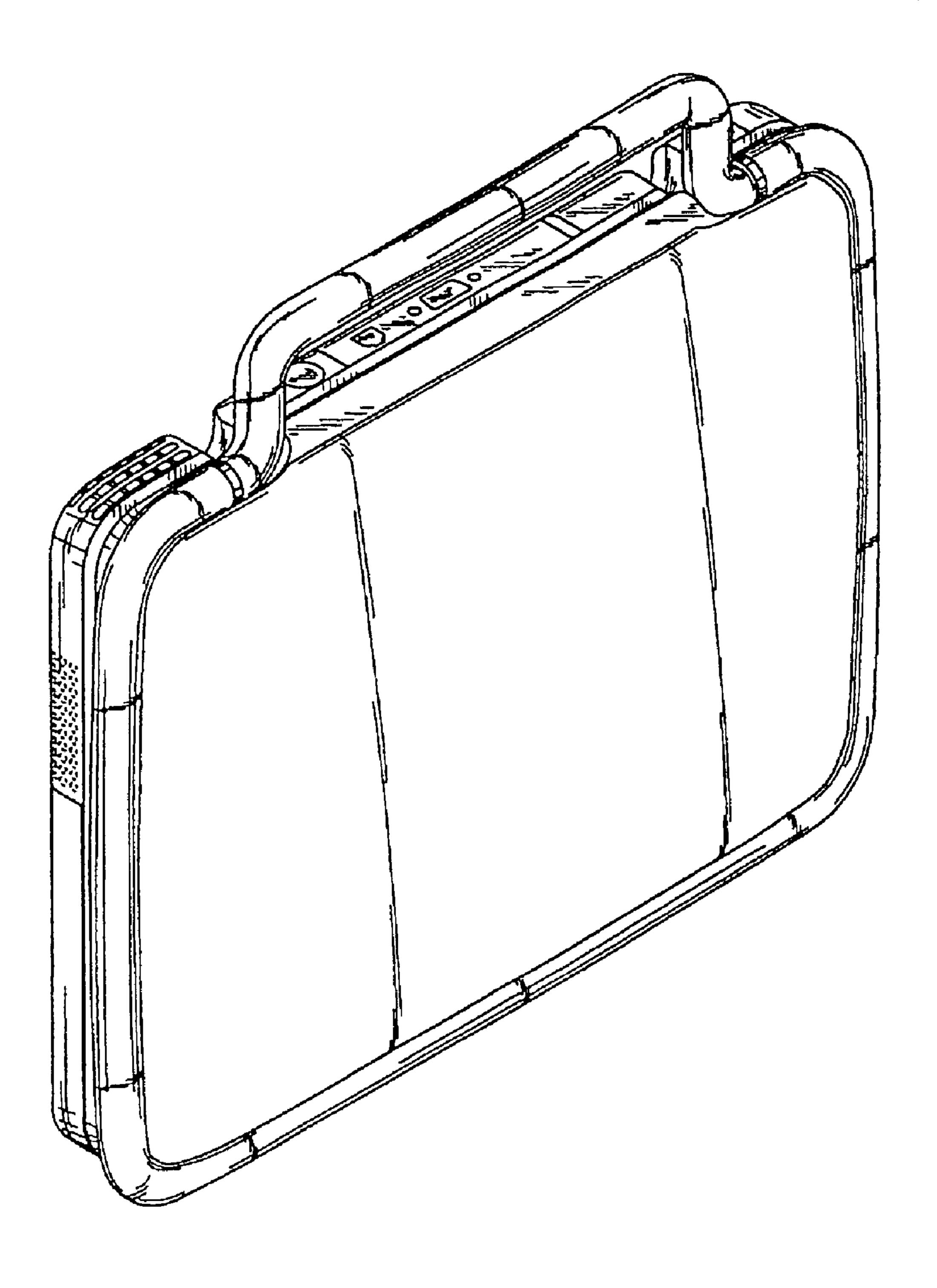


Fig. 1

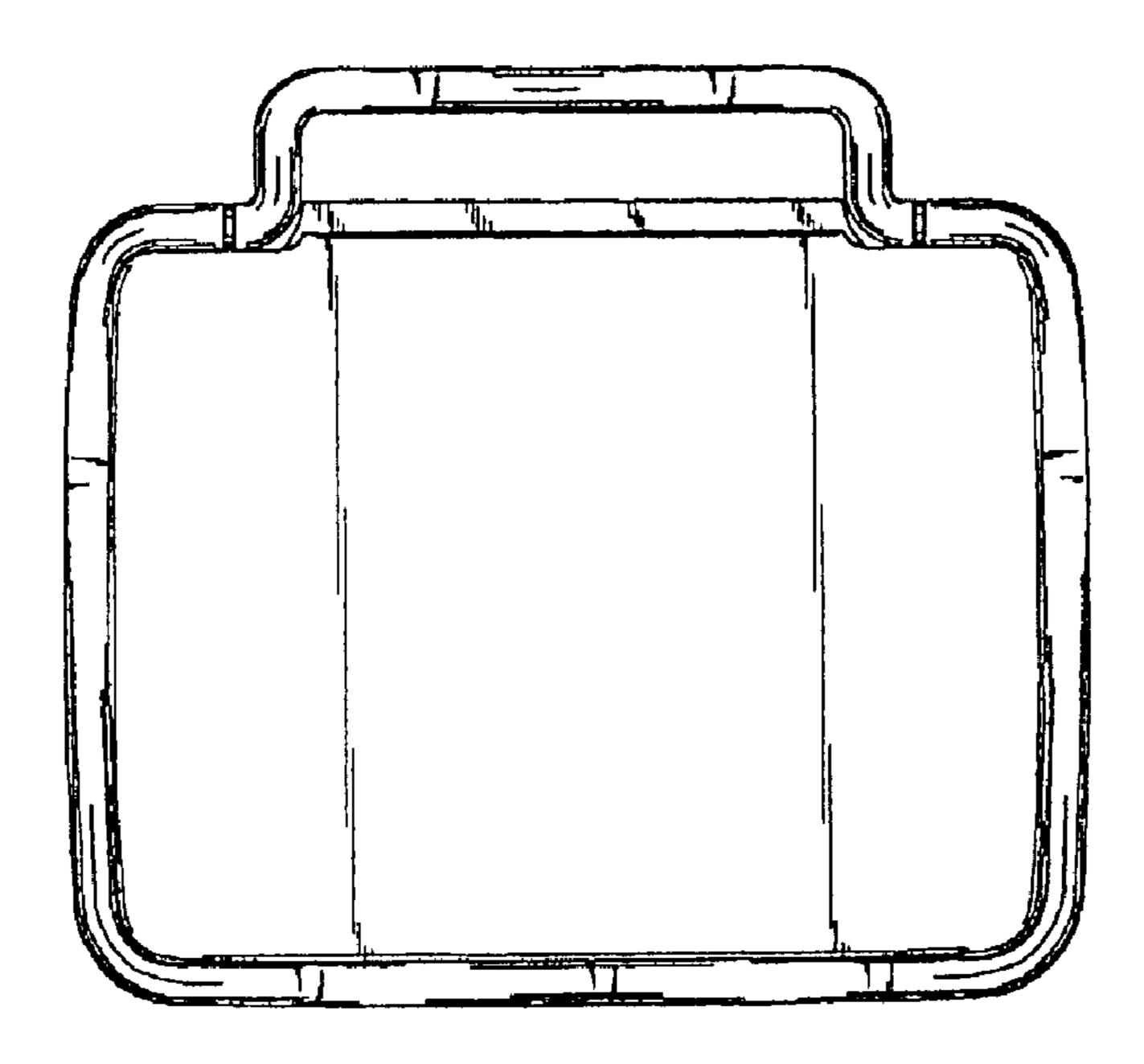


Fig. 2

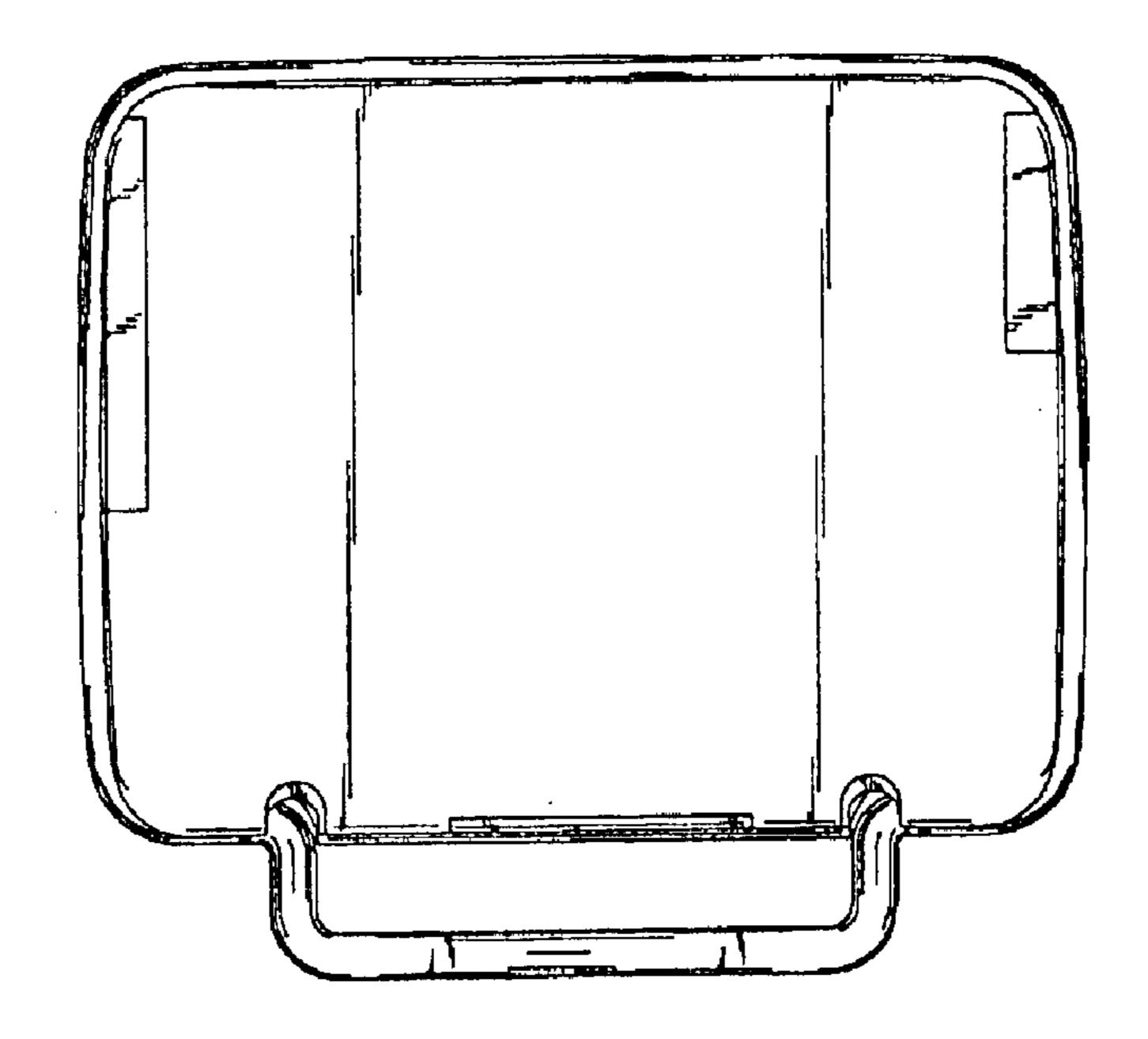


Fig. 3

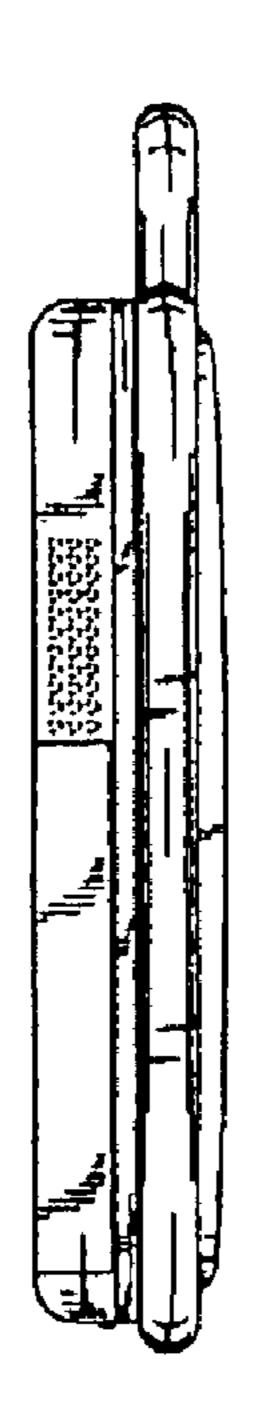


Fig. 4



Fig. 5

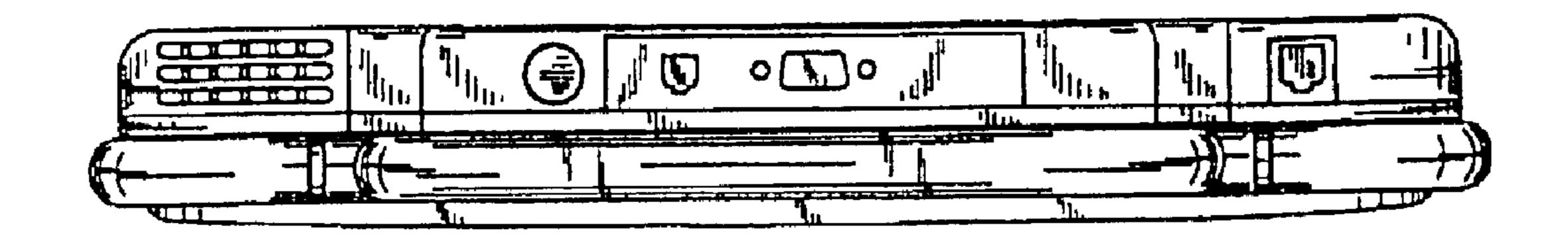


Fig. 6

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Fig. 7

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : RE 40,094 E

APPLICATION NO. : 29/218399

DATED : February 26, 2008

INVENTOR(S) : Nakano

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page illustrating a figure(s) should be deleted, and replaced with title page illustrating a figure(s). (attached)

Delete drawing sheets 1-4, and replace with new drawing sheets 1-4. (attached)

Signed and Sealed this

Twenty-third Day of June, 2009

JOHN DOLL
Acting Director of the United States Patent and Trademark Office

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Nakano

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- (75) Inventor: Rumi Nakano, Tokyo (JP)
- (73) Assignee: Sony Corporation, Tokyo (JP)
- (21) Appl. No.: 29/218,399
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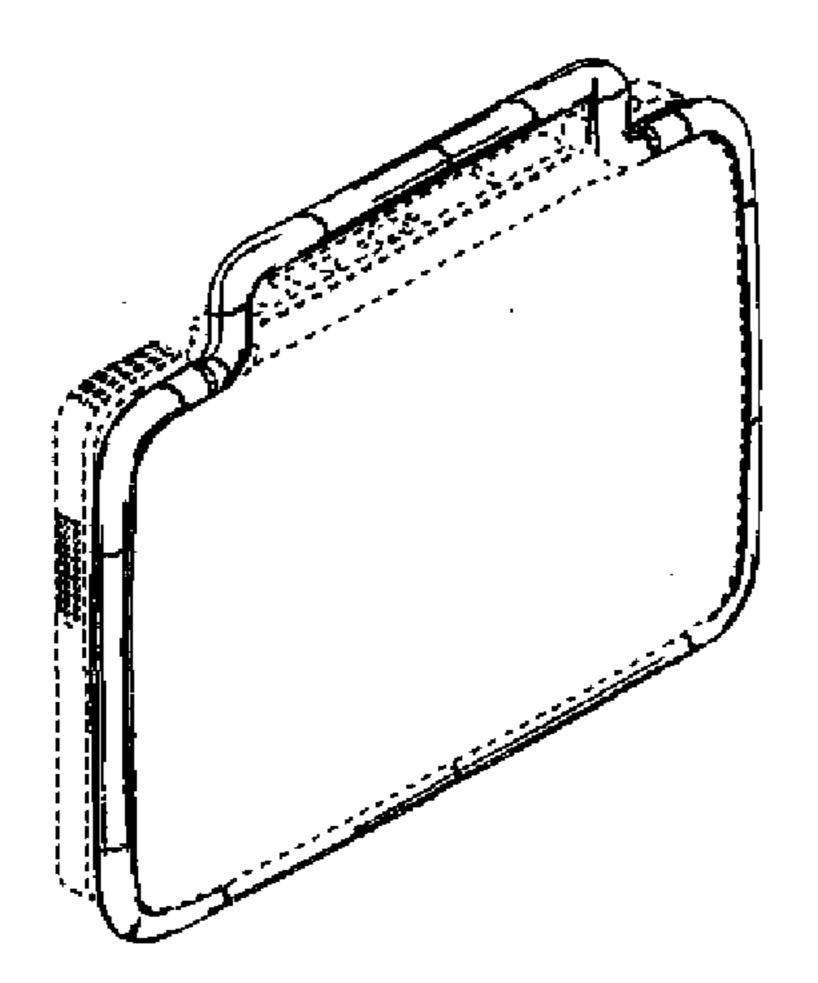
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Sheet 1 of 4

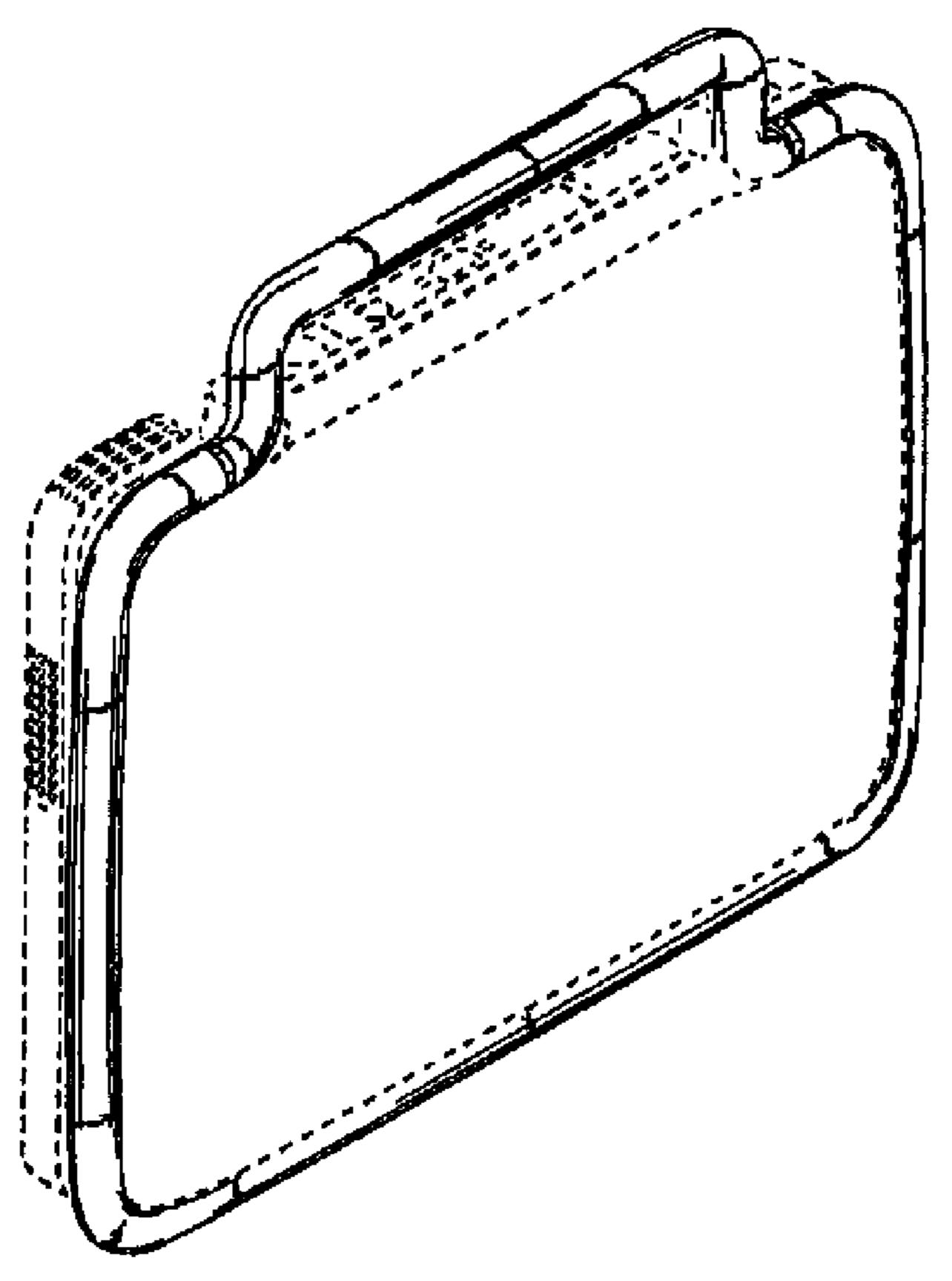


Fig.1

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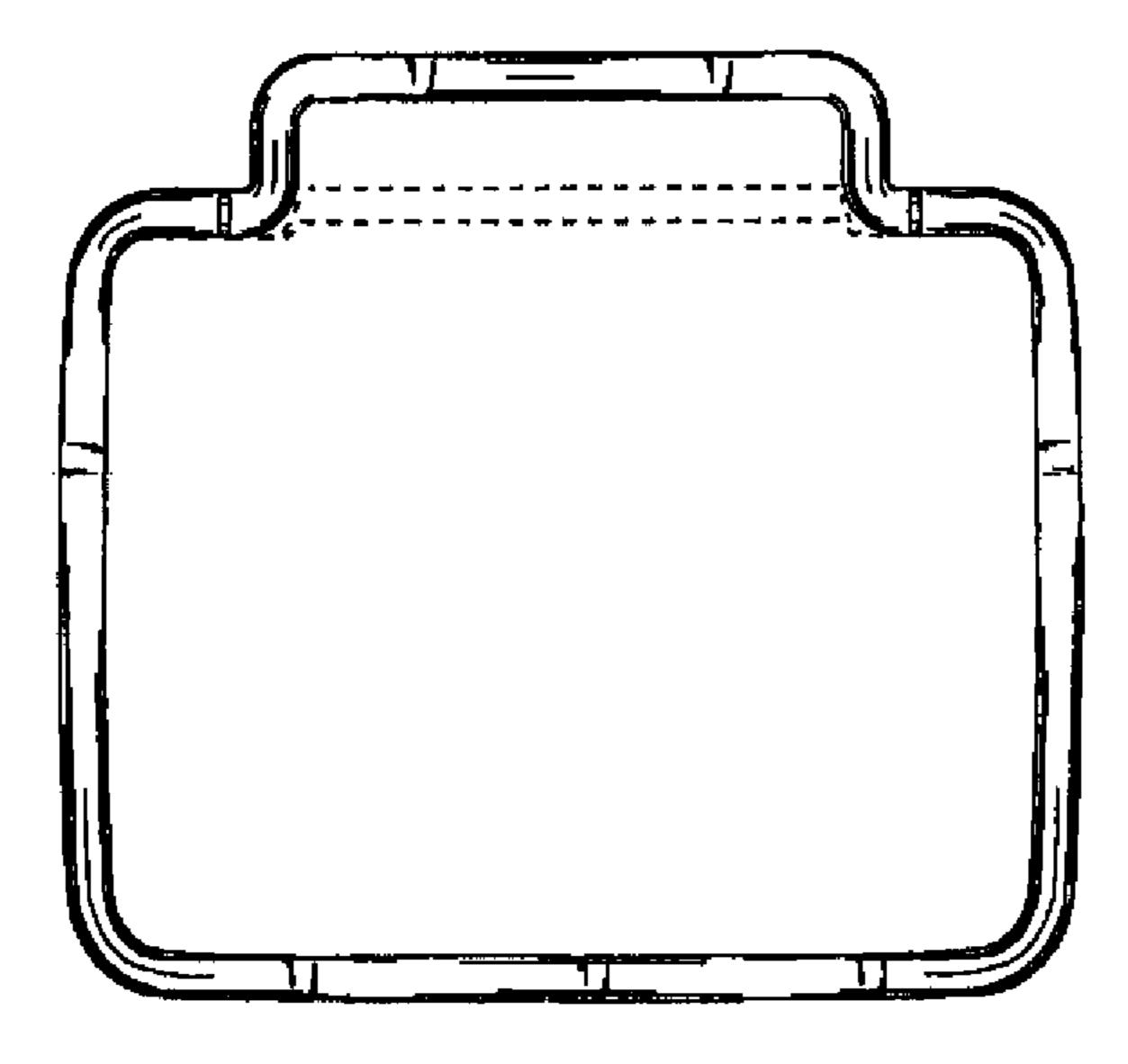
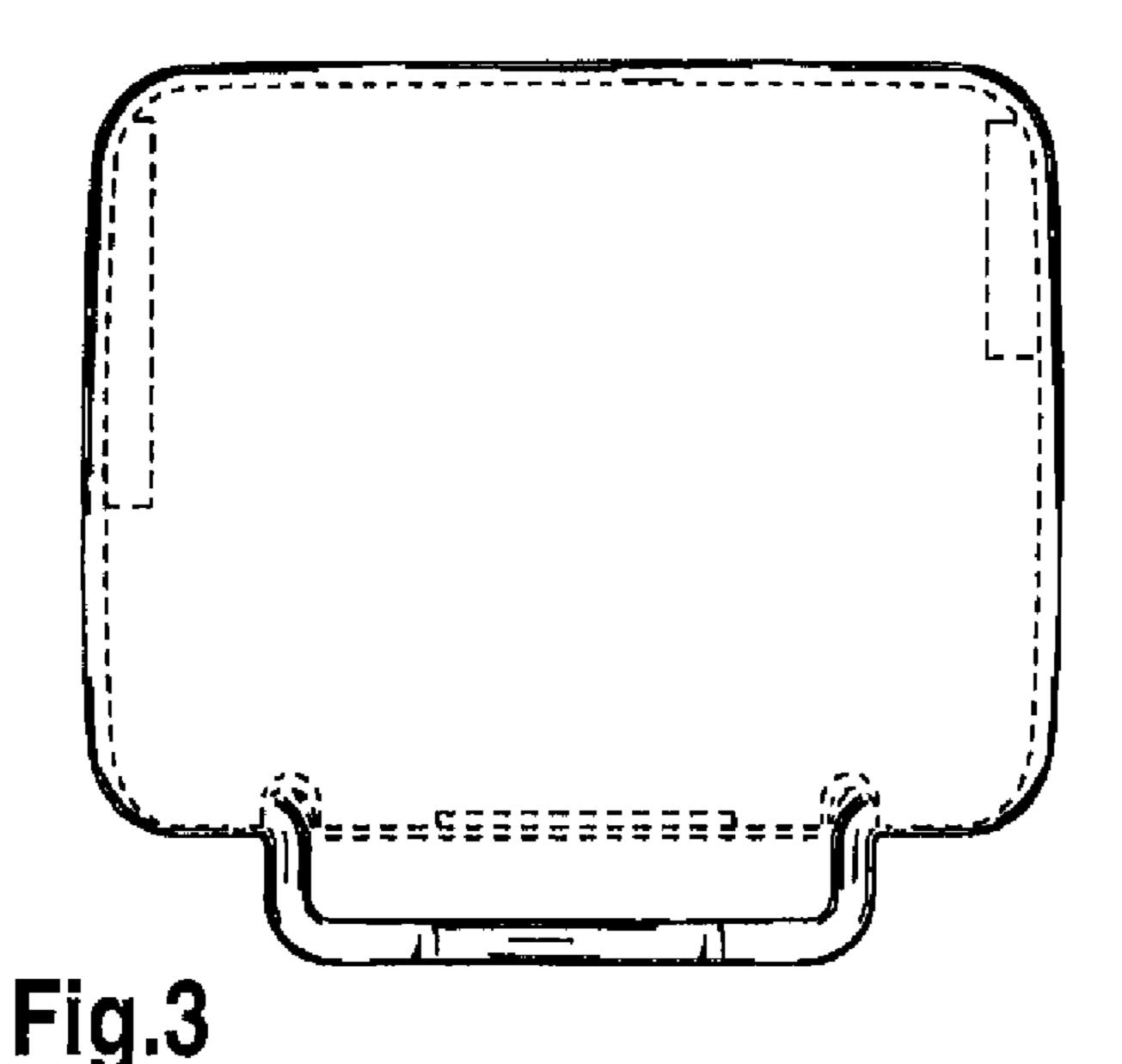


Fig.2



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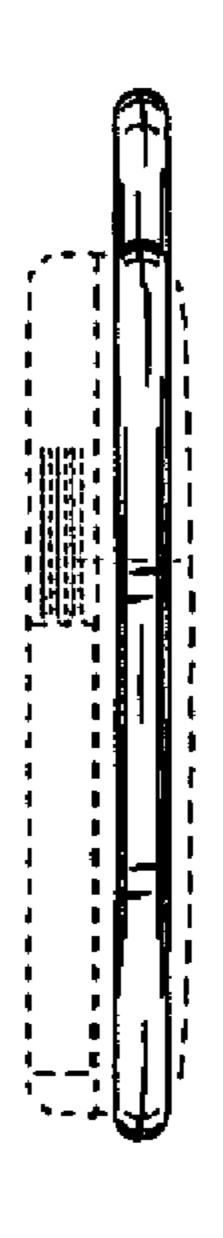


Fig. 4

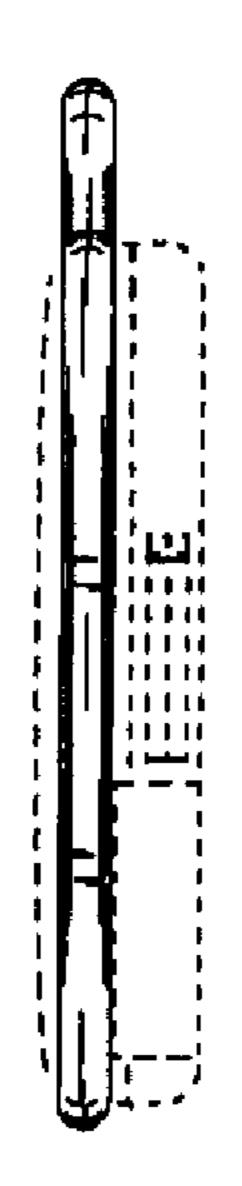


Fig. 5

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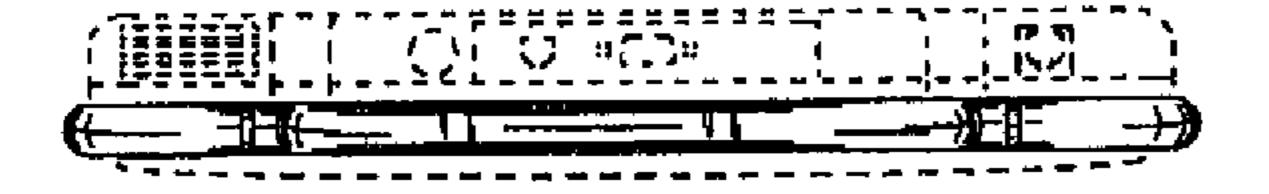


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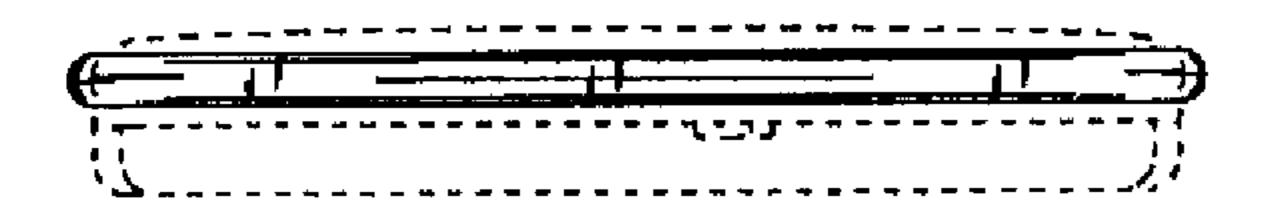


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This certificate supersedes the Certificate of Correction issued June 23, 2009.

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JOHN DOLL

Acting Director of the United States Patent and Trademark Office

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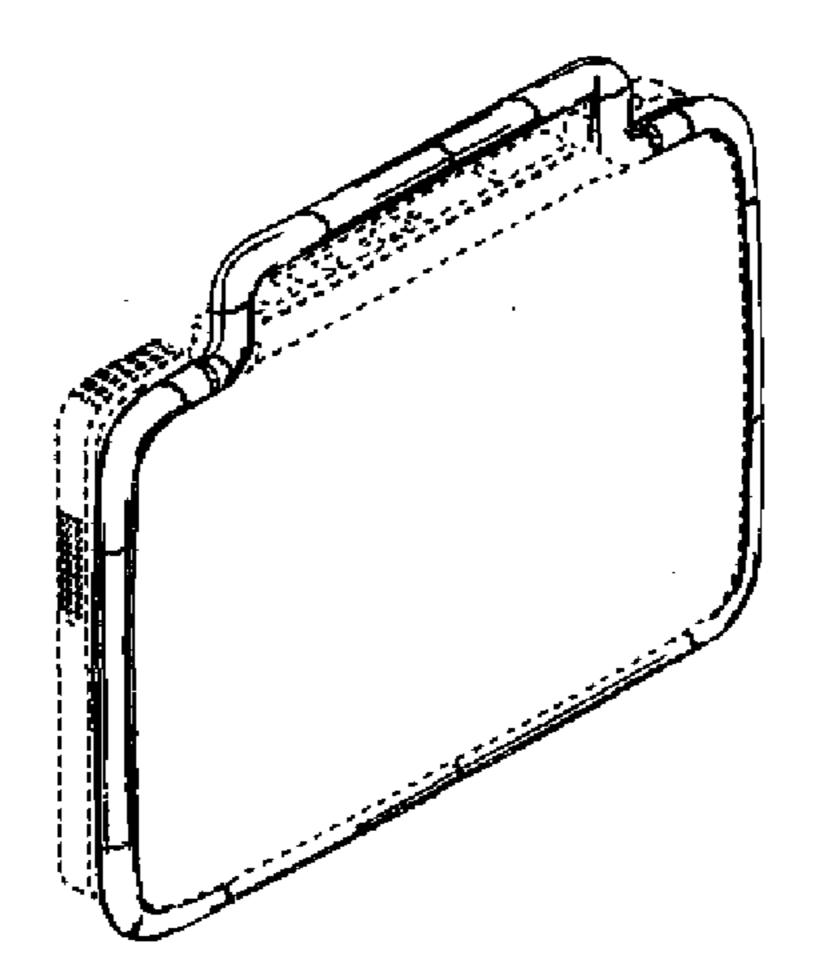
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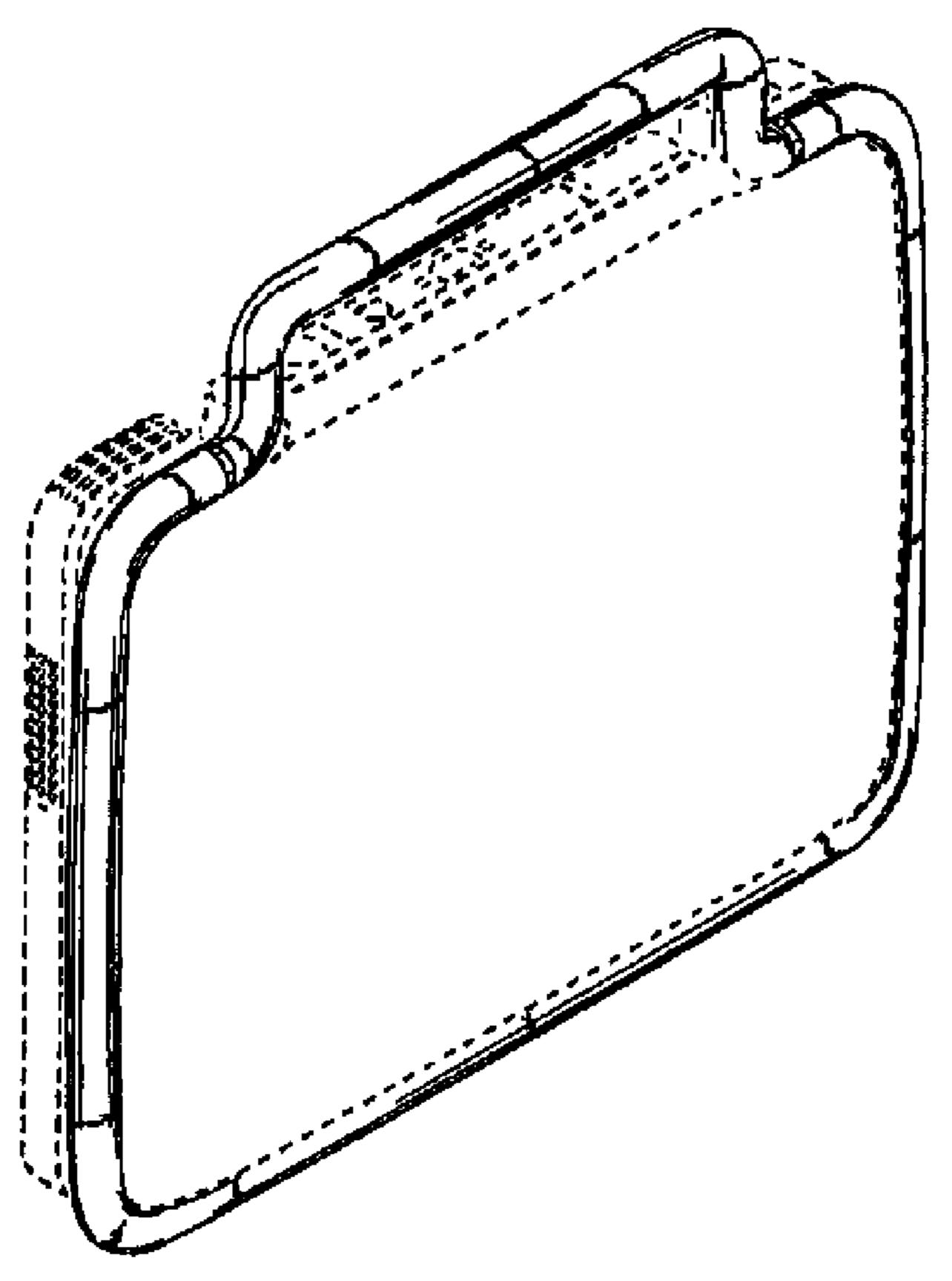


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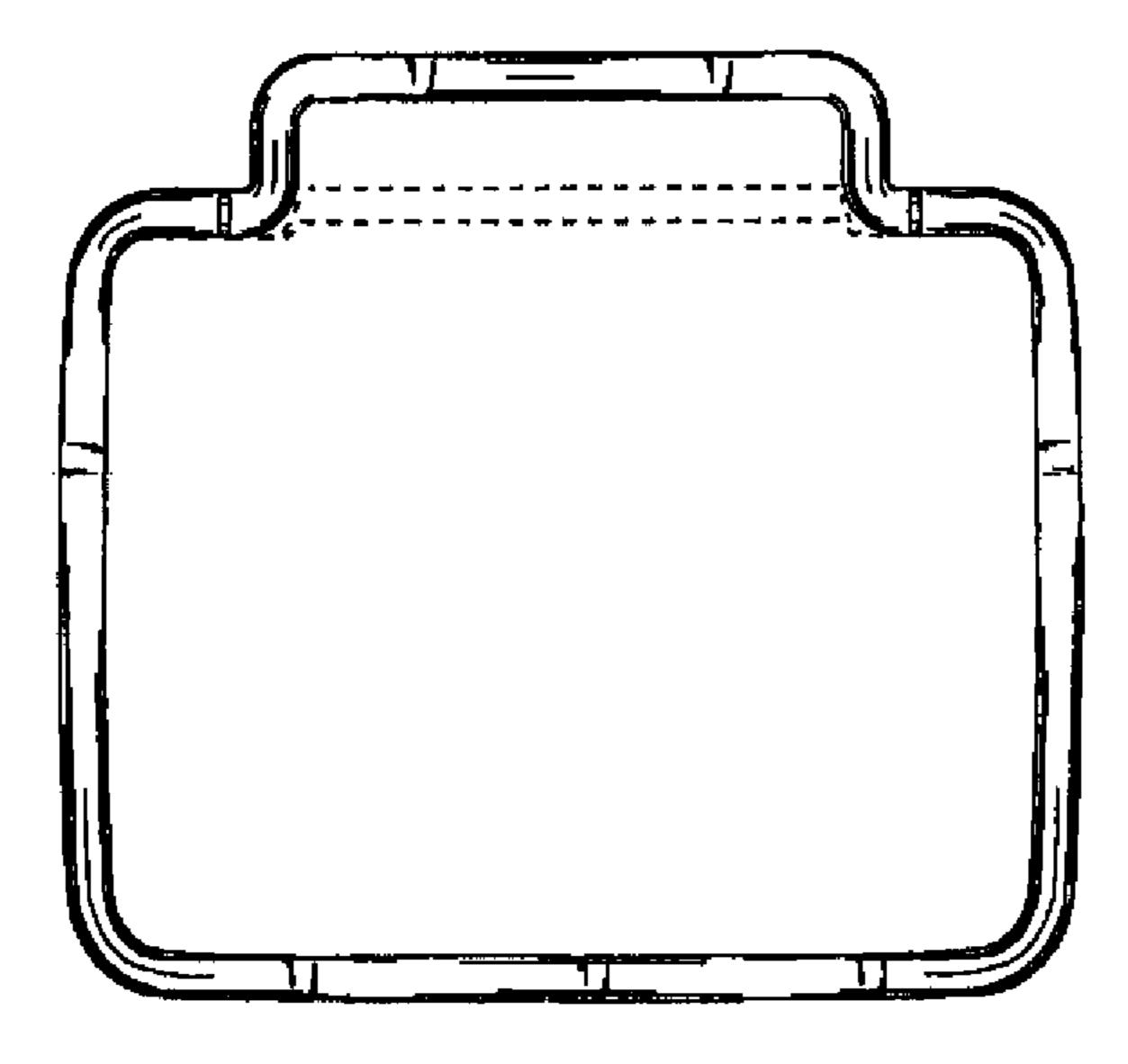
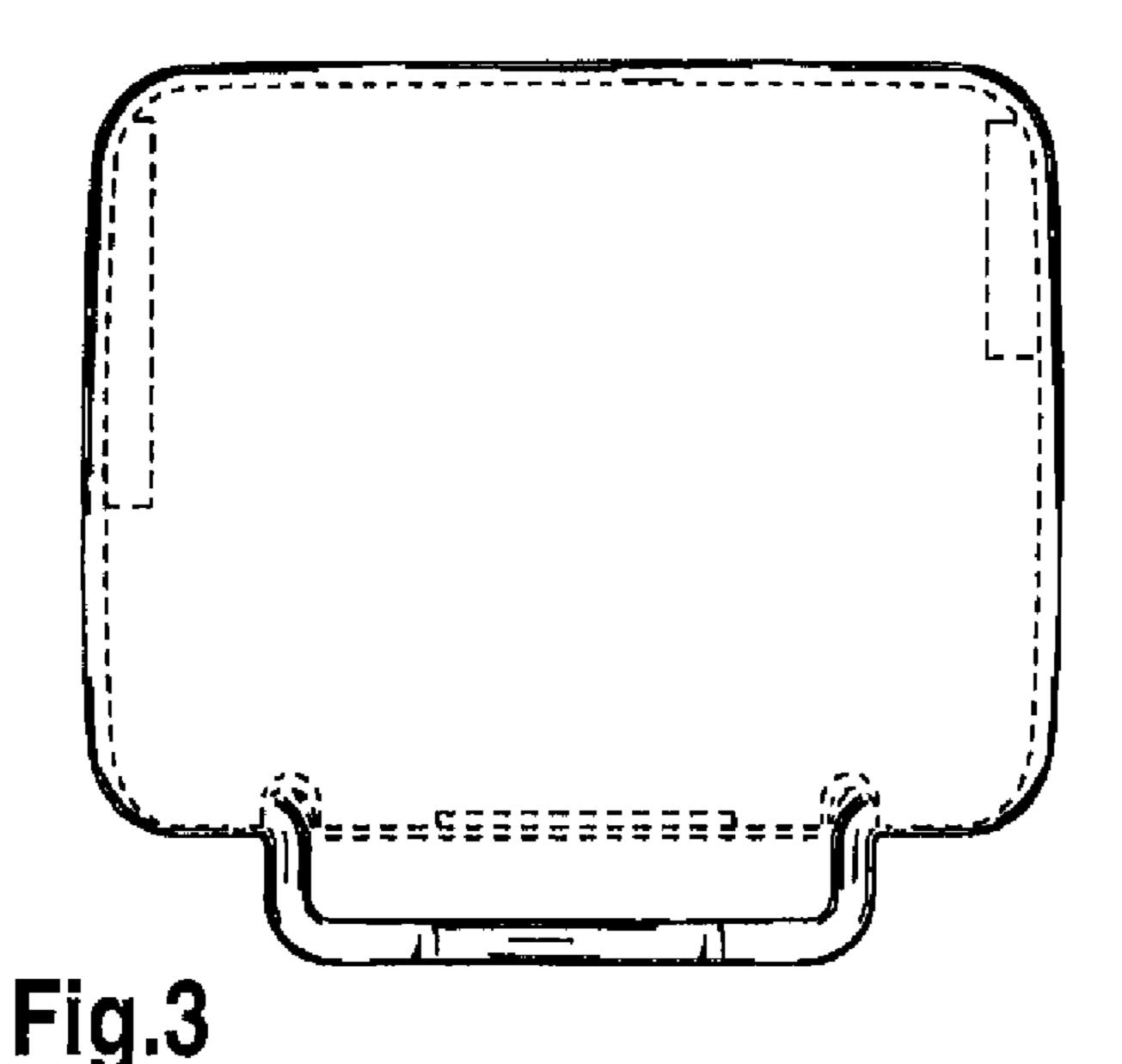


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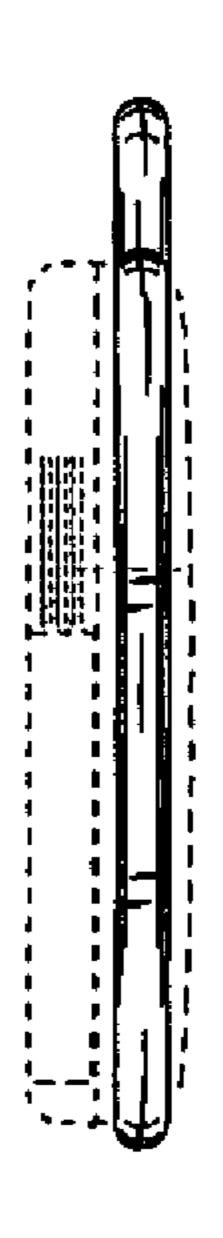


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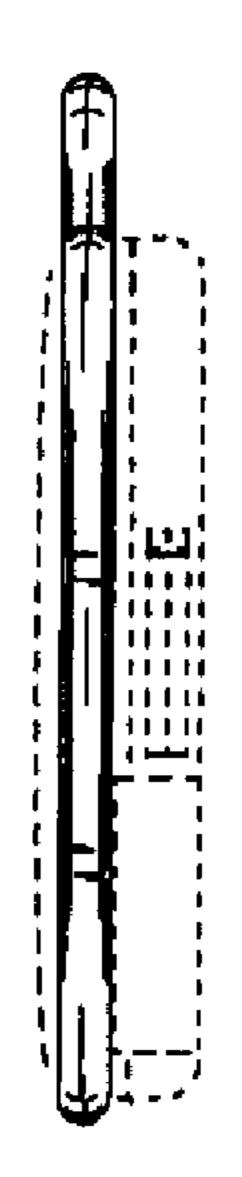


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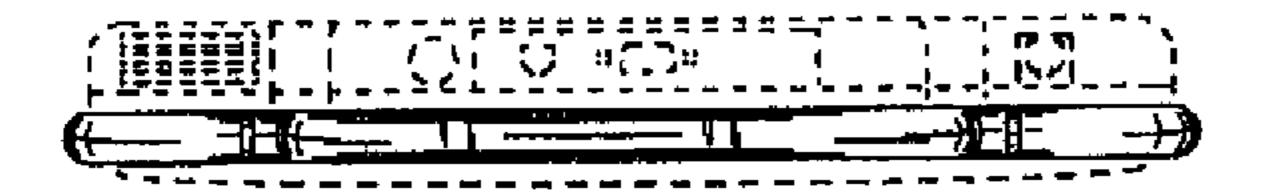


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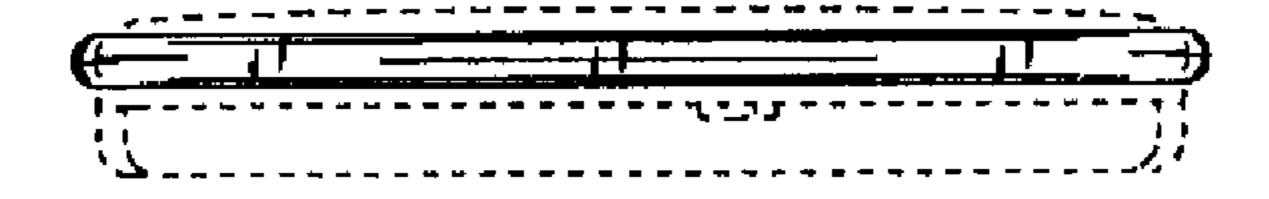


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