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(12) United States Design Patent

Tamatani et al.

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(54) **CAPACITOR**

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(**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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Dec. 18, 2019 (JP) D2019-028106
Dec. 18, 2019 (JP) D2019-028107
Dec. 18, 2019 (JP) D2019-028108

(51) **LOC (14) Cl.** **13-02**

(52) **U.S. Cl.**
USPC **D13/110**

(58) **Field of Classification Search**
USPC D13/101, 110, 117, 118, 120, 121, 125, D13/129, 133
CPC H01G 4/12; H01G 4/38; H01G 4/232; H01G 4/248; H01G 4/385; C23C 14/24; C23C 14/0641; H01L 21/2855; H01L 21/32051; H01L 23/552; H01L 23/3135
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a capacitor, as shown and described.

DESCRIPTION

FIG. **1** is a front view of a capacitor of the present design;
FIG. **2** is a rear view of the capacitor shown in FIG. **1**;
FIG. **3** is a right side view of the capacitor shown in FIG. **1**;
FIG. **4** is a left side view of the capacitor shown in FIG. **1**;
FIG. **5** is a top view of the capacitor shown in FIG. **1**;
FIG. **6** is a bottom view of the capacitor shown in FIG. **1**;
FIG. **7** is a first perspective view of the capacitor shown in FIG. **1**; and,
FIG. **8** is a second perspective view of the capacitor shown in FIG. **1**.

1 Claim, 4 Drawing Sheets

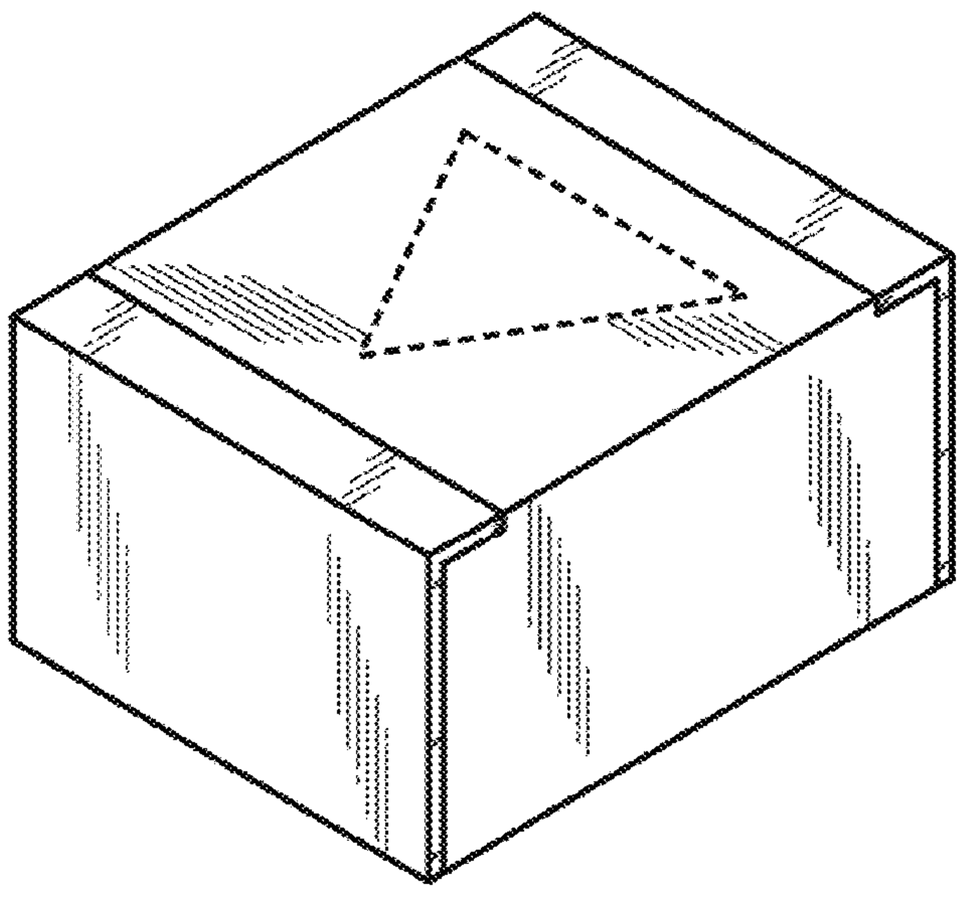

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FIG. 1

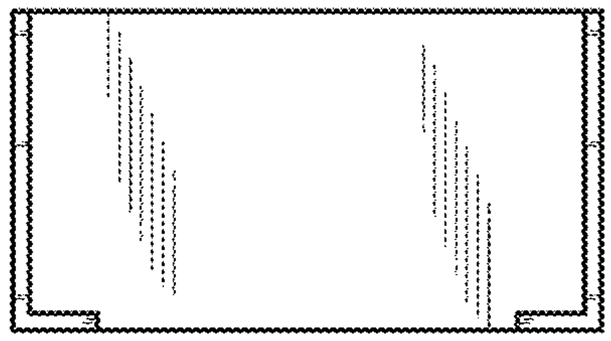

FIG. 2

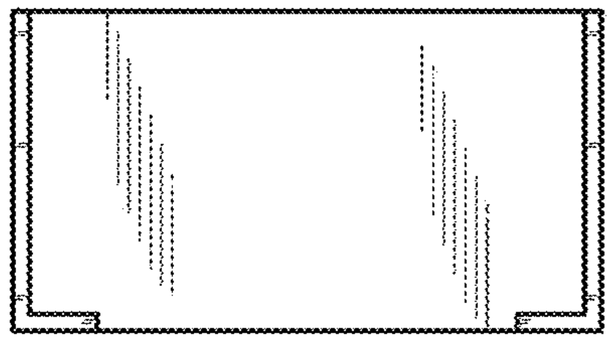

FIG. 3

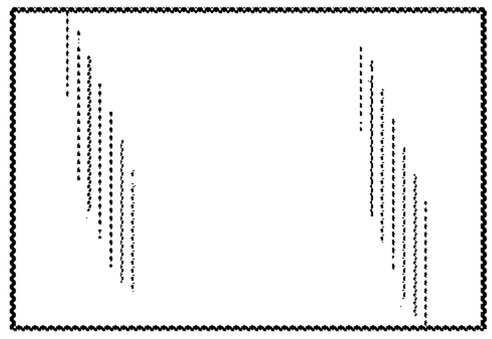

FIG. 4

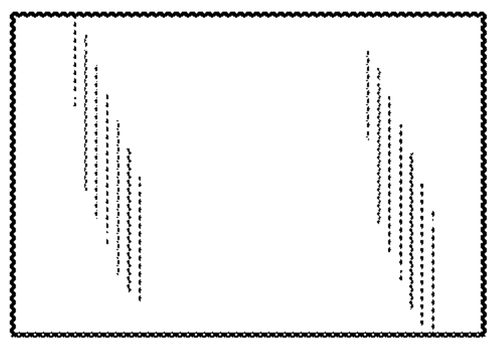

FIG. 5

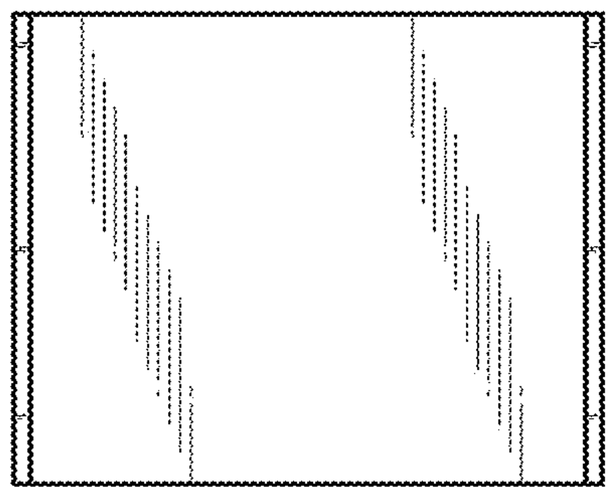

FIG. 6

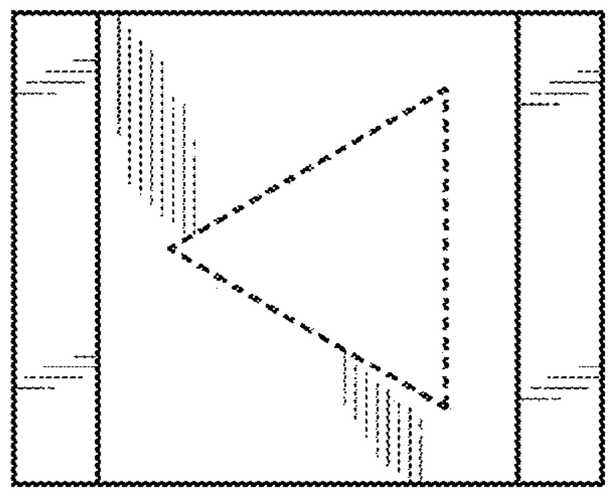

FIG. 7

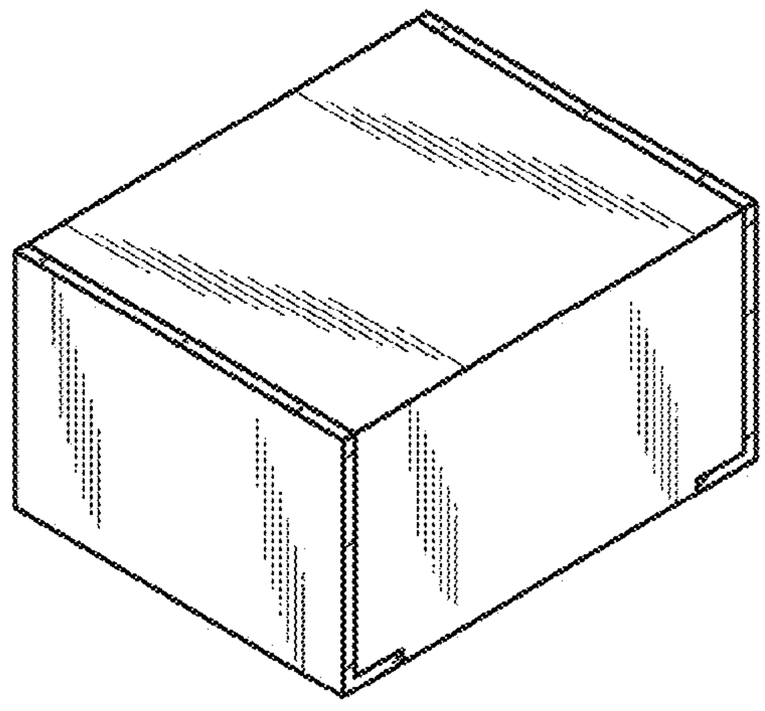

FIG. 8