



US00D992144S

(12) **United States Design Patent** (10) **Patent No.:** **US D992,144 S**
Takano et al. (45) **Date of Patent:** **** Jul. 11, 2023**

(54) **CONTROL ROOM**

(56) **References Cited**

(71) Applicant: **YOKOGAWA ELECTRIC CORPORATION**, Musashino (JP)

U.S. PATENT DOCUMENTS

(72) Inventors: **Naoto Takano**, Musashino (JP);
Yasuyuki Omi, Musashino (JP)

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(73) Assignee: **YOKOGAWA ELECTRIC CORPORATION**, Musashino (JP)

(**) Term: **15 Years**

(21) Appl. No.: **29/844,679**

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JP	2020-006315	11/2020

Related U.S. Application Data

(62) Division of application No. 35/511,630, filed on Sep. 29, 2020 (U.S. filing date under 35 U.S.C. 384), and having an international filing date of Sep. 29, 2020, now Pat. No. Des. 965,177.

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Primary Examiner — Khawaja Anwar

(74) *Attorney, Agent, or Firm* — Saidman DesignLaw Group, LLC

(30) **Foreign Application Priority Data**

Apr. 1, 2020 (JP) 2020-006316 D

(51) **LOC (14) Cl.** **25-03**

(52) **U.S. Cl.**
USPC **D25/1**

(58) **Field of Classification Search**
USPC D25/1-37
CPC ... A47B 83/001; A47B 21/06; A47B 2220/00;
A47B 2230/00; A47B 57/425; A47B
96/04; E04B 2/7433; E04F 11/00; H02G
3/288; E04C 2/52

See application file for complete search history.

(57) **CLAIM**

The ornamental design for a control room, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a control room showing our new design;

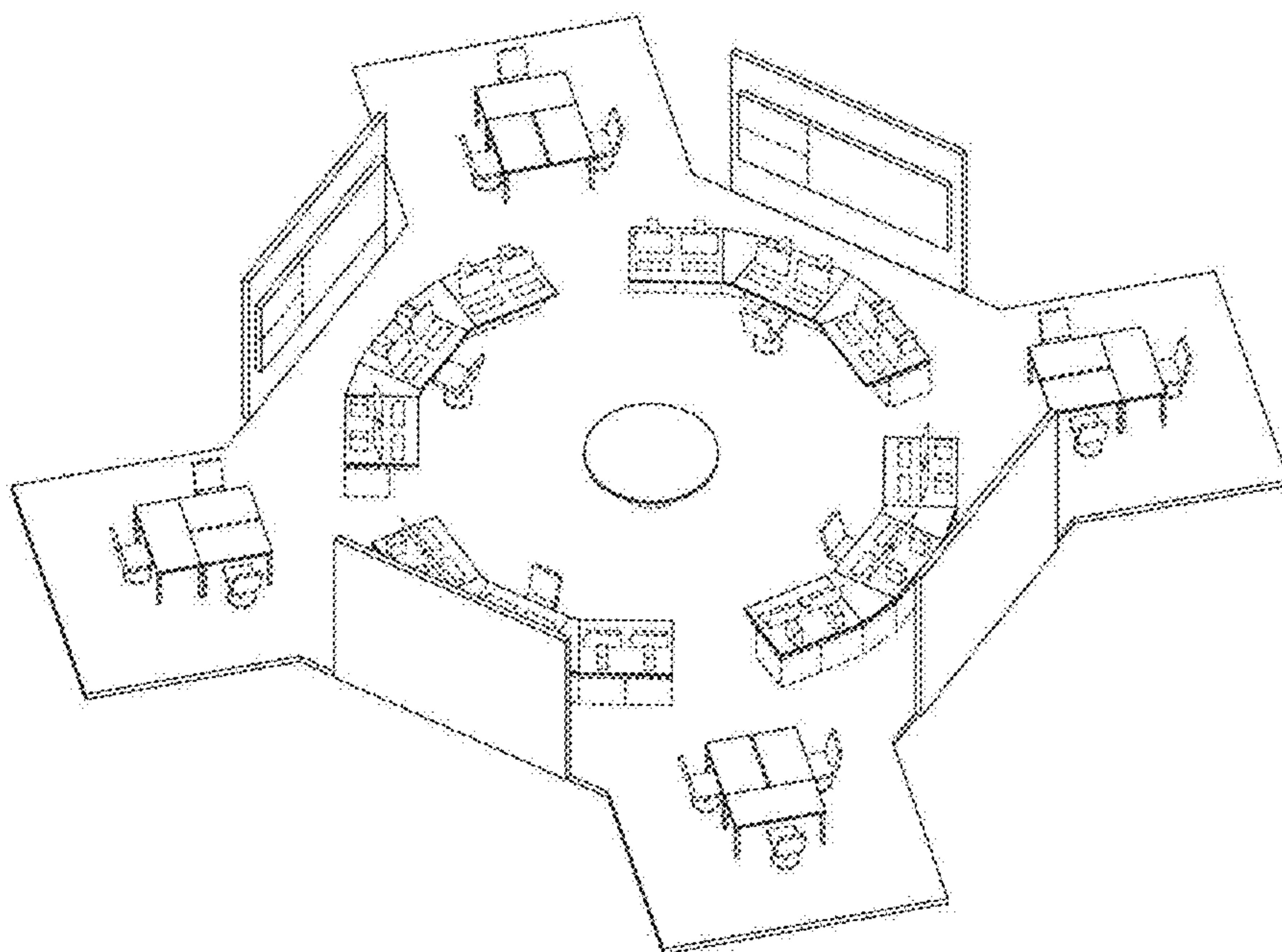
FIG. 2 is a top view thereof;

FIG. 3 is a cross-sectional view thereof, taken along lines 3-3 of FIG. 2; and,

FIG. 4 is a front view thereof.

The broken lines depict portions of the control room which form no part of the claimed design.

1 Claim, 4 Drawing Sheets



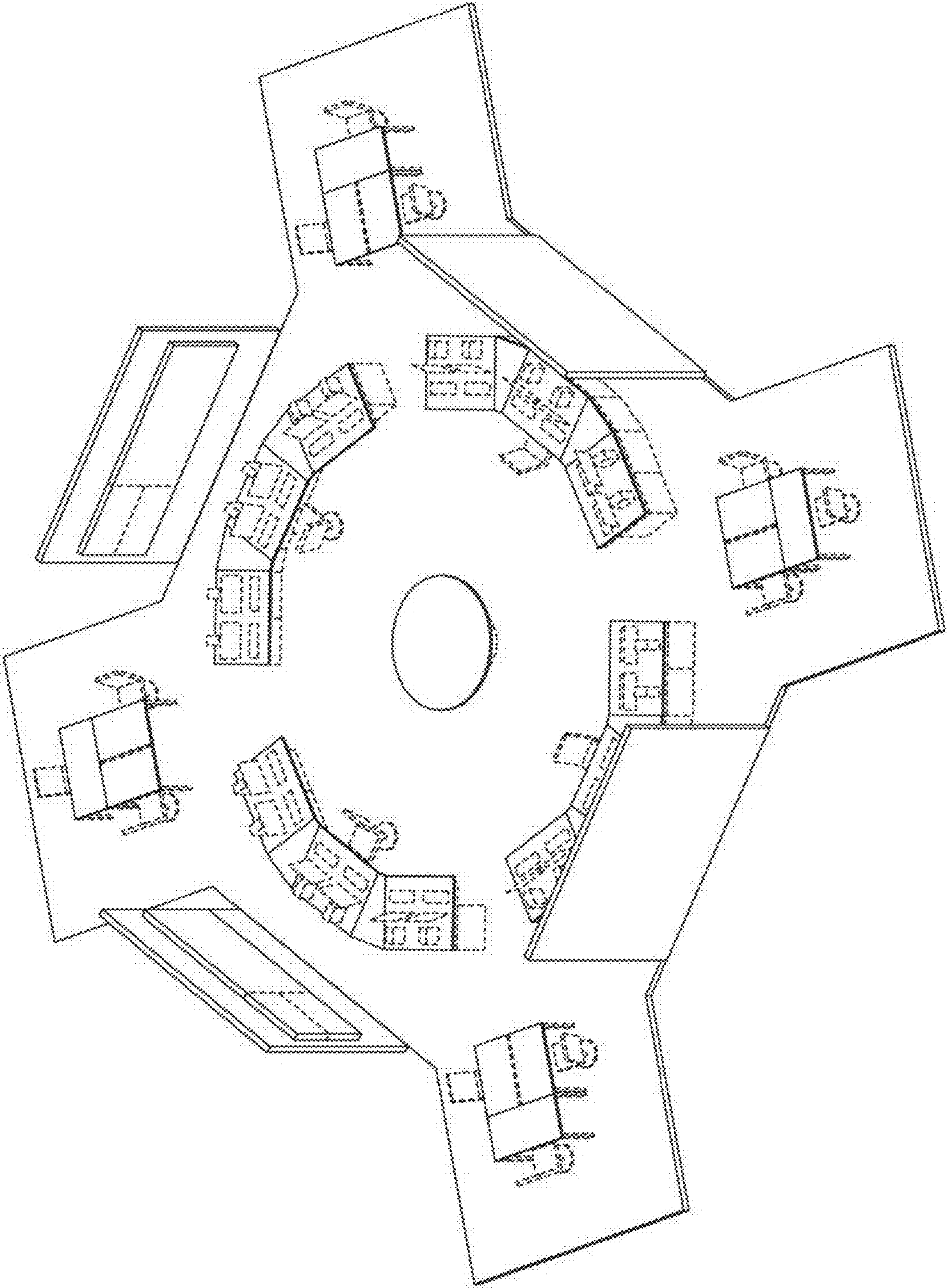


FIG. 1

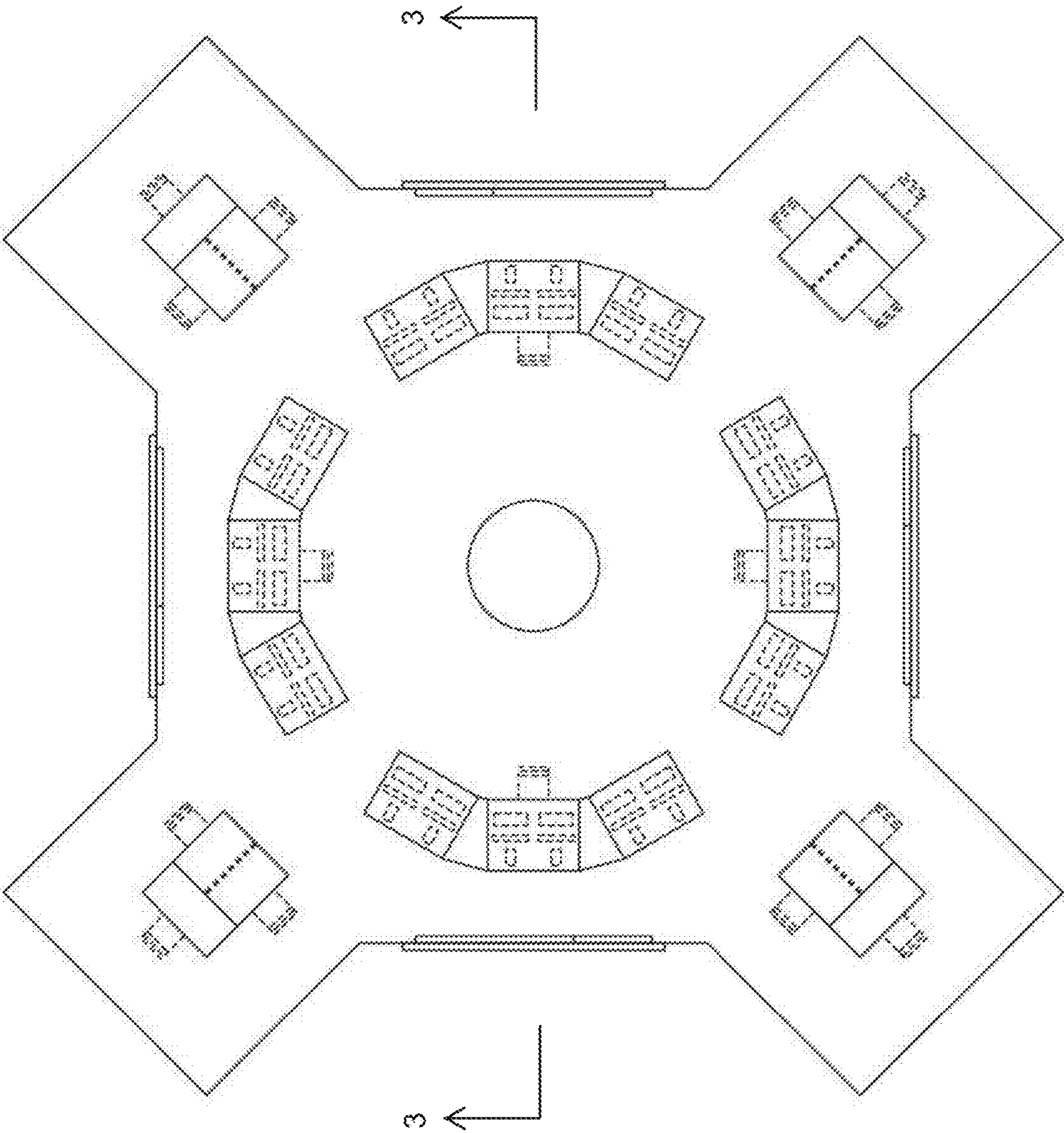


FIG. 2

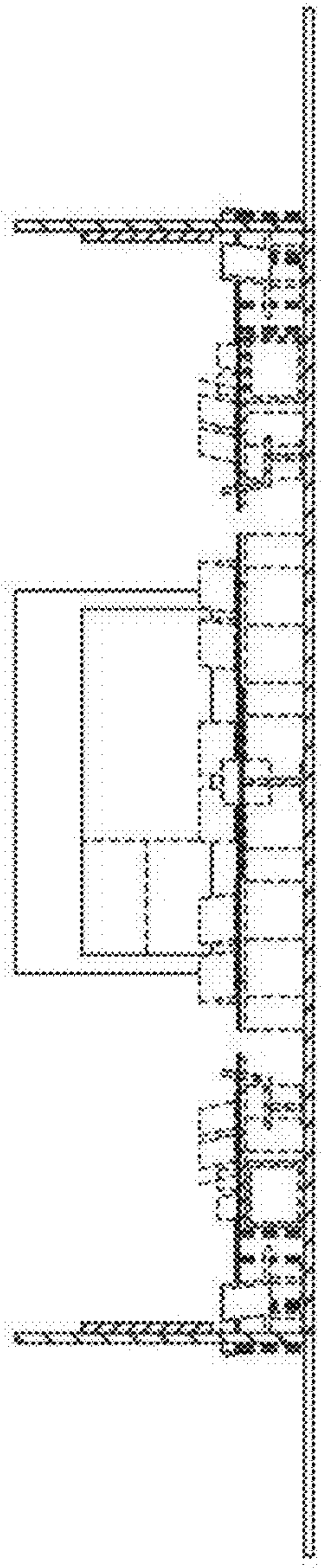


FIG. 3

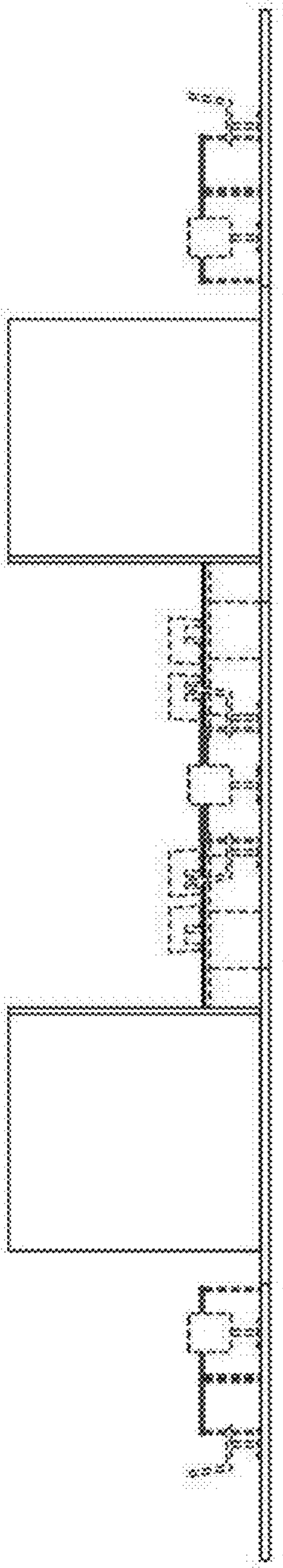


FIG. 4