



US00D989713S

(12) **United States Design Patent**  
**Horino et al.**

(10) **Patent No.:** **US D989,713 S**  
(45) **Date of Patent:** **\*\* Jun. 20, 2023**

- (54) **ELECTRICAL CONNECTOR**
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**Masayoshi Kakino**, Yokohama (JP)
- (73) Assignee: **KYOCERA CORPORATION**, Kyoto (JP)
- (\*\*) Term: **15 Years**
- (21) Appl. No.: **29/768,446**
- (22) Filed: **Jan. 29, 2021**
- (30) **Foreign Application Priority Data**

Oct. 6, 2020 (JP) ..... 2020-021423 D

- (51) **LOC (14) Cl.** ..... **13-03**
- (52) **U.S. Cl.**  
USPC ..... **D13/147; D13/133**
- (58) **Field of Classification Search**  
USPC ..... D13/133, 146-147, 149, 154, 184, 199  
CPC ..... H01R 12/52; H01R 12/707; H01R 12/716  
See application file for complete search history.

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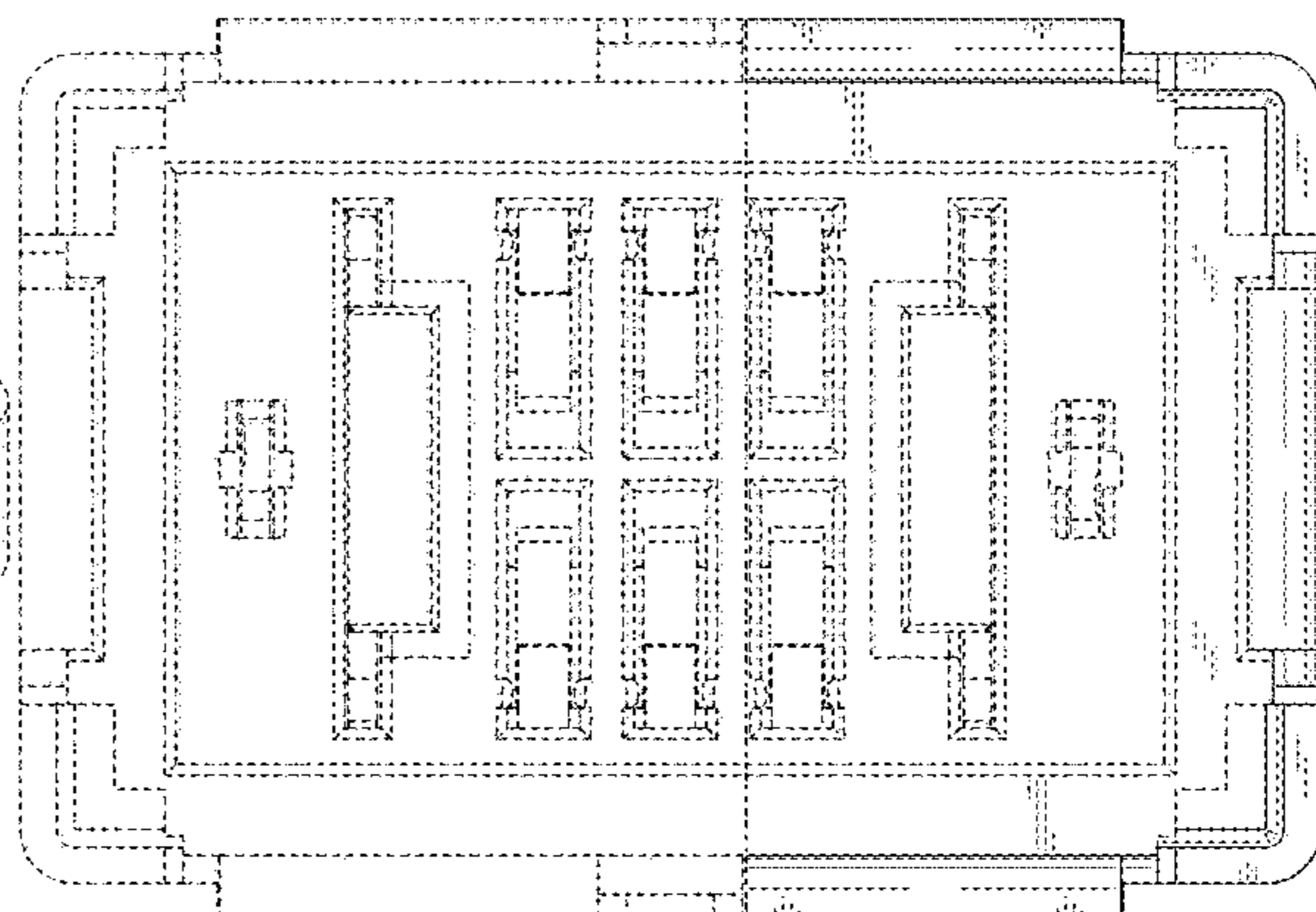
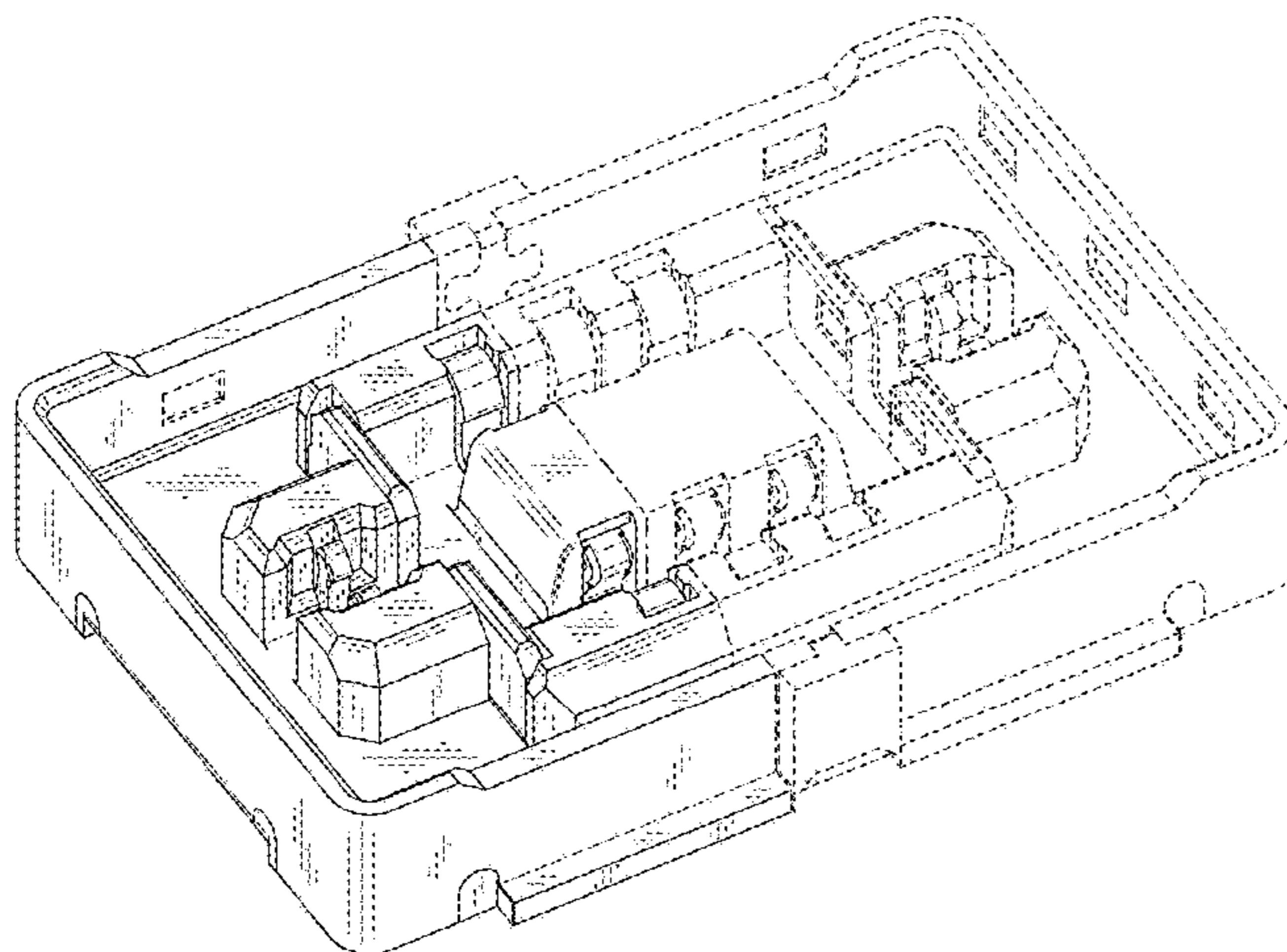
(57) **CLAIM**

The ornamental design for an electrical connector, as shown and described.

**DESCRIPTION**

FIG. 1 is a front, bottom and left-side perspective view of an electrical connector showing our new design; FIG. 2 is a front, top and left-side perspective view thereof; FIG. 3 is a front, top and right-side perspective view thereof; FIG. 4 is a front elevational view thereof; FIG. 5 is rear elevational view thereof; FIG. 6 is a left-side elevational view thereof; FIG. 7 is a right-side elevational view thereof; FIG. 8 is a top plan view thereof; and, FIG. 9 is a bottom plan view thereof. The broken lines depict portions of the electrical connector that form no part of the claimed design. The dot-dash boundary lines define the boundary of the claimed design and form no part of the claimed design.

**1 Claim, 7 Drawing Sheets**



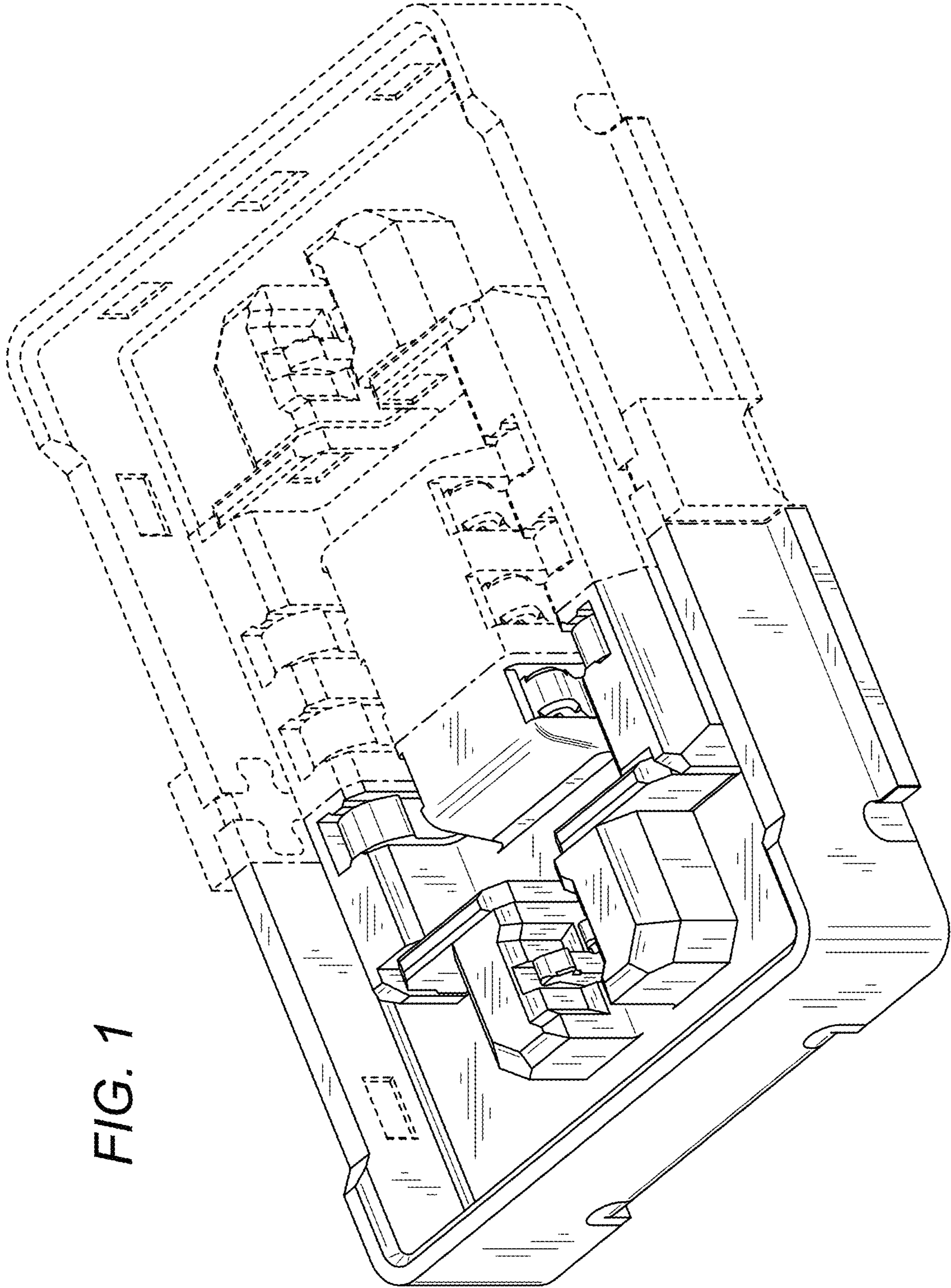
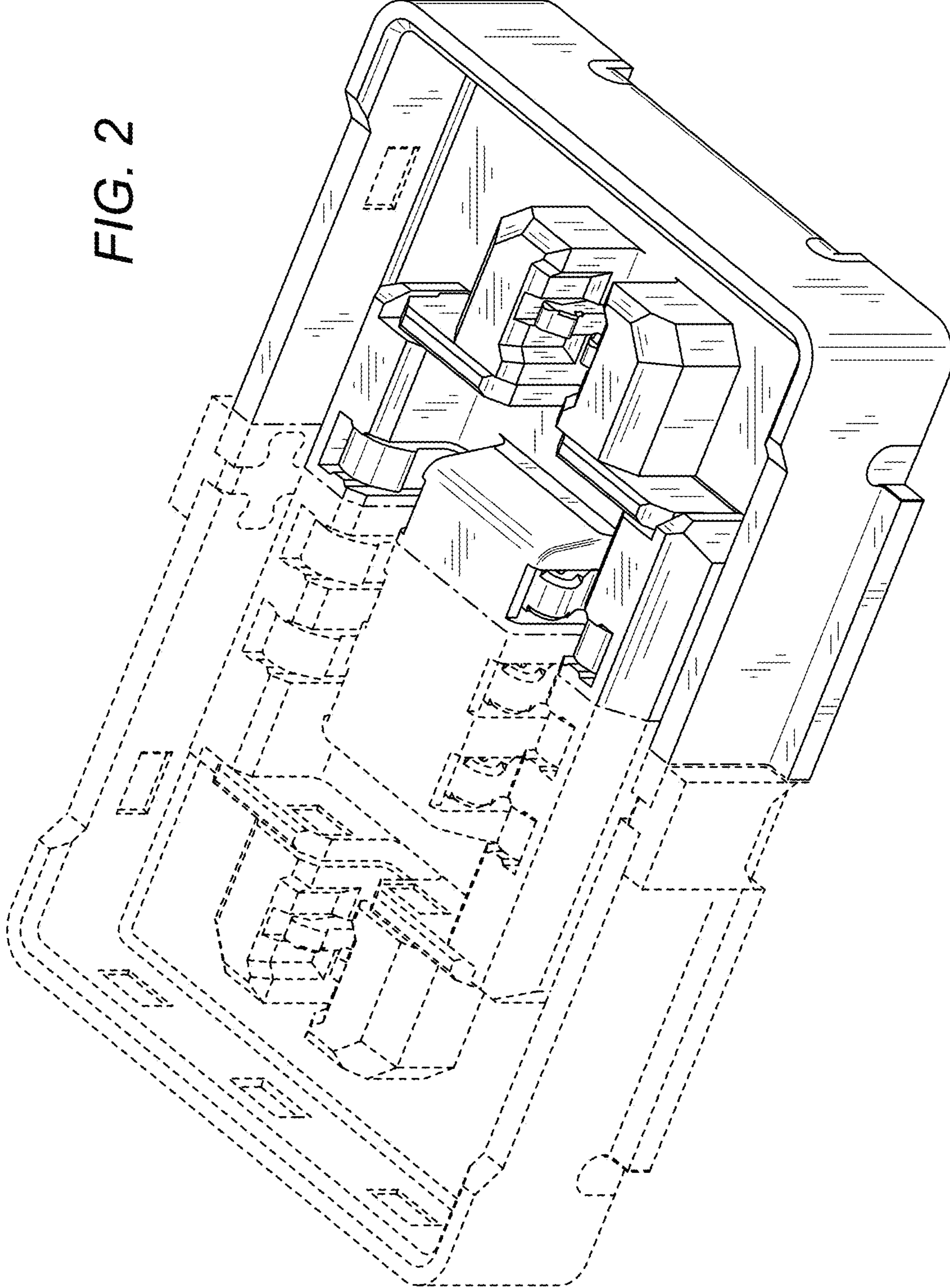


FIG. 1



FIG. 2



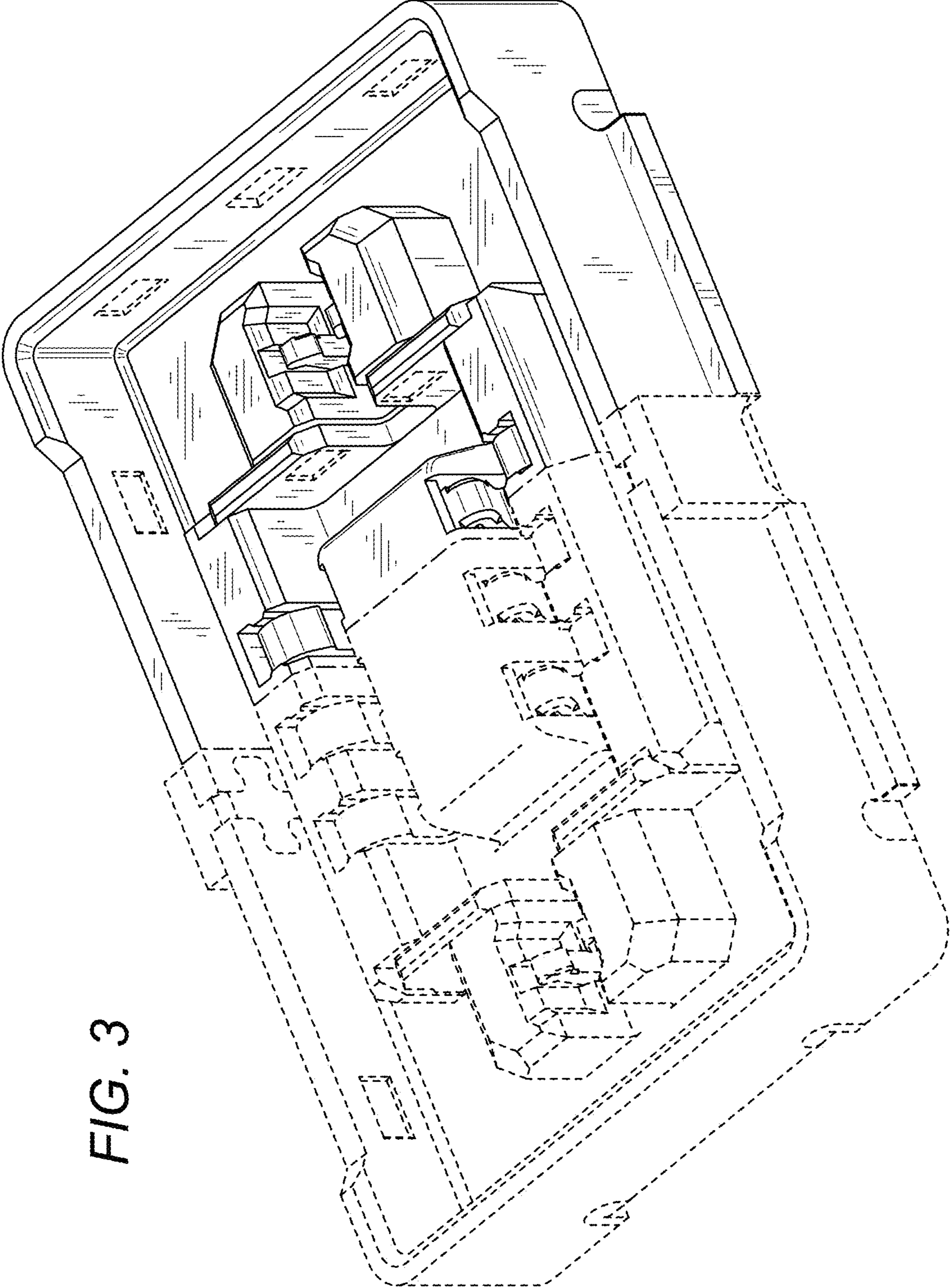


FIG. 3

FIG. 4

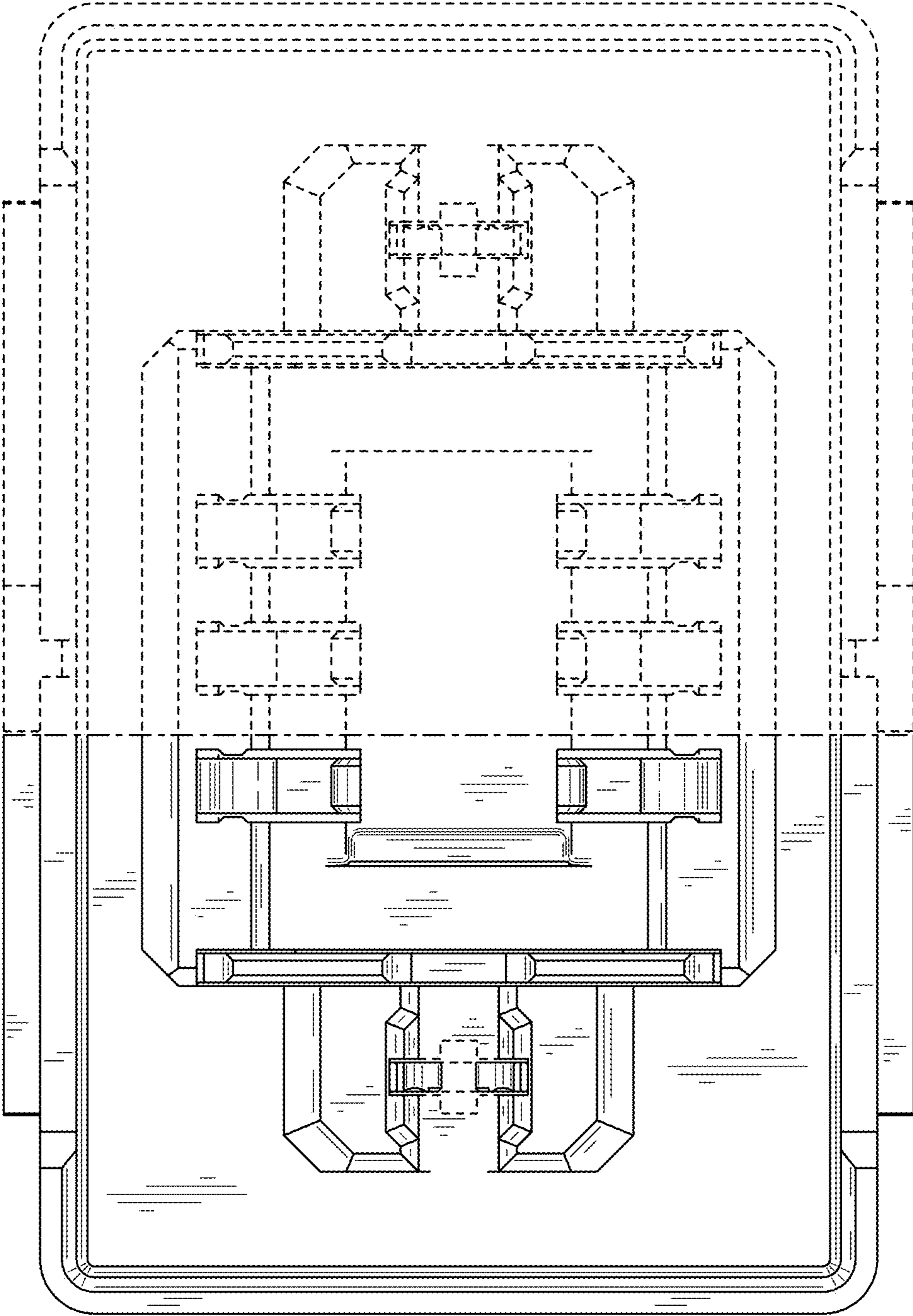
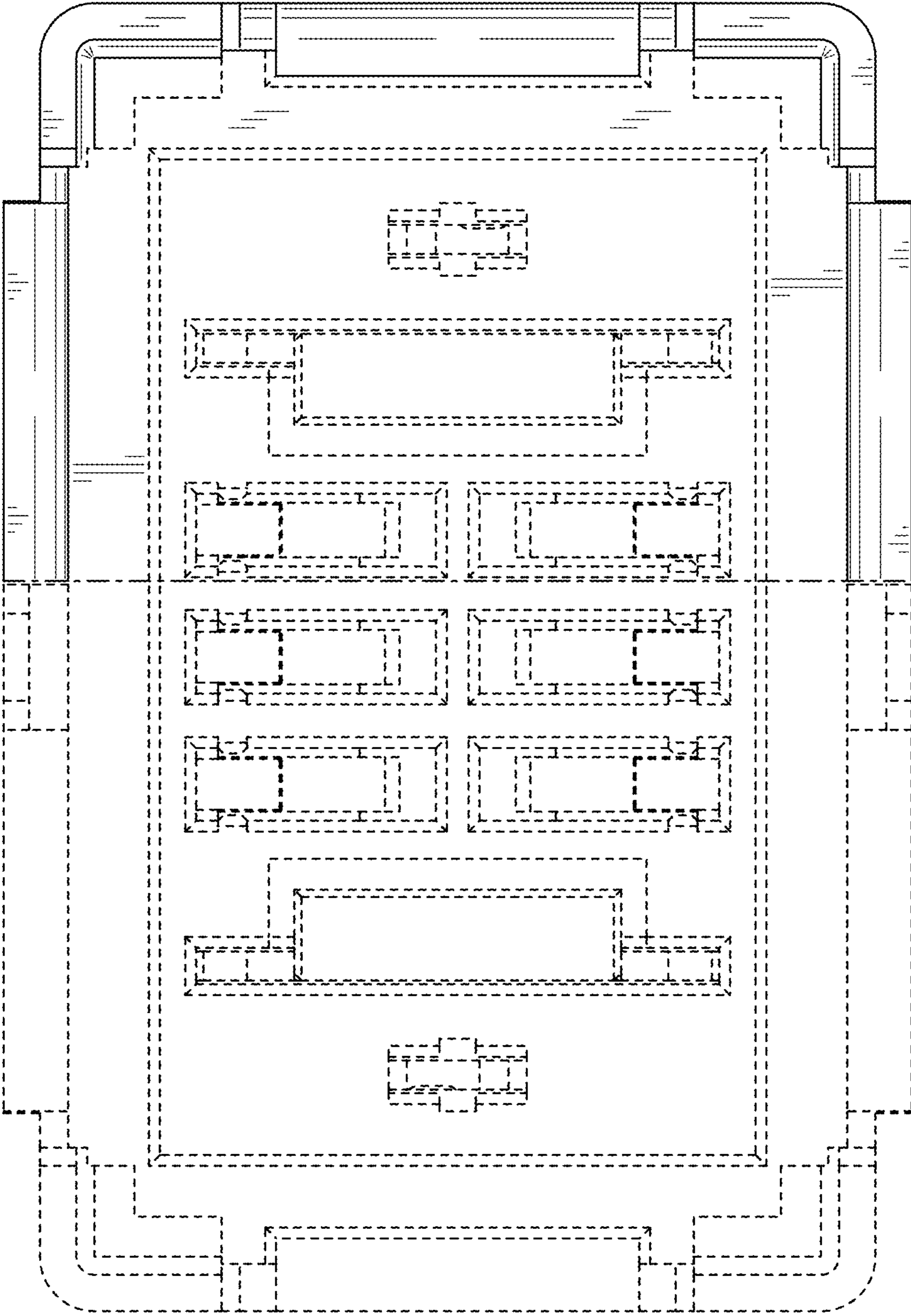
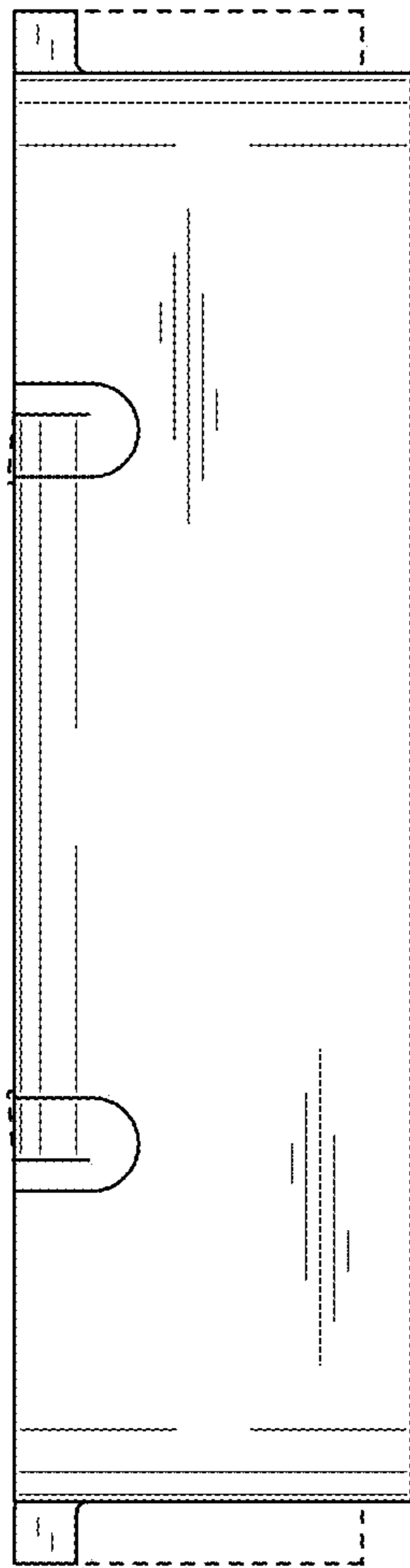




FIG. 5



*FIG. 6*



*FIG. 7*

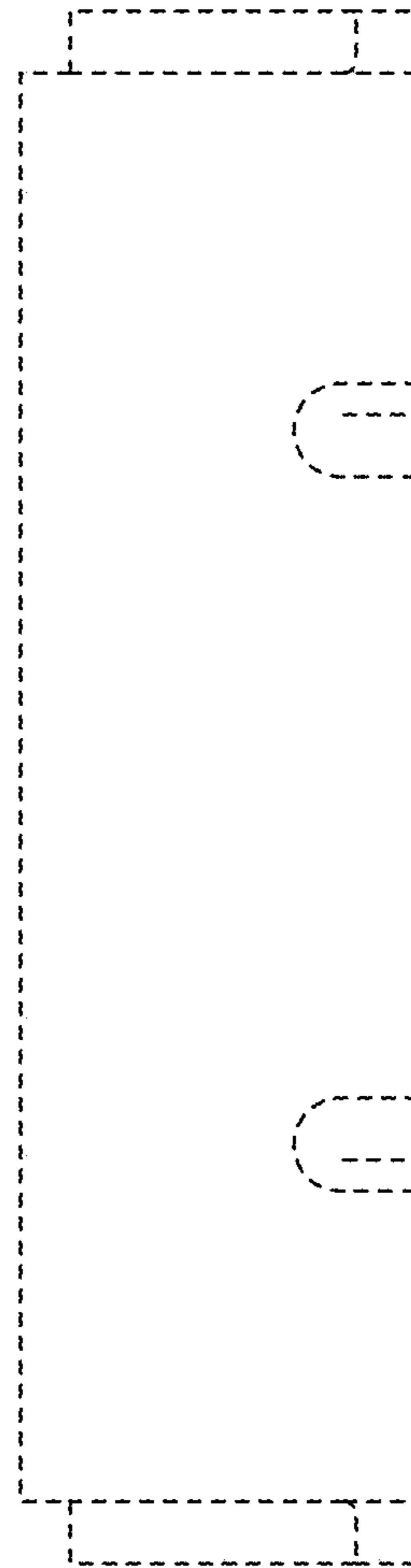


FIG. 8

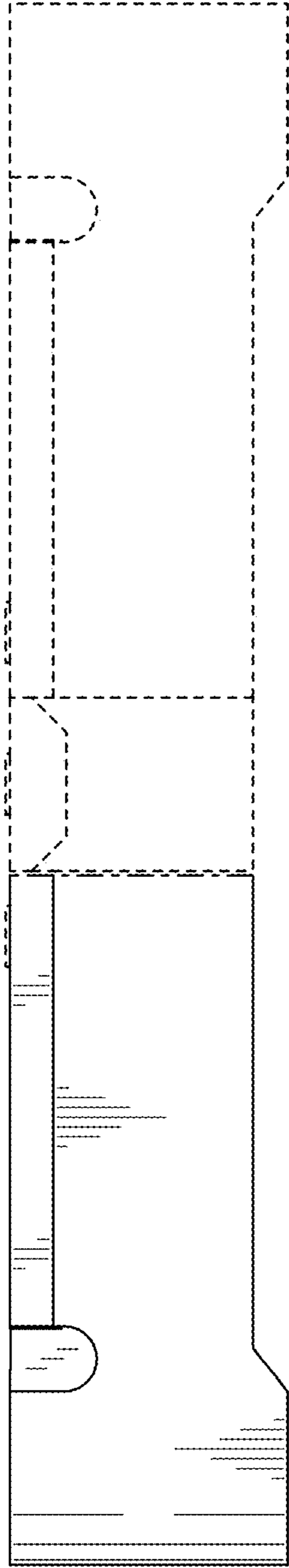


FIG. 9

