



US00D986192S

(12) **United States Design Patent** (10) **Patent No.:** **US D986,192 S**
Wen (45) **Date of Patent:** **** May 16, 2023**

(54) **CIRCUIT BOARD**

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CN 307135974 * 3/2022
CN 307775983 * 1/2023
KR 301199352.0000 * 1/2023

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(**) Term: **15 Years**

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(21) Appl. No.: **29/882,236**

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(22) Filed: **Jan. 12, 2023**

(Continued)

(51) **LOC (14) Cl.** **13-03**

(52) **U.S. Cl.**

USPC **D13/182**

(58) **Field of Classification Search**

USPC D14/125–135, 239, 336, 358, 371,
D14/374–377, 448; D20/10, 18, 19, 21
CPC H04N 7/00; H04N 9/3141; H04N 13/0459;
H04N 17/04; H04N 17/004; H04N
21/43637; G06F 1/1601; G06F 1/1605;
G06F 1/162

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See application file for complete search history.

(57) **CLAIM**

The ornamental design for a circuit board, as shown and described.

(56) **References Cited**

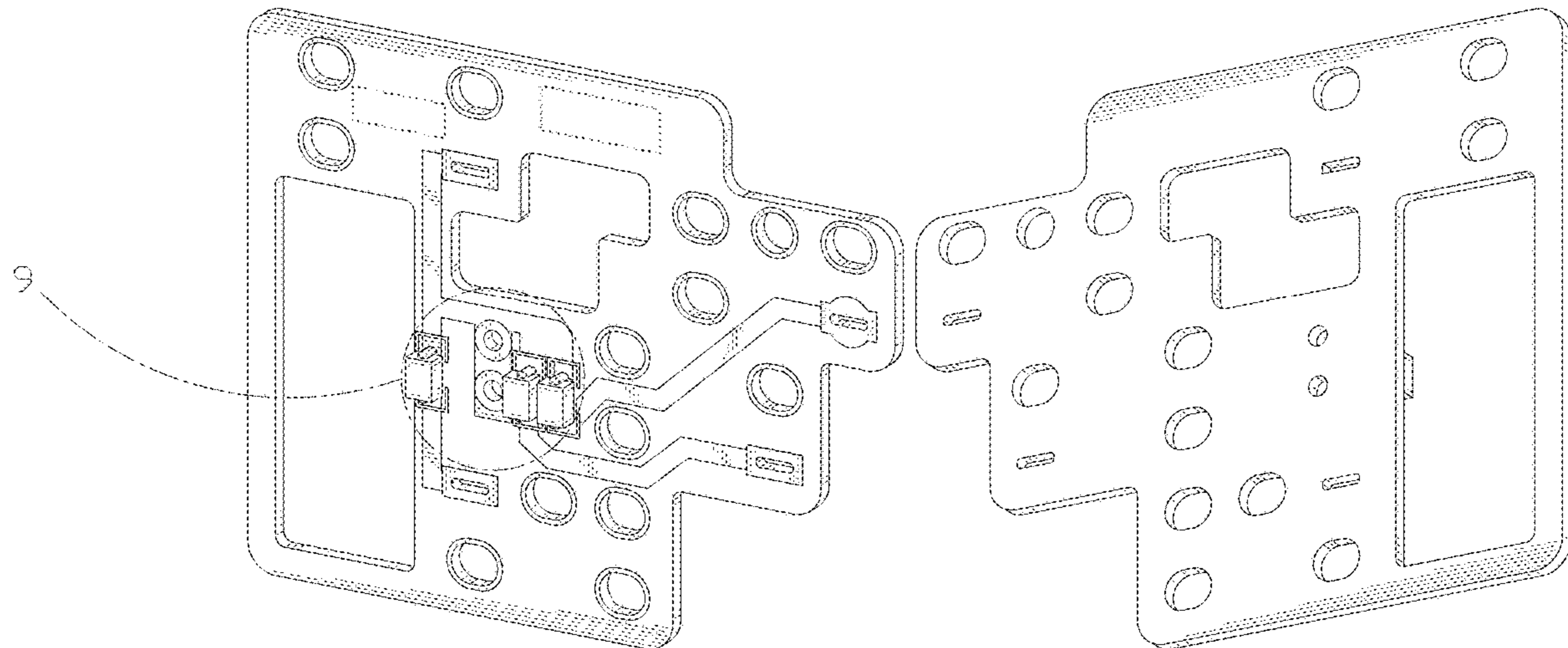
DESCRIPTION

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FIG. 1 is a perspective view of a circuit board showing my new design;
FIG. 2 is another perspective view thereof;
FIG. 3 is a front elevational view thereof;
FIG. 4 is a rear elevational view thereof;
FIG. 5 is a left side elevational view thereof;
FIG. 6 is a right side elevational view thereof;
FIG. 7 is a top plan view thereof;
FIG. 8 is a bottom plan view thereof; and,
FIG. 9 is an enlarged view of portion 9 shown in FIG. 1.
The dash-dash broken lines in the drawings depict portions of the circuit board that form no part of the claimed design. The dot-dash broken lines represent boundaries of the enlarged portions and form no part of the claimed design.

1 Claim, 9 Drawing Sheets



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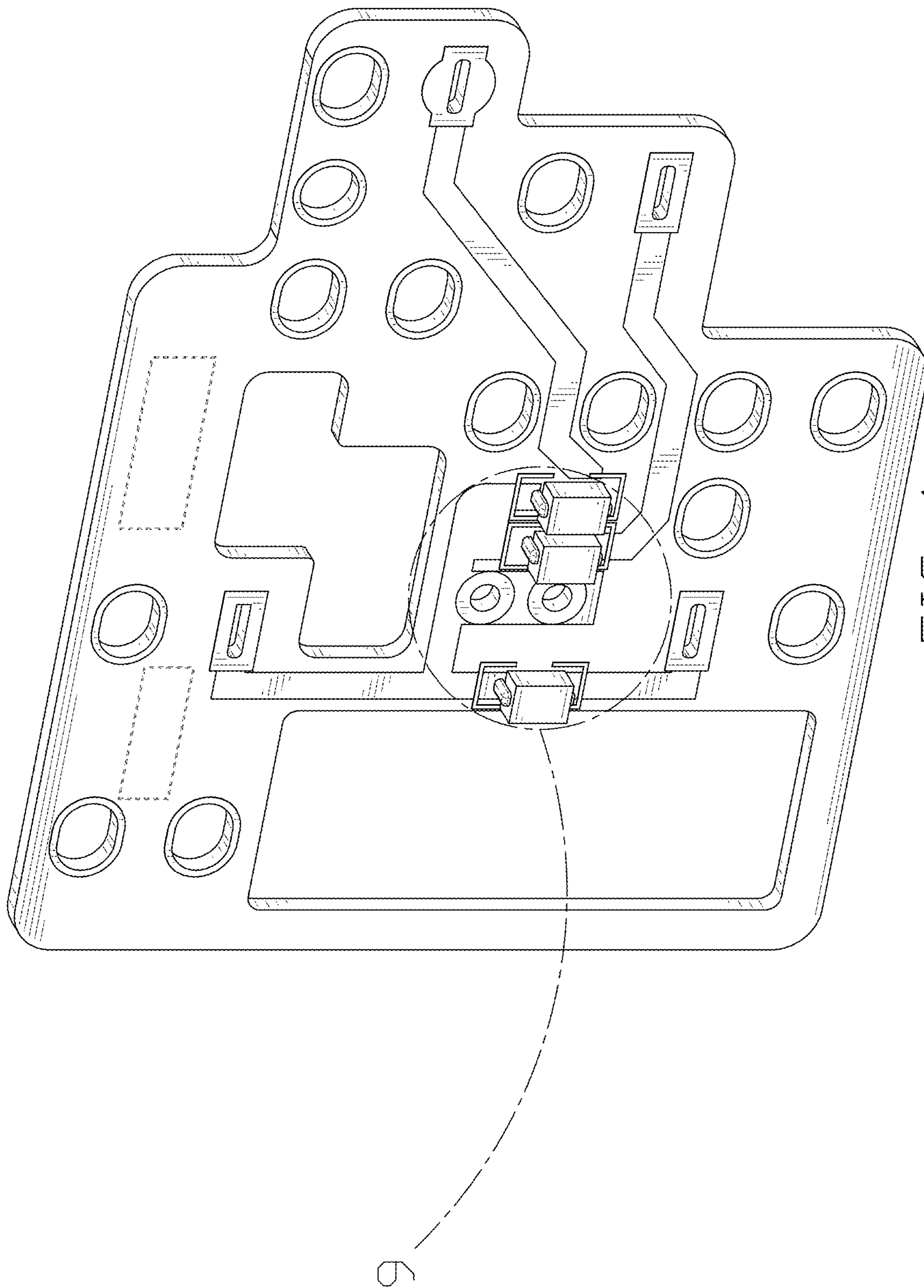


FIG. 1

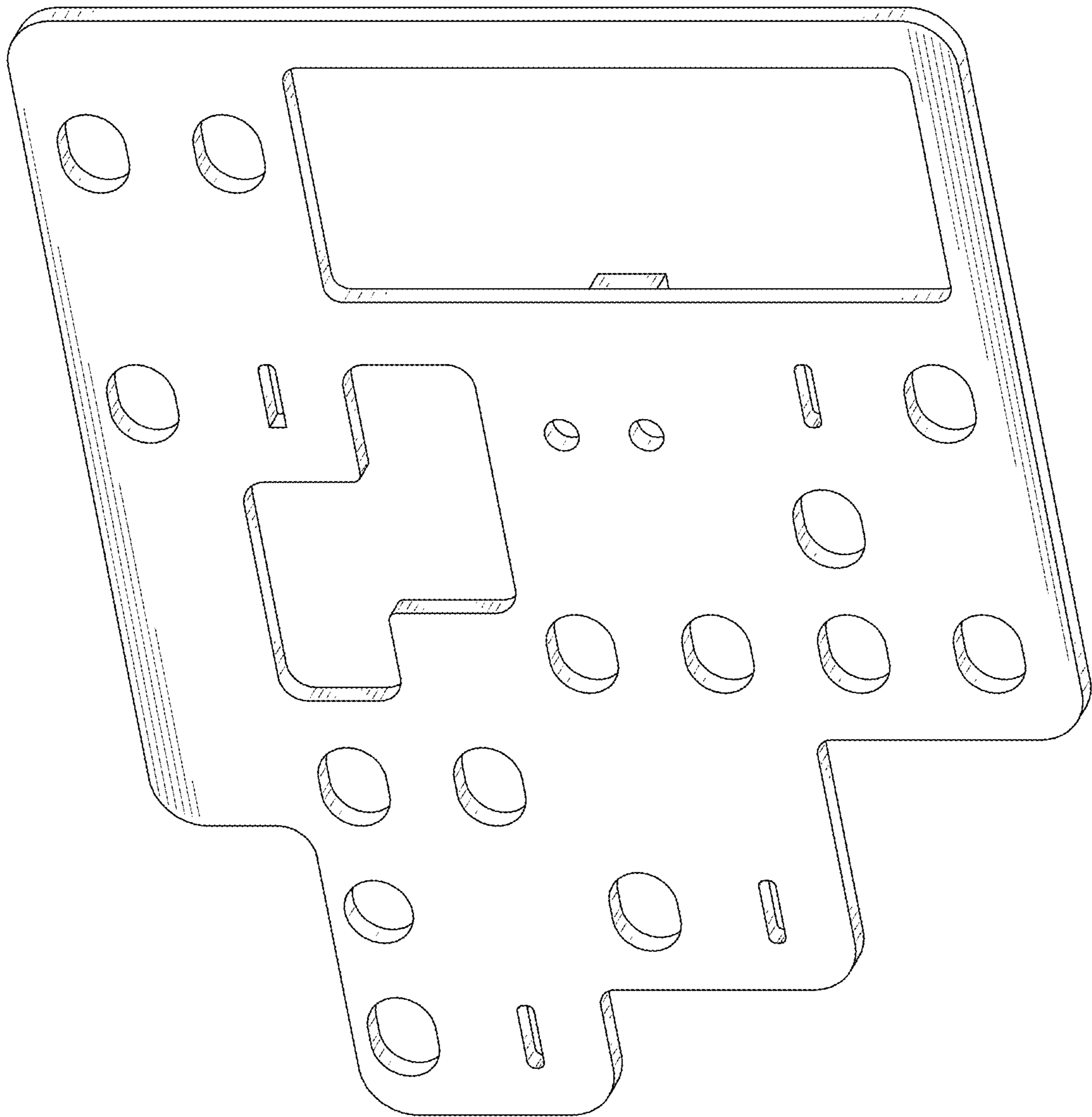


FIG. 2

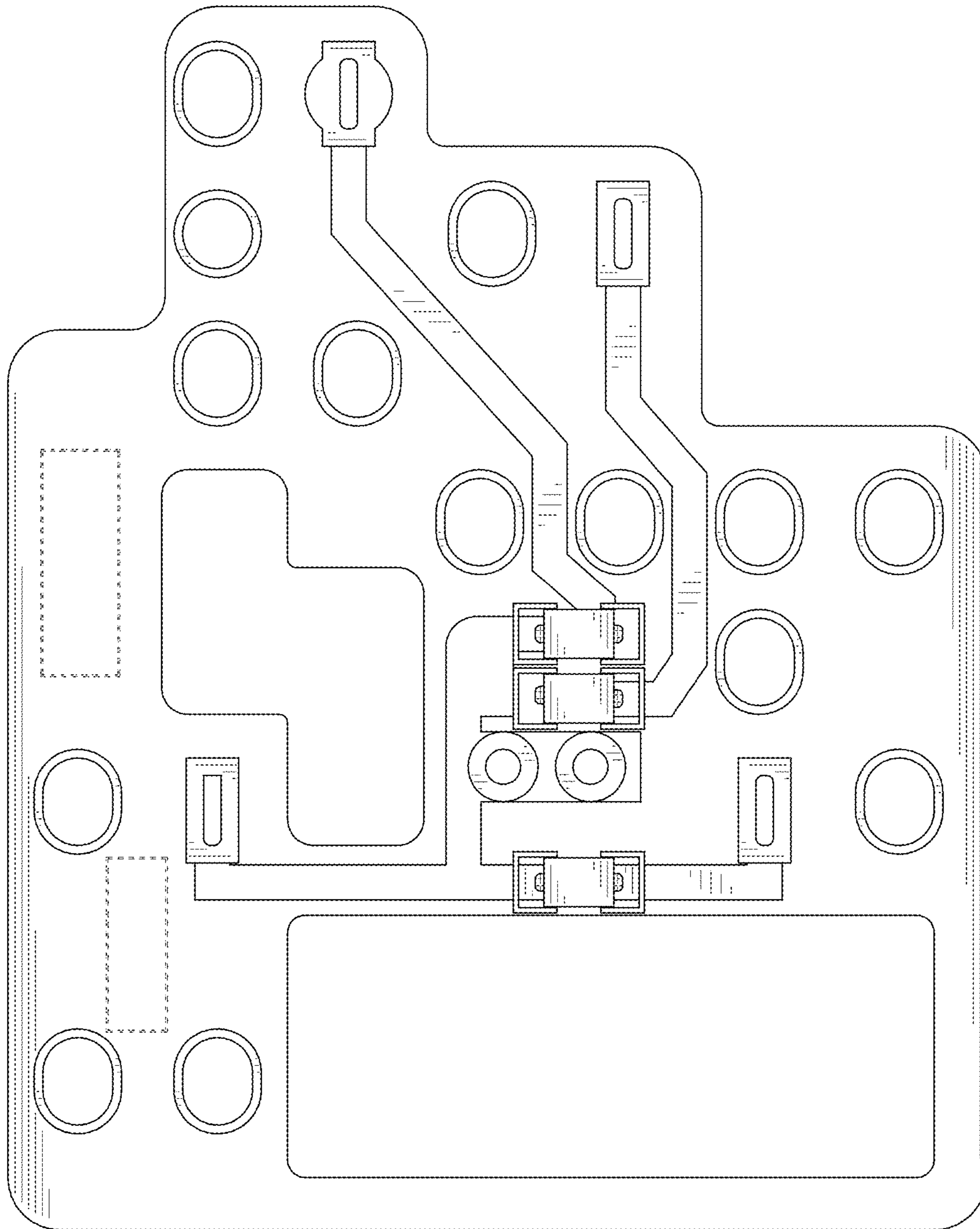


FIG. 3

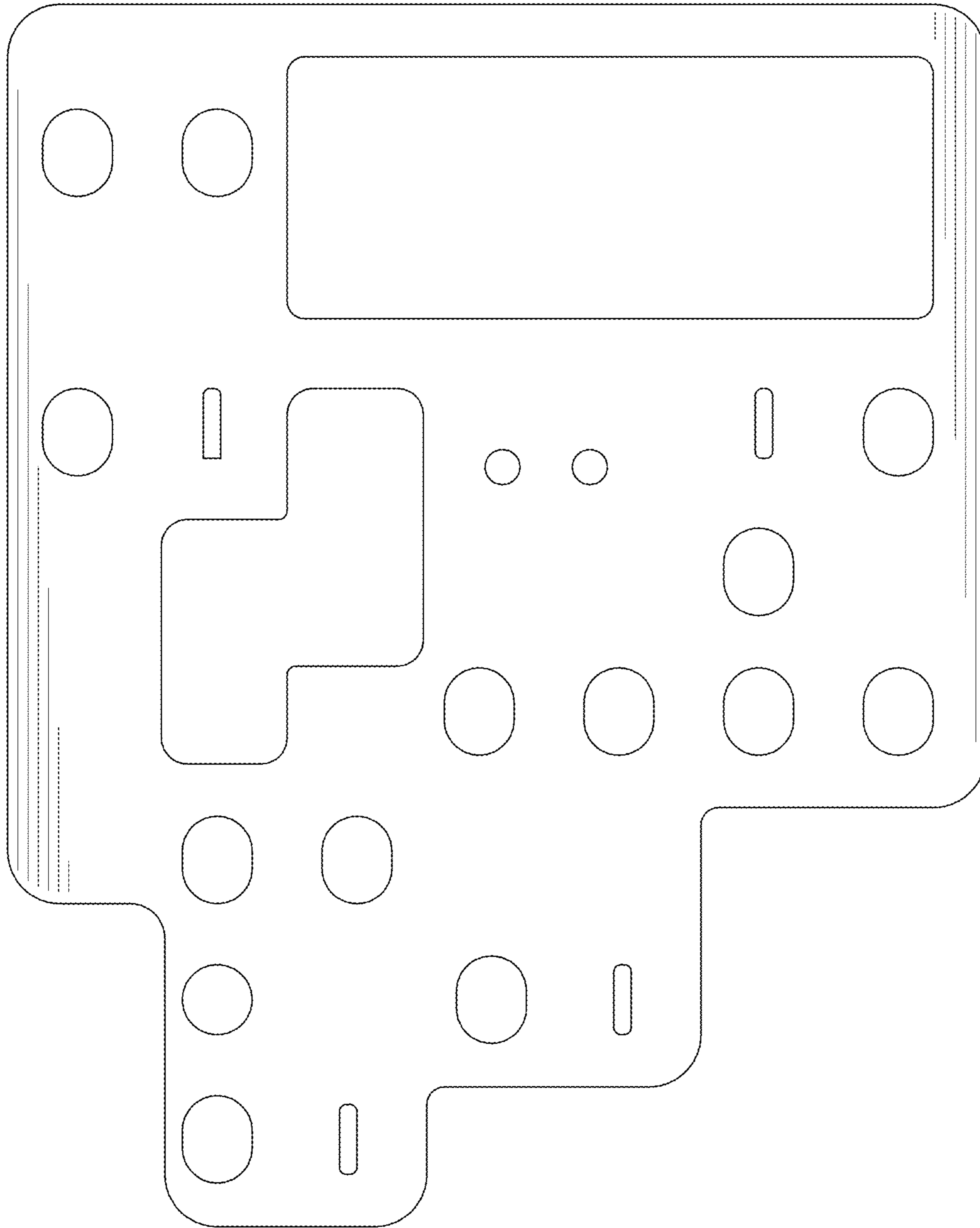


FIG. 4



FIG. 5

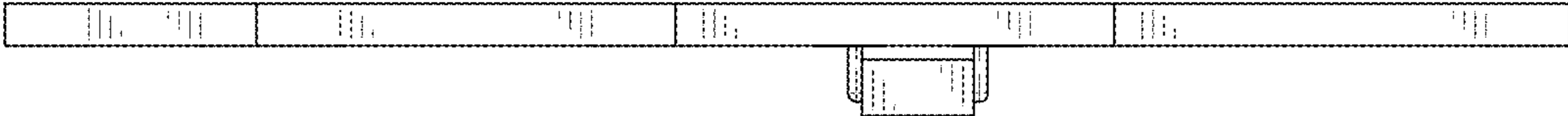


FIG. 6

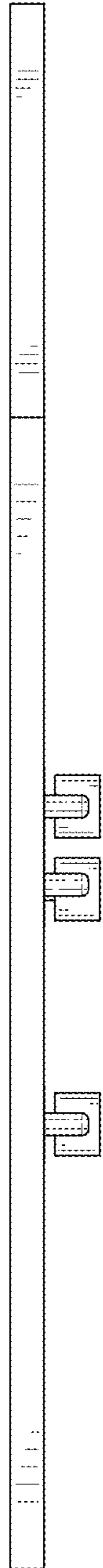


FIG. 7

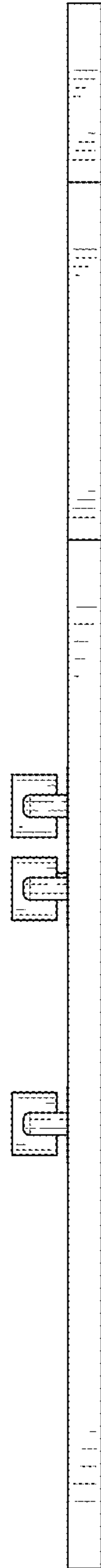


FIG. 8

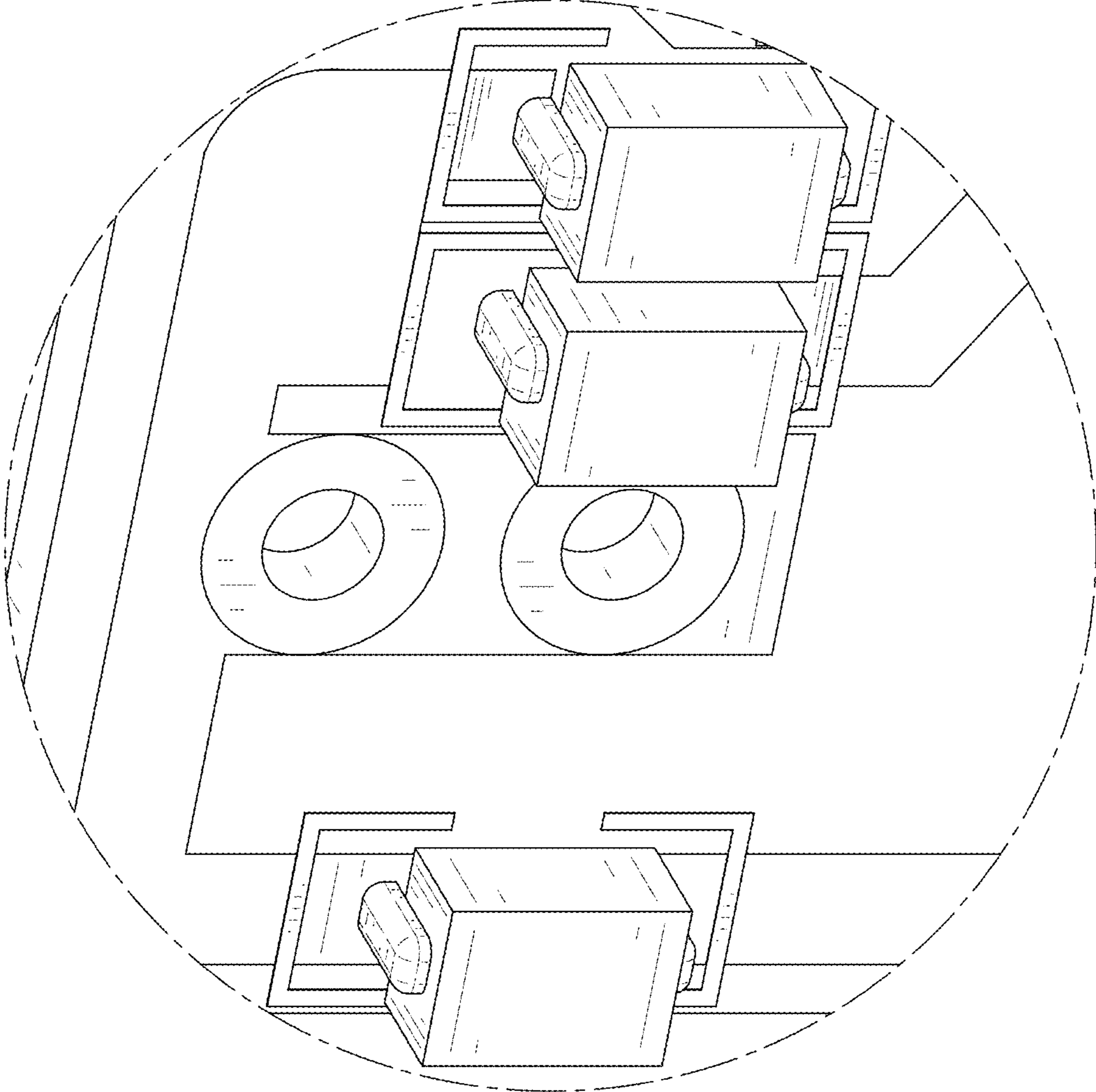


FIG. 9