



US00D985548S

(12) **United States Design Patent** (10) **Patent No.:** **US D985,548 S**
Koons et al. (45) **Date of Patent:** **** May 9, 2023**

(54) **MOUNT FOR ELECTRONIC DEVICES**

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(**) Term: **15 Years**

(21) Appl. No.: **29/813,822**

(22) Filed: **Nov. 1, 2021**

(51) **LOC (14) Cl.** **14-99**

(52) **U.S. Cl.**
USPC **D14/238.1**; D6/553; D14/251

(58) **Field of Classification Search**
USPC D14/251–253, 224, 224.1, 447, 451, 452, D14/238, 238.1; D12/114, 415, 426.1, D12/223; D3/218; D13/108; D6/553, D6/567

CPC . B60R 11/00; B60R 11/0241; B60R 11/0252; B60R 2011/0003; B60R 2011/0005; B60R 2011/0008; H04M 1/04; A45C 2011/002; A45C 2011/003; A45F 2200/0516; A45F 2200/0525

See application file for complete search history.

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Primary Examiner — Rebekah A Caruso

(57) **CLAIM**

The ornamental design for a mount for electronic devices, as shown and described.

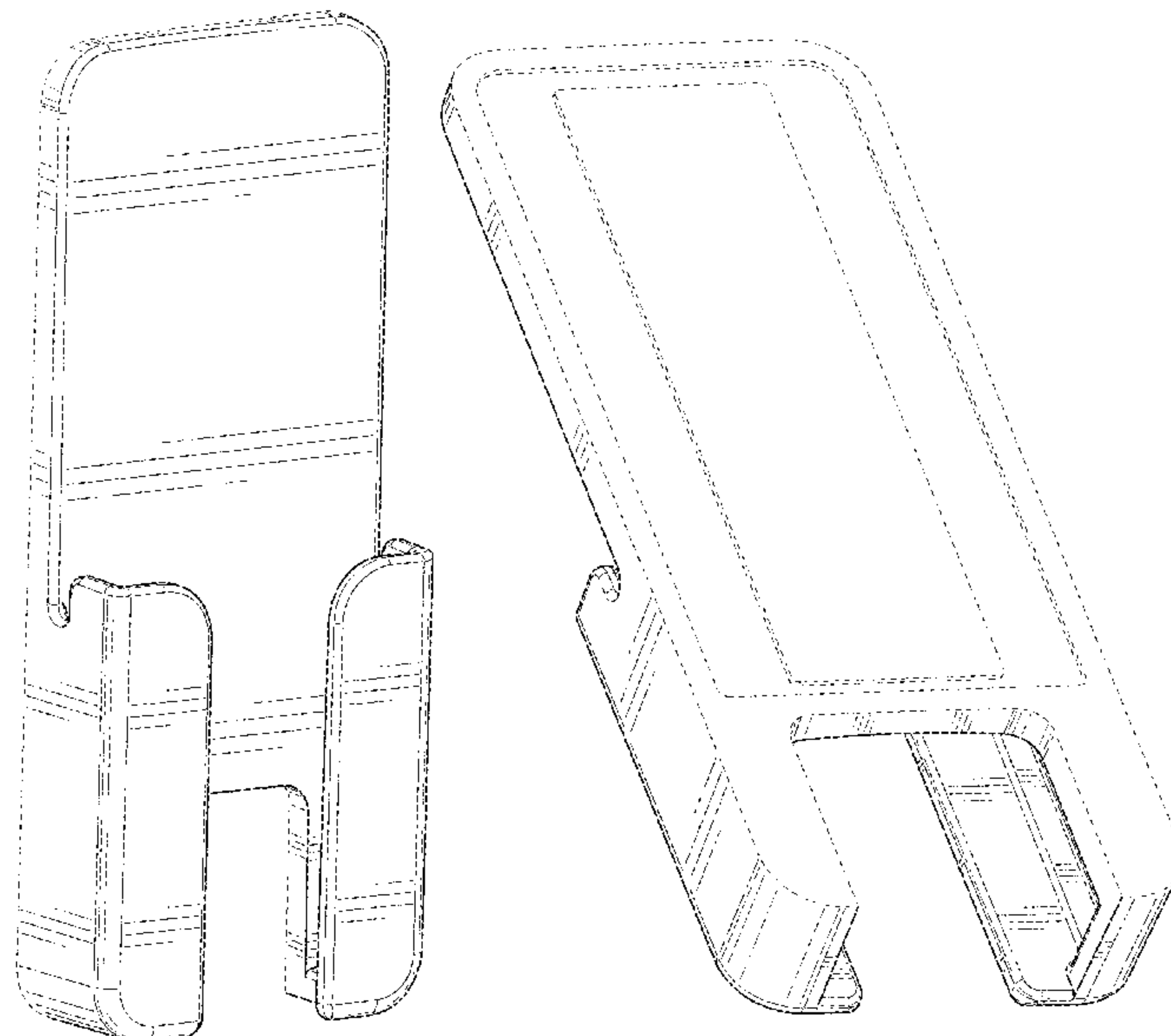
DESCRIPTION

FIG. 1 is a front, first side, and top perspective view of a mount for electronic devices embodying the new design; FIG. 2 is a back, second side, and bottom perspective view thereof;

FIG. 3 is a front view thereof; FIG. 4 is a back view thereof; FIG. 5 is a first side view thereof; FIG. 6 is a second side view thereof; FIG. 7 is a bottom view thereof; and, FIG. 8 is a top view thereof.

The uniform repeating broken lines depict portions of a mount for electronic devices in which the claimed design is embodied and form no part of the claimed design.

1 Claim, 8 Drawing Sheets



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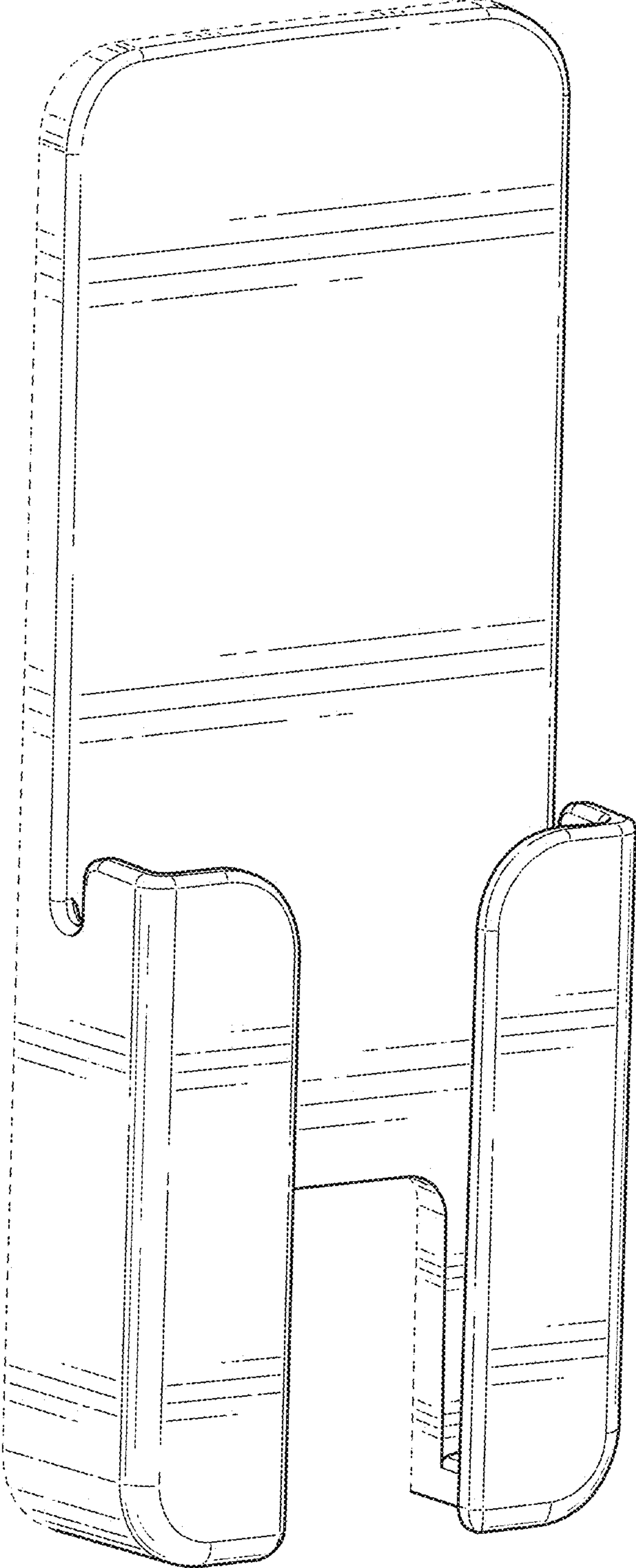


FIG. 1

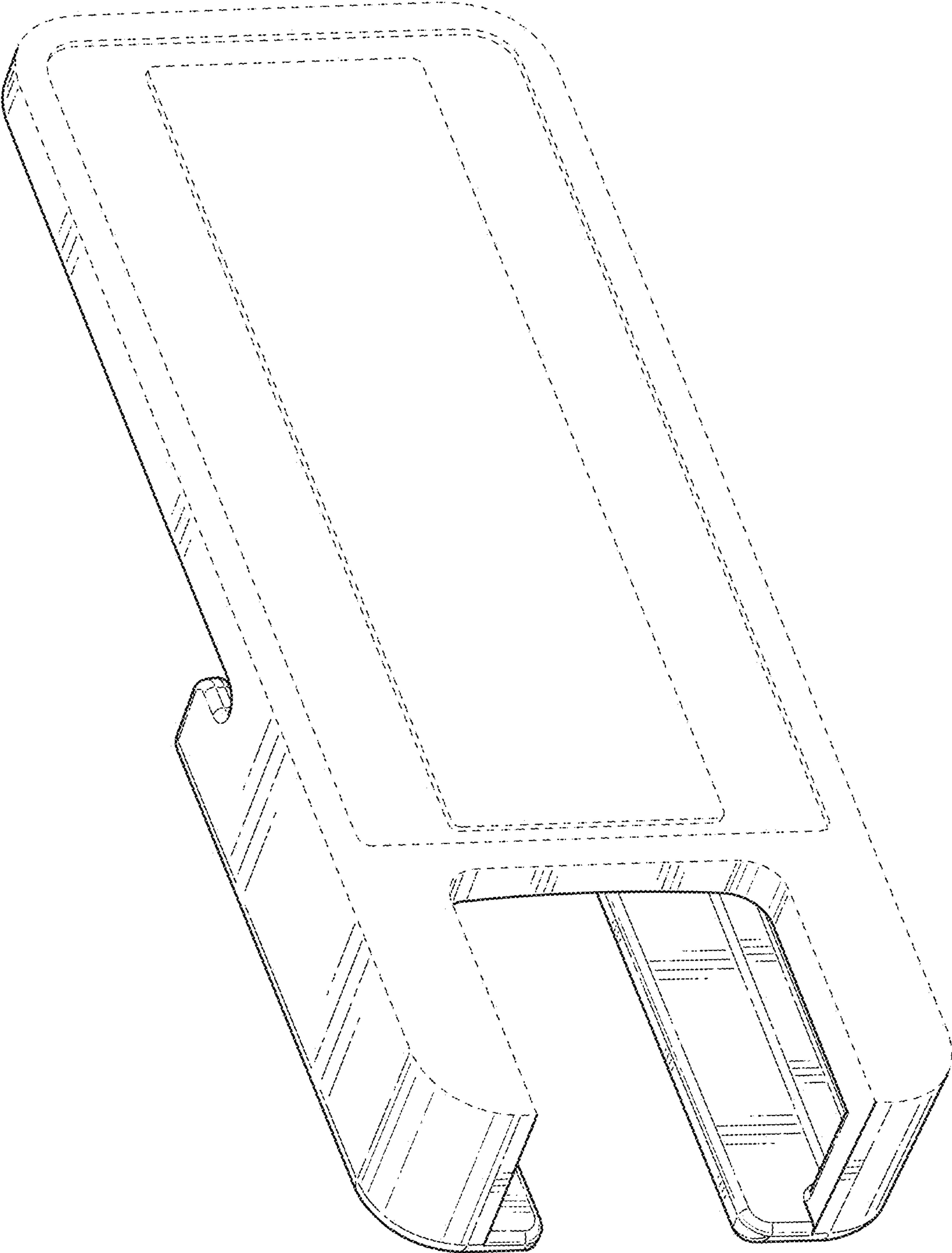


FIG. 2

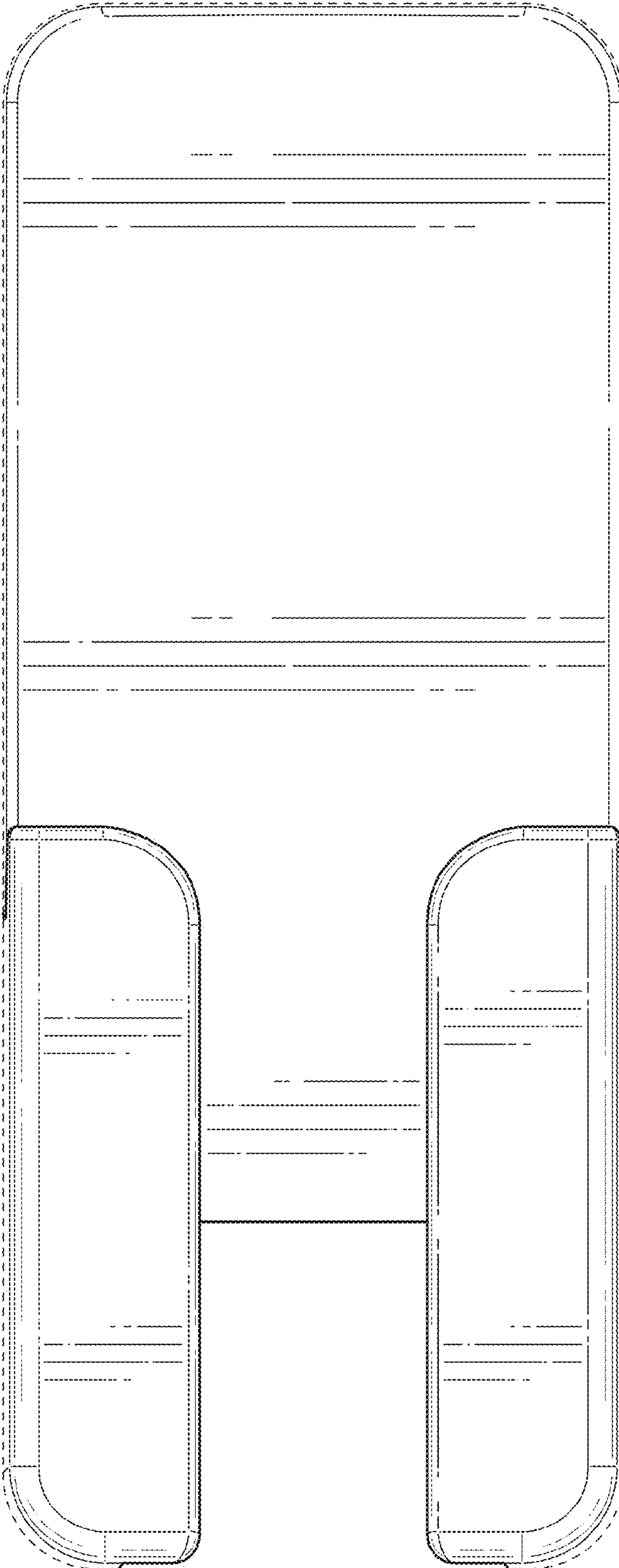


FIG. 3

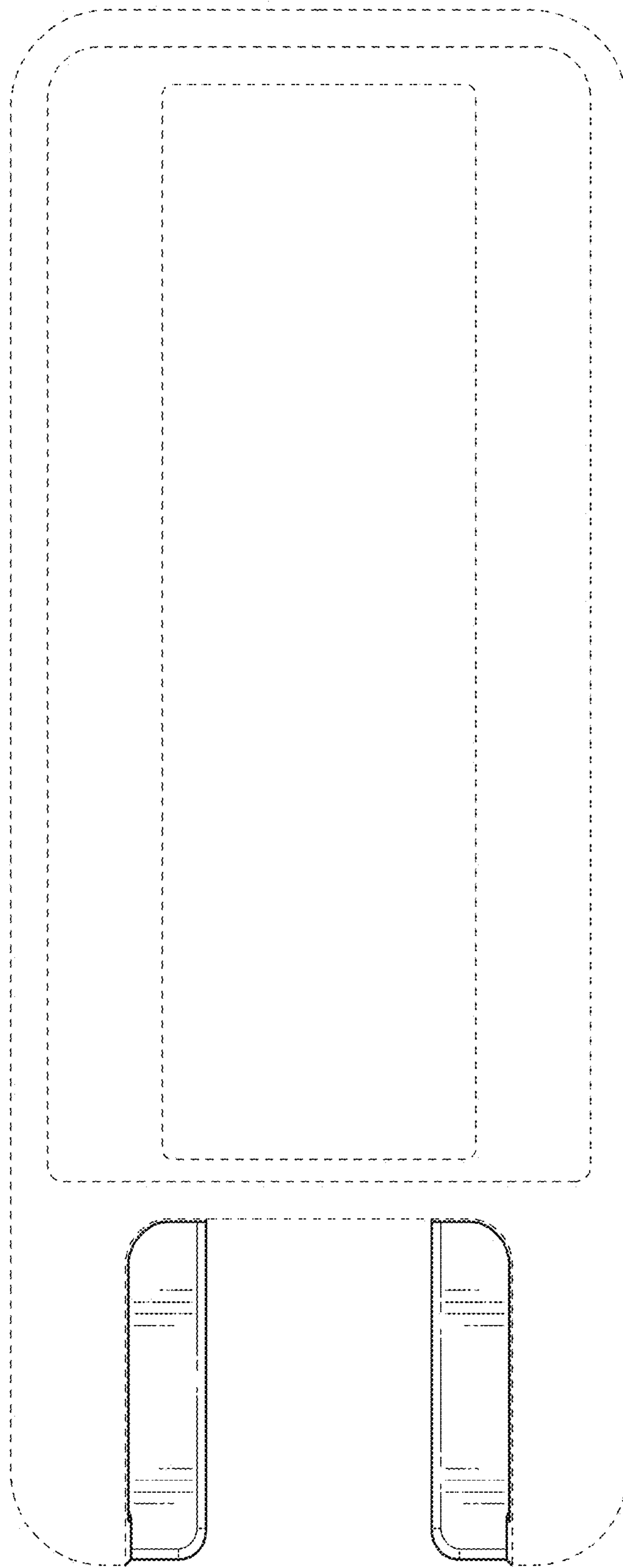


FIG. 4

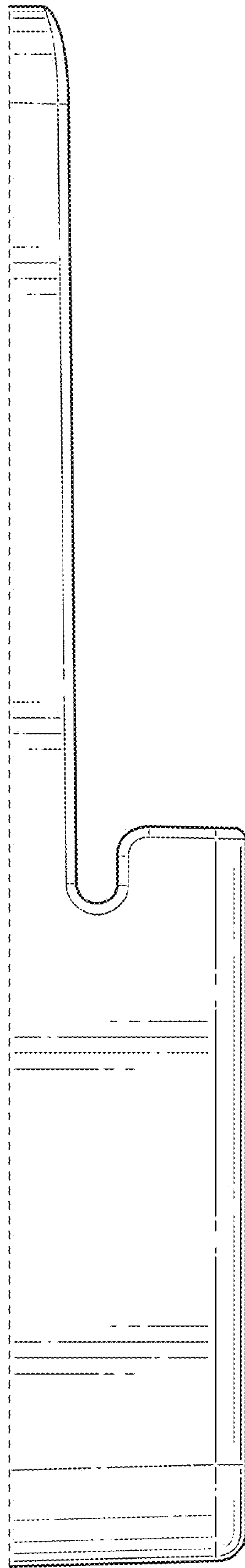


FIG. 5

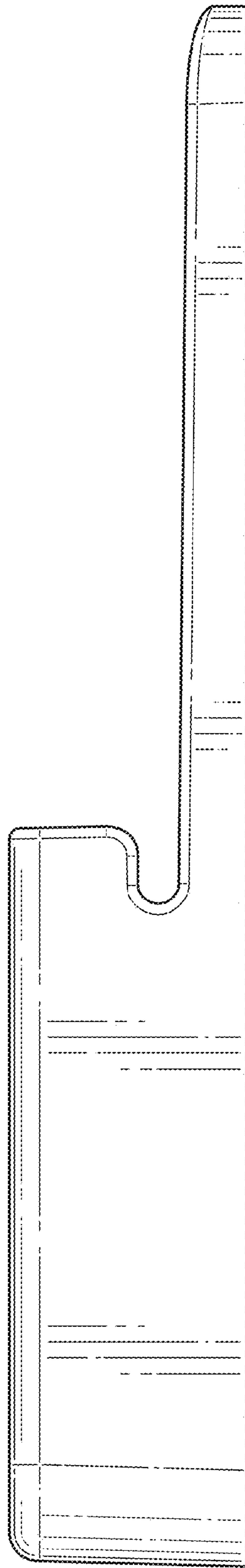


FIG. 6

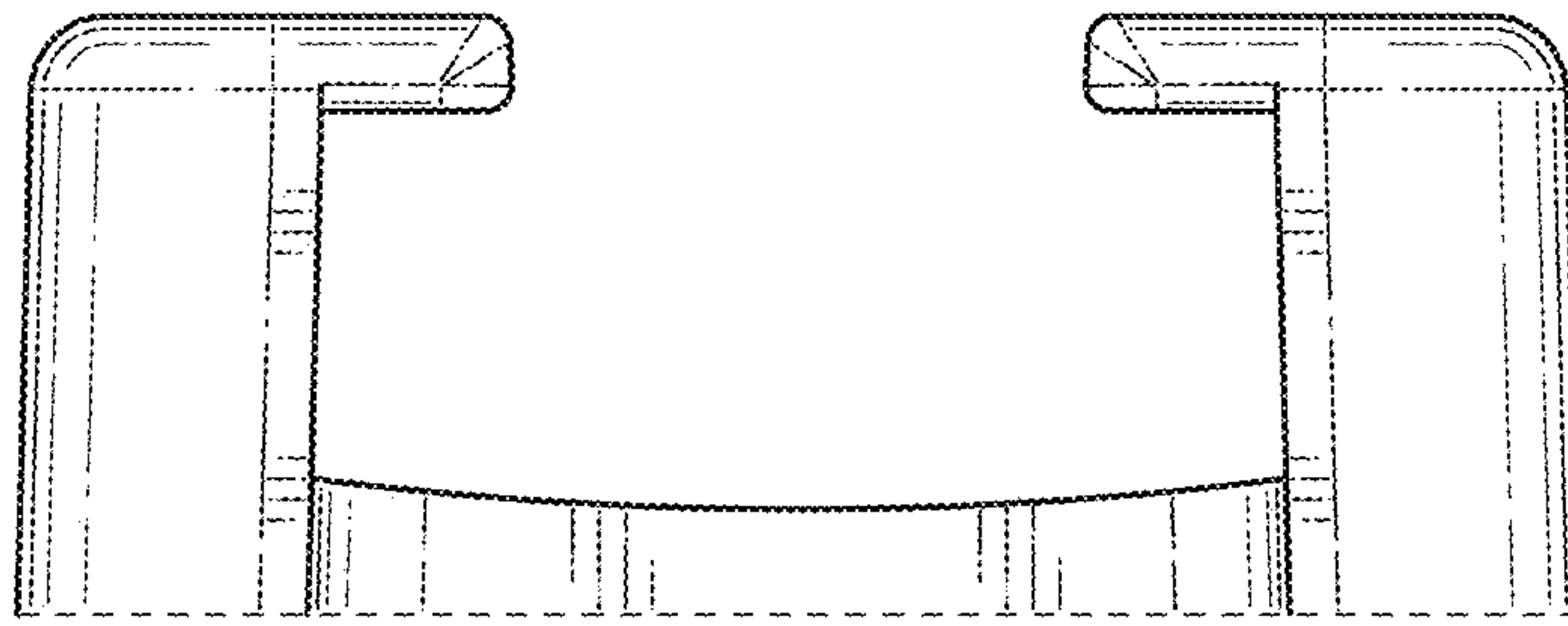


FIG. 7

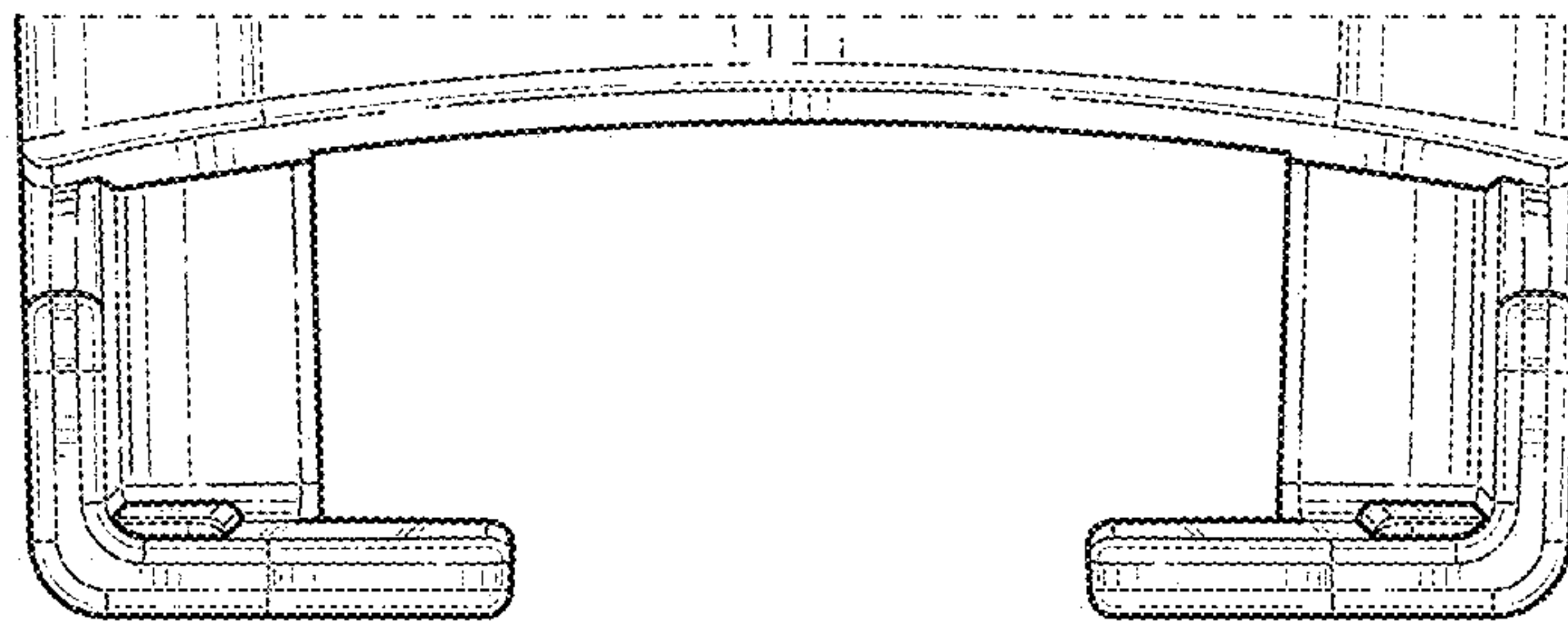


FIG. 8