



US00D984535S

(12) **United States Design Patent** (10) **Patent No.:** **US D984,535 S**
Mamo (45) **Date of Patent:** **** Apr. 25, 2023**

(54) **SECURITY LABEL FOR AN ELECTRONIC DEVICE**

internet: <https://www.amazon.com/100pcs-Residue-evident-Security-Sticker/dp/B0B6SPKL7C/> (Year: 2022).*

(Continued)

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(**) Term: **15 Years**

(57) **CLAIM**

The ornamental design for a security label for an electronic device, as shown and described.

(21) Appl. No.: **29/764,866**

DESCRIPTION

(22) Filed: **Jan. 4, 2021**

(51) **LOC (14) Cl.** **19-08**

FIG. 1 is a top view of a security label for an electronic device shown in a first state in a transition sequence, showing my new design;

(52) **U.S. Cl.**

USPC **D20/22**; D20/23; D20/27; D10/109.1

FIG. 2 is a bottom view thereof;

(58) **Field of Classification Search**

USPC D10/104.1, 109.1, 109.2; D19/1, 2, 5, 6; D20/11, 19, 22, 23, 27, 99

FIG. 3 is a top view shown in an activated second state in a transition sequence;

CPC G09F 3/0292; G09F 3/10; G09F 3/0341;

G09F 2003/0257; G09F 2003/0277; G09F

2003/0213; G09F 3/0288; G09F 3/0291;

G09F 2003/0241; G09F 3/0294; G09F

2003/0276

FIG. 4 is a bottom view thereof;

See application file for complete search history.

FIG. 5 is a front view of a security label for an electronic device shown in a first state in a transition sequence, shown in an environment of use;

FIG. 6 is a rear view thereof;

FIG. 7 is a front view of a security label for an electronic device shown in a second state in a transition sequence, shown in an environment of use; and,

FIG. 8 is a rear view thereof.

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(Continued)

The article in this patent includes a process or period in which the surface ornamentation changes into another surface ornamentation. The process or period in which the first state transitions into the second state forms no part of the claimed design.

The stippling shown in FIGS. 2 and 4 depicts a contrast in appearance.

The broken lines shown on the label show portions of the article that forms no part of the claimed design. The broken lines showing an electronic device in FIGS. 5-8 show environment that forms no part of the claimed design.

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1 Claim, 6 Drawing Sheets



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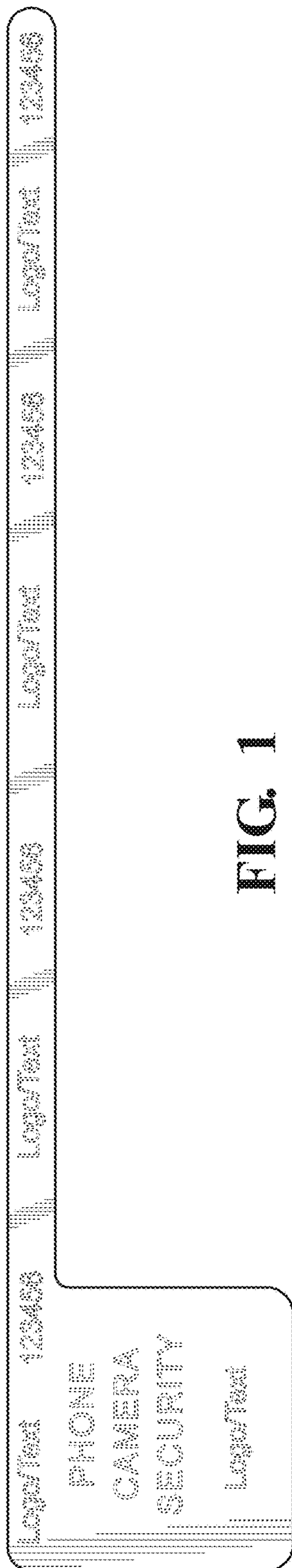


FIG. 1

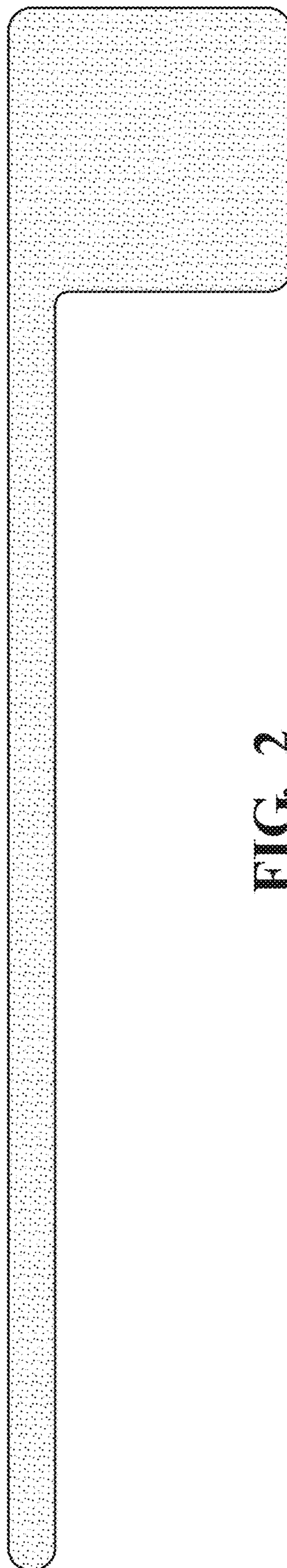


FIG. 2

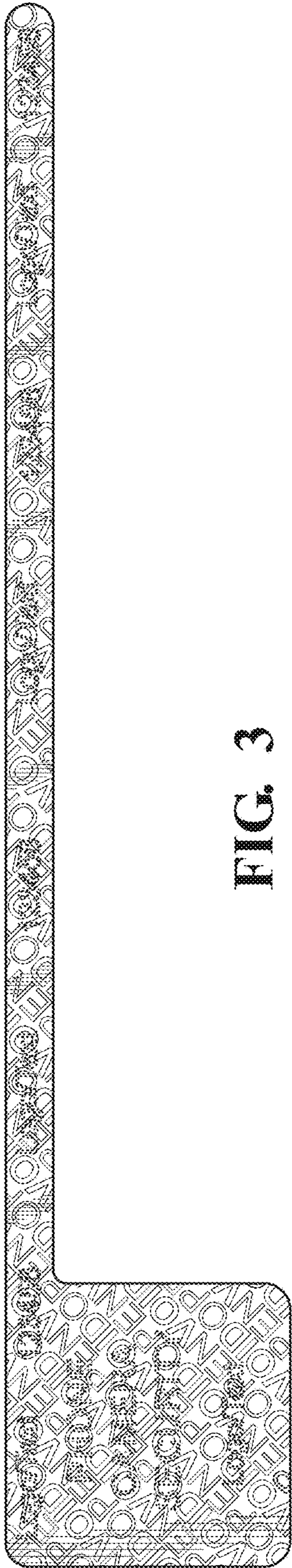


FIG. 3

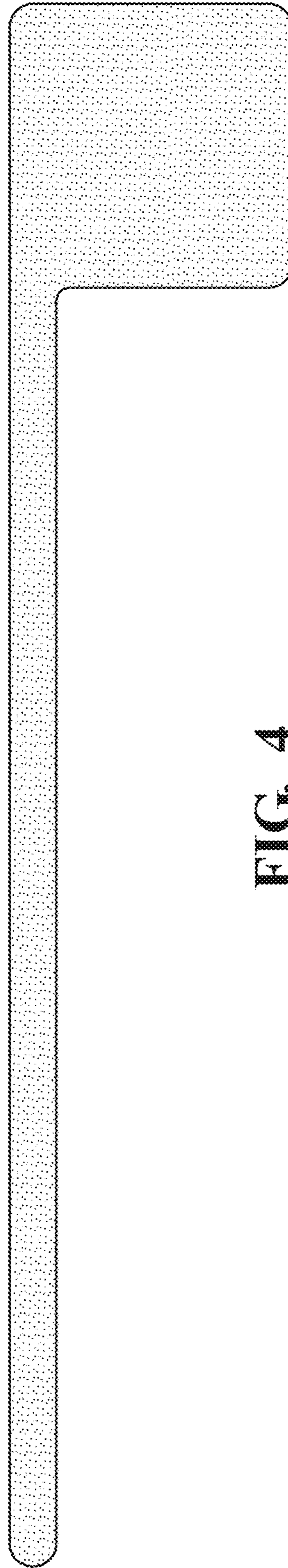


FIG. 4



FIG. 5

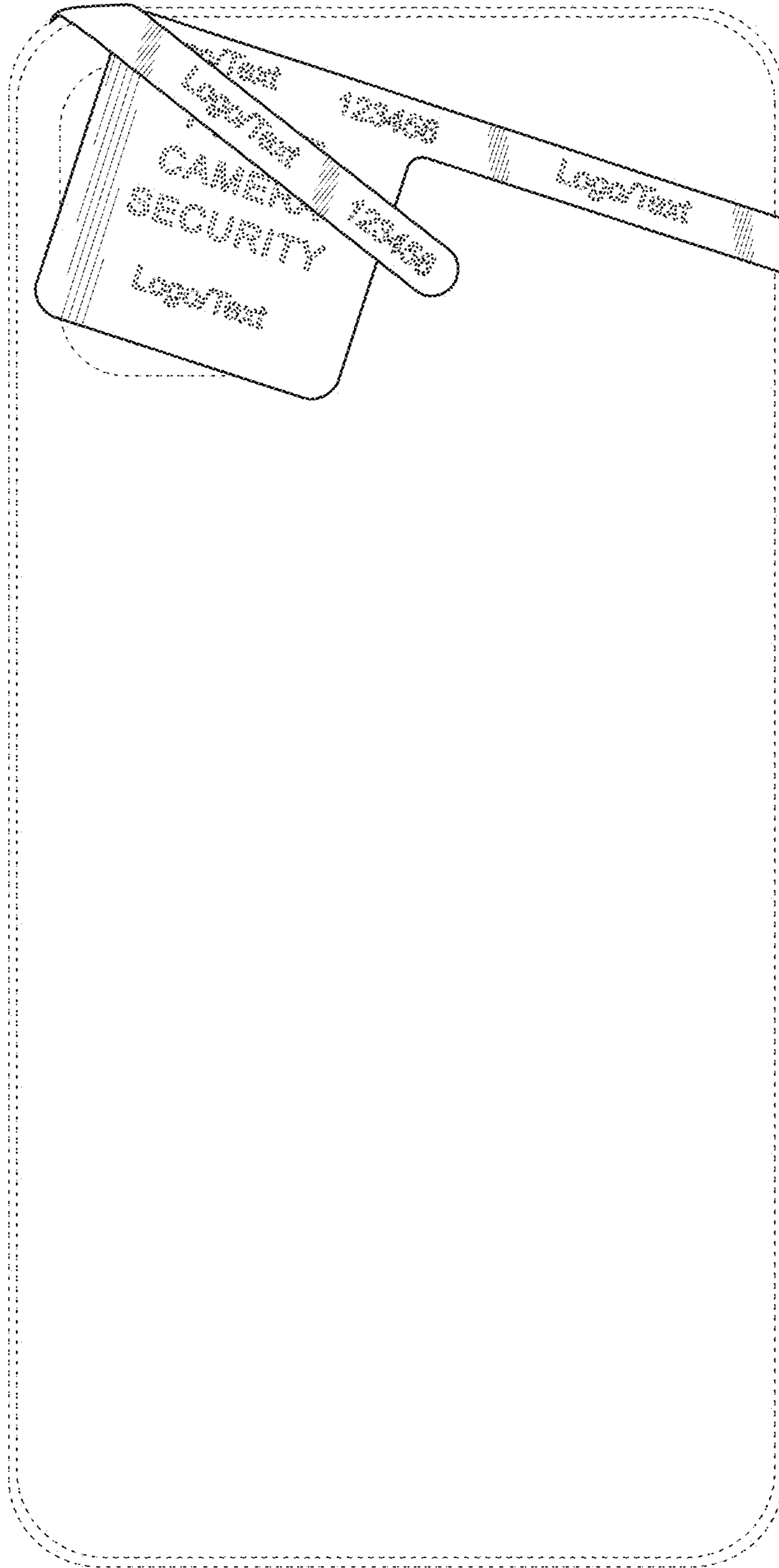


FIG. 6



FIG. 7

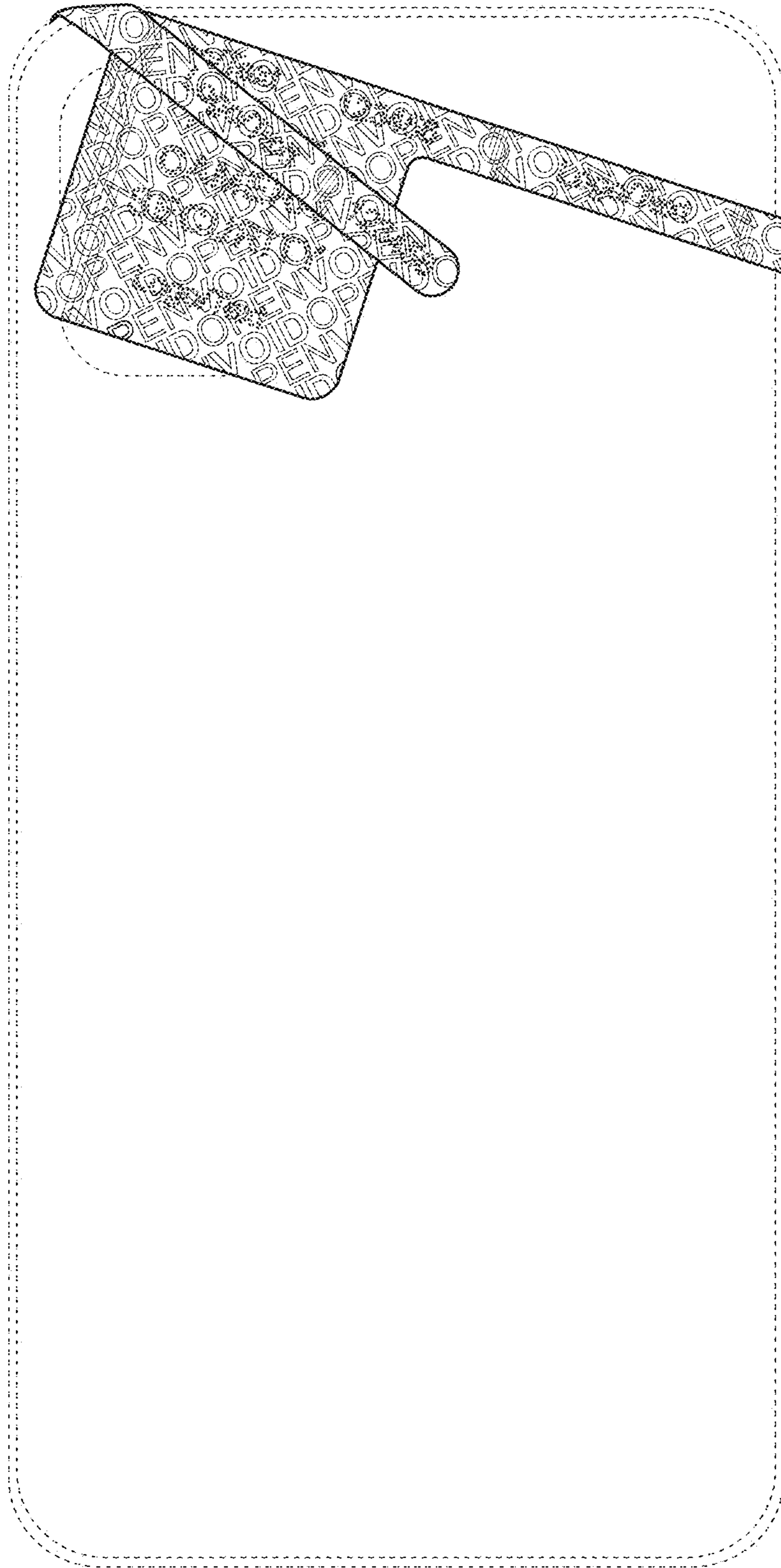


FIG. 8