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(12) United States Design Patent

Rosson et al.

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(54) **MULTIPORT TERMINAL FOR MAKING OPTICAL CONNECTIONS**

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(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **15 Years**

(21) Appl. No.: **29/770,223**

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Related U.S. Application Data

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(51) **LOC (14) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/146**; D14/433

(58) **Field of Classification Search**
USPC D13/146, 147, 123, 133, 154, D13/137.1–139.8, 110, 159, 184, 199; D14/433, 240, 242
CPC .. G02B 6/4441; G02B 6/4451; G02B 6/3897; G02B 6/4466; G02B 6/00; G02B 6/4439; G02B 6/4472; G02B 6/3885; G02B 6/44; G02B 6/3831; G02B 6/3825; G02B 6/3869; G02B 6/3893
See application file for complete search history.

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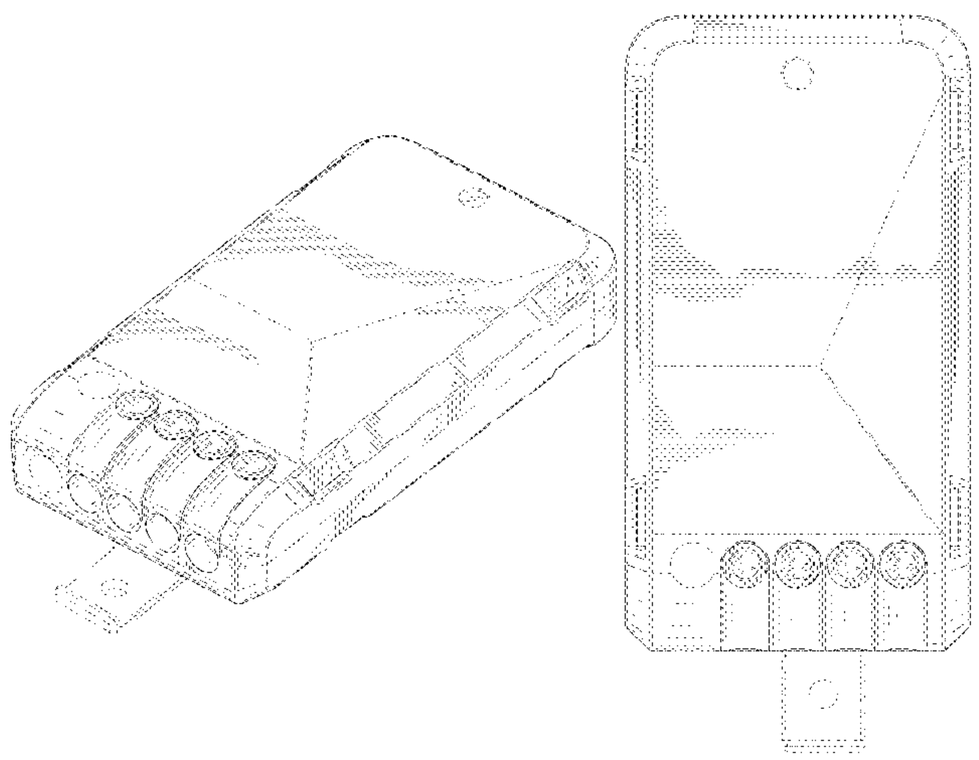

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(57) **CLAIM**

The ornamental design for a multiport terminal for making optical connections, as shown and described.

DESCRIPTION

FIG. **1** is a top perspective view of a first embodiment of a multiport terminal for making optical connections showing our new design;
FIG. **2** is a bottom perspective view thereof of FIG. **1**;
FIG. **3** is a front view thereof of FIG. **1**;
FIG. **4** is a rear view thereof of FIG. **1**;
FIG. **5** is a right side view thereof of FIG. **1**;
FIG. **6** is a left side view thereof of FIG. **1**;
FIG. **7** is a top view thereof of FIG. **1**; and
FIG. **8** is a bottom view thereof of FIG. **1**.

FIG. **9** is a top perspective view of a second embodiment of a multiport terminal for making optical connections showing our new design;
FIG. **10** is a bottom perspective view thereof of FIG. **9**;
FIG. **11** is a front view thereof of FIG. **9**;
FIG. **12** is a rear view thereof of FIG. **9**;
FIG. **13** is a right side view thereof of FIG. **9**;
FIG. **14** is a left side view thereof of FIG. **9**;
FIG. **15** is a top view thereof of FIG. **9**; and
FIG. **16** is a bottom view thereof of FIG. **9**.
FIG. **17** is a top perspective view of a third embodiment of a multiport terminal for making optical connections showing our new design;
FIG. **18** is a bottom perspective view thereof of FIG. **17**;
FIG. **19** is a front view thereof of FIG. **17**;
FIG. **20** is a rear view thereof of FIG. **17**;
FIG. **21** is a right side view thereof of FIG. **17**;
FIG. **22** is a left side view thereof of FIG. **17**;
FIG. **23** is a top view thereof of FIG. **17**; and
FIG. **24** is a bottom view thereof of FIG. **17**.
FIG. **25** is a top perspective view of a fourth embodiment of a multiport terminal for making optical connections showing our new design;
FIG. **26** is a bottom perspective view thereof of FIG. **25**;
FIG. **27** is a front view thereof of FIG. **25**;
FIG. **28** is a rear view thereof of FIG. **25**;
FIG. **29** is a right side view thereof of FIG. **25**;
FIG. **30** is a left side view thereof of FIG. **25**;
FIG. **31** is a top view thereof of FIG. **25**; and
FIG. **32** is a bottom view thereof of FIG. **25**.
FIG. **33** is a top perspective view of a fifth embodiment of a multiport terminal for making optical connections showing our new design;
FIG. **34** is a bottom perspective view thereof of FIG. **33**;
FIG. **35** is a front view thereof of FIG. **33**;
FIG. **36** is a rear view thereof of FIG. **33**;
FIG. **37** is a right side view thereof of FIG. **33**;
FIG. **38** is a left side view thereof of FIG. **33**;
FIG. **39** is a top view thereof of FIG. **33**; and
FIG. **40** is a bottom view thereof of FIG. **33**.
FIG. **41** is a top perspective view of a sixth embodiment of a multiport terminal for making optical connections showing our new design;
FIG. **42** is a bottom perspective view thereof of FIG. **41**;
FIG. **43** is a front view thereof of FIG. **41**;
FIG. **44** is a rear view thereof of FIG. **41**;
FIG. **45** is a right side view thereof of FIG. **41**;
FIG. **46** is a left side view thereof of FIG. **41**;
FIG. **47** is a top view thereof of FIG. **41**; and,
FIG. **48** is a bottom view thereof of FIG. **41**.
In FIGS. **1-48**, the evenly-spaced broken lines are included for the purpose of illustrating environmental structure and form no part of the claimed design.

1 Claim, 36 Drawing Sheets

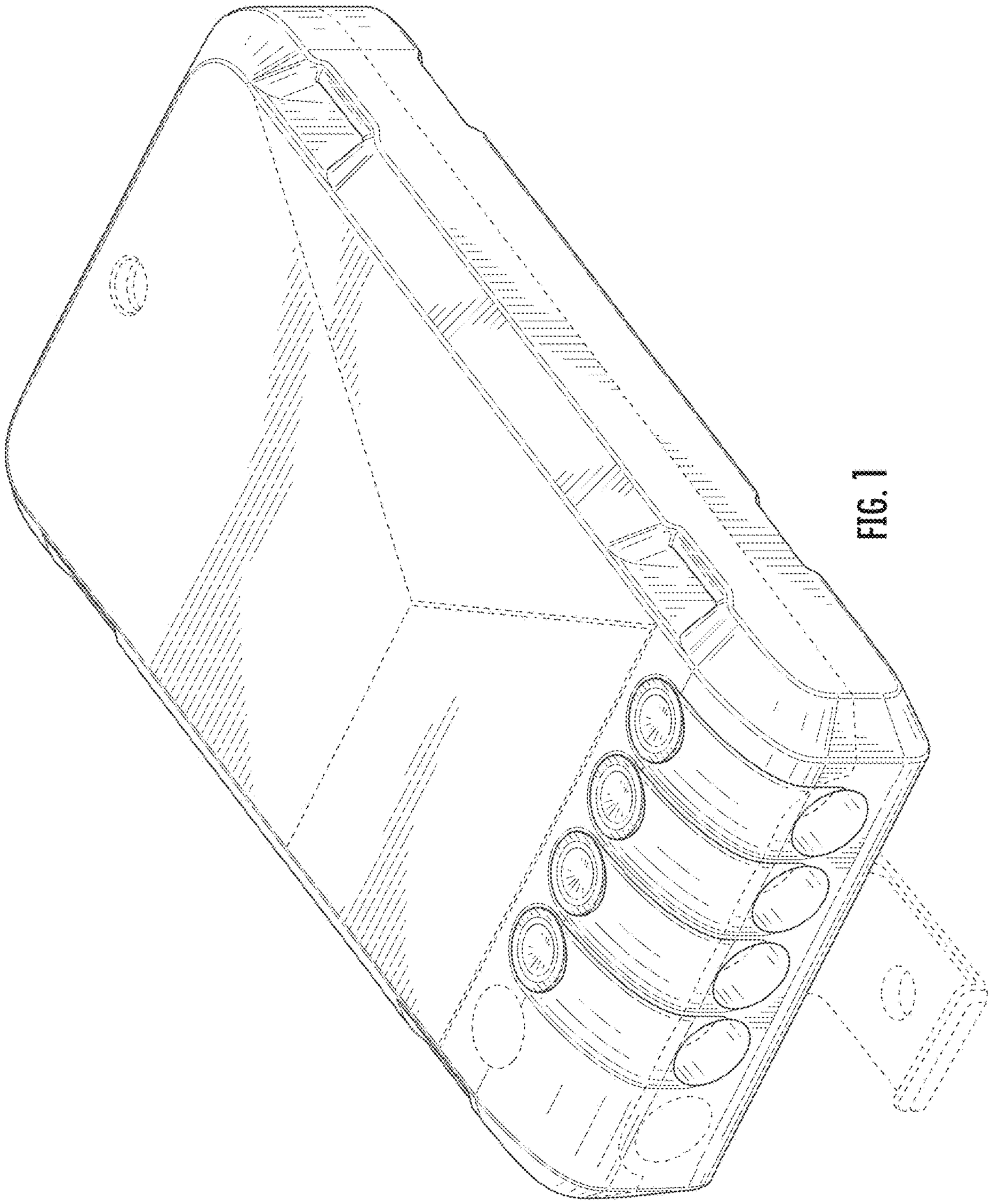


FIG. 1

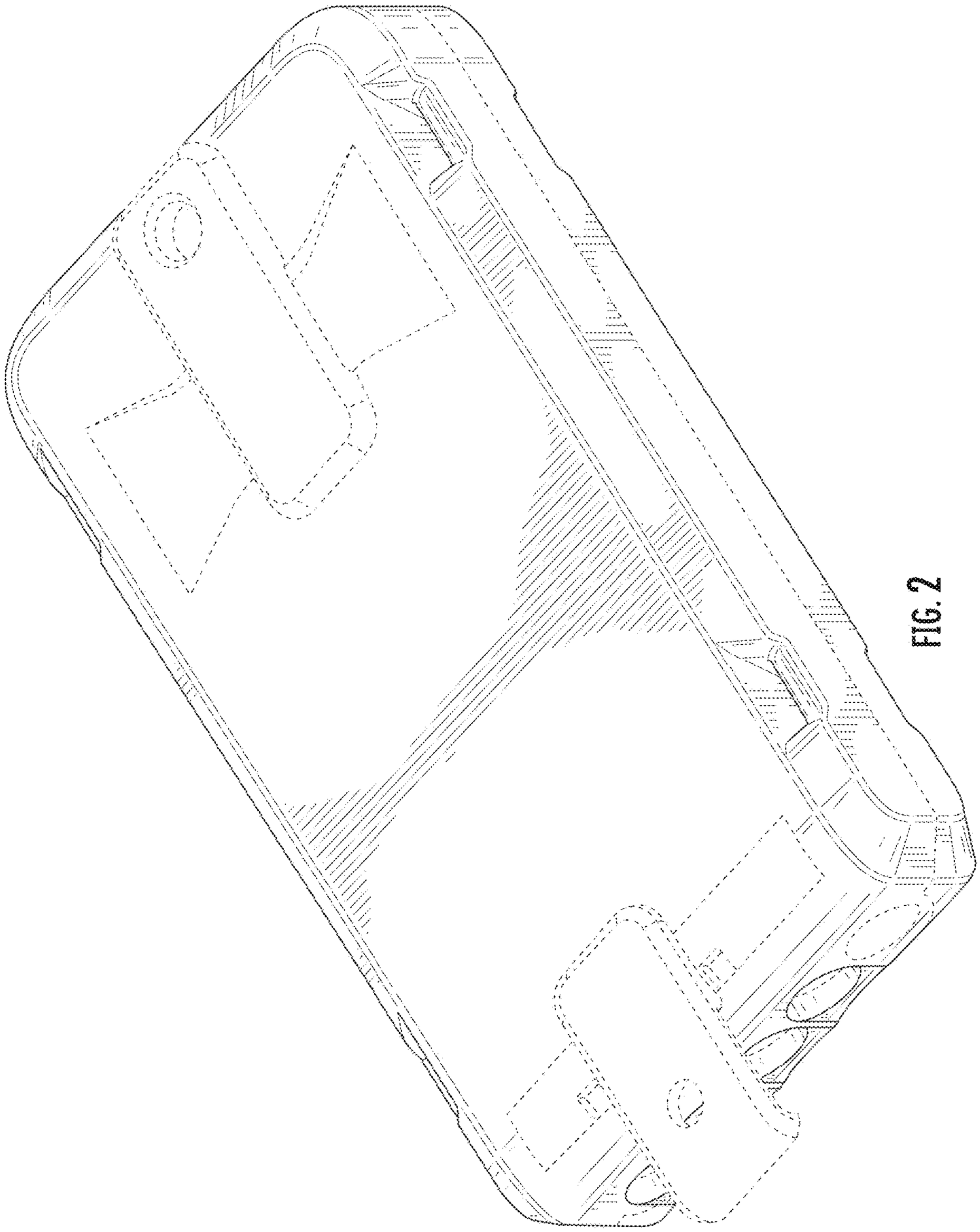


FIG. 2

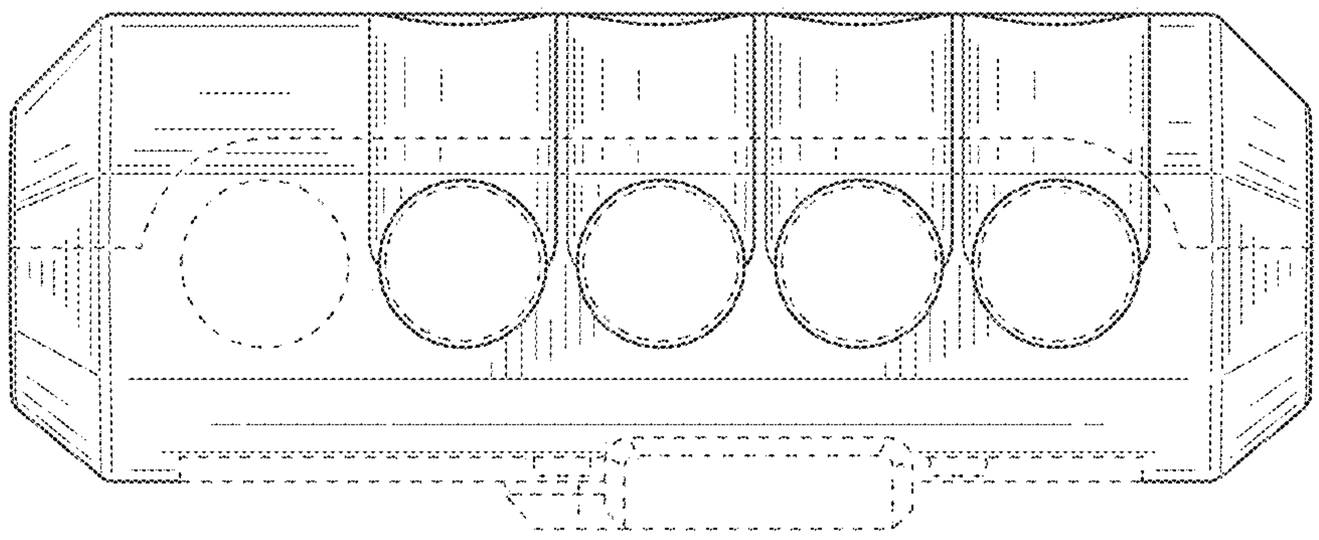


FIG. 3

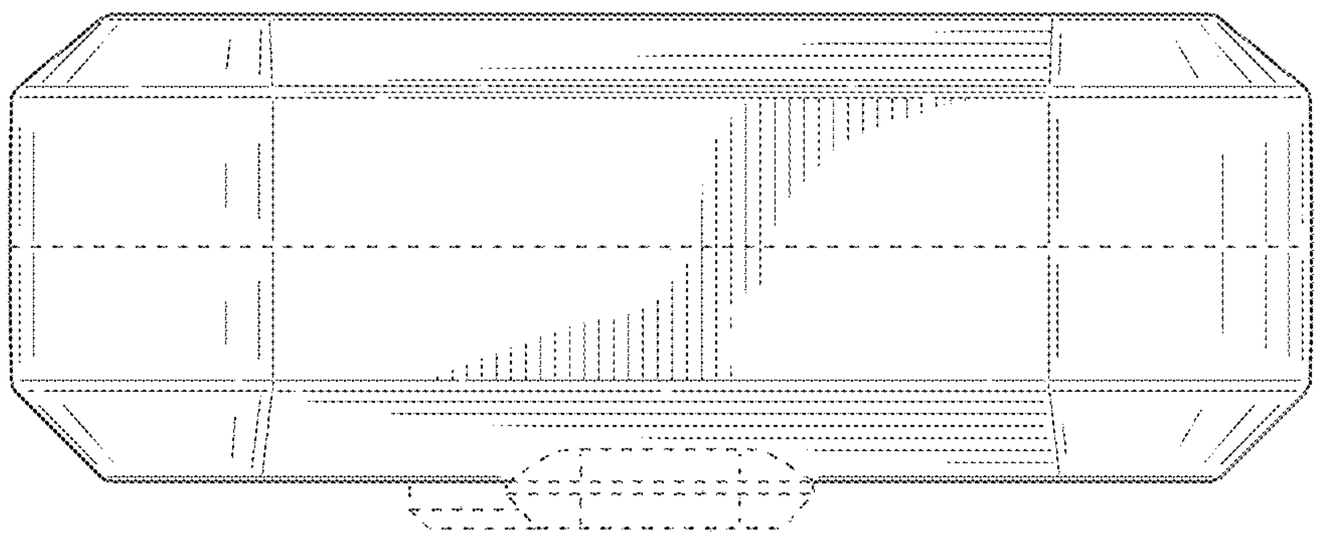


FIG. 4

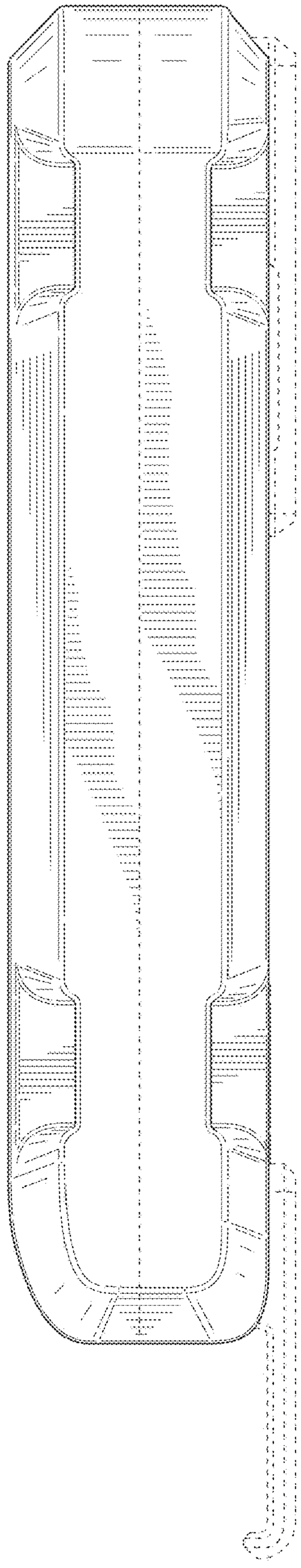


FIG. 5

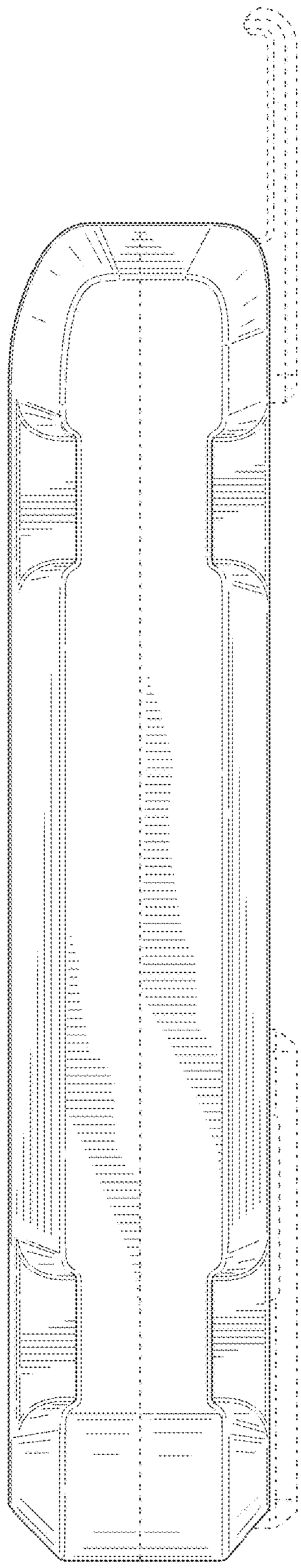


FIG. 6

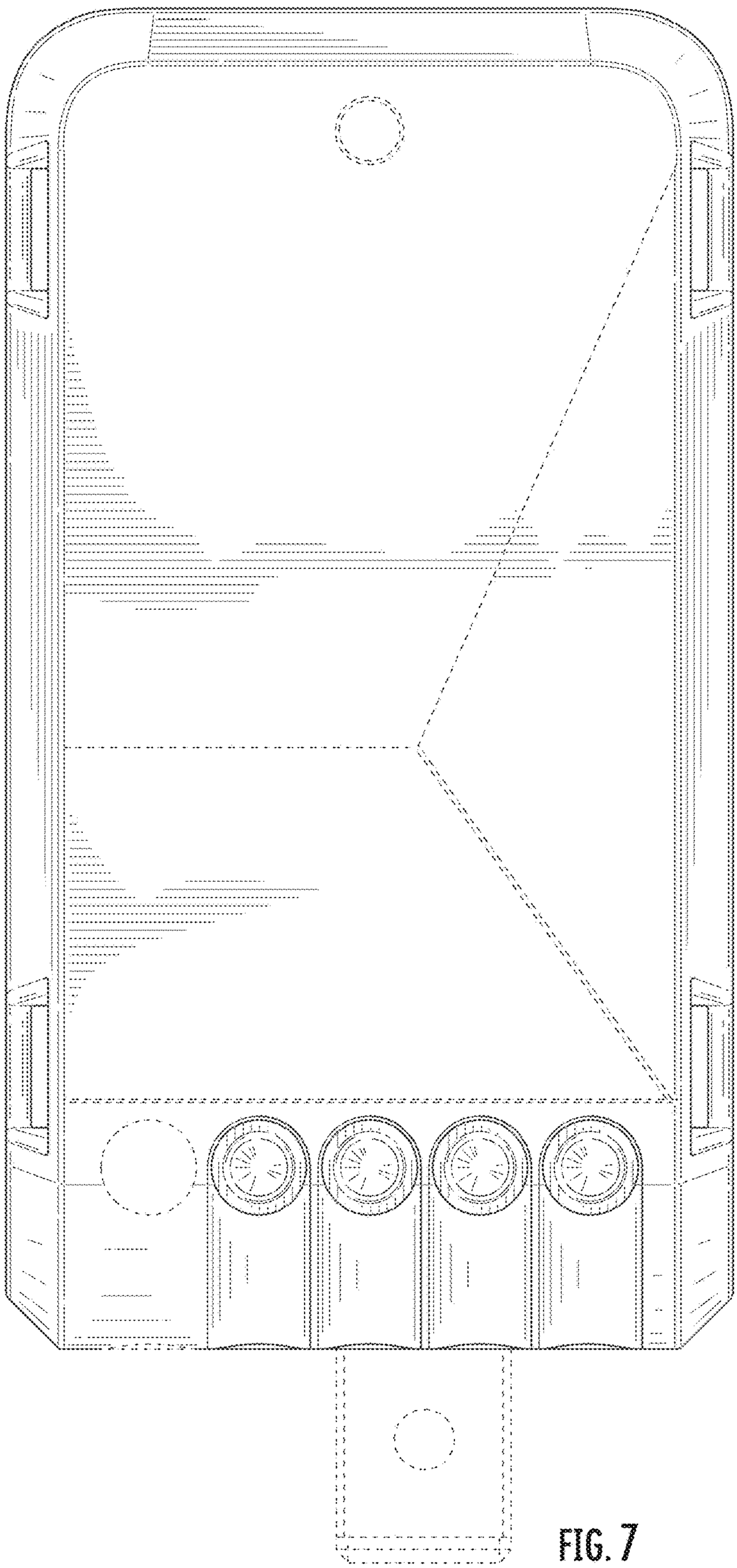


FIG. 7

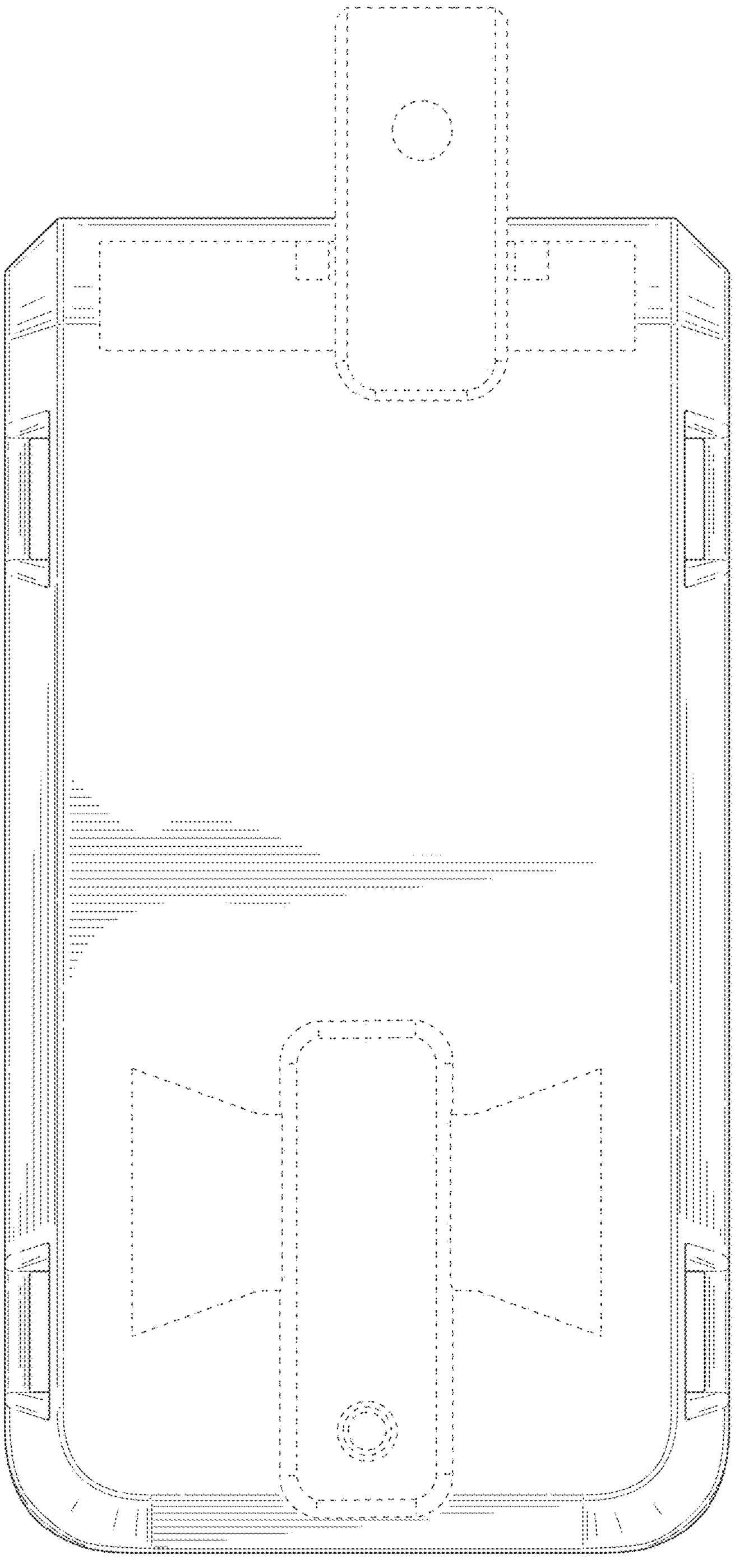


FIG. 8

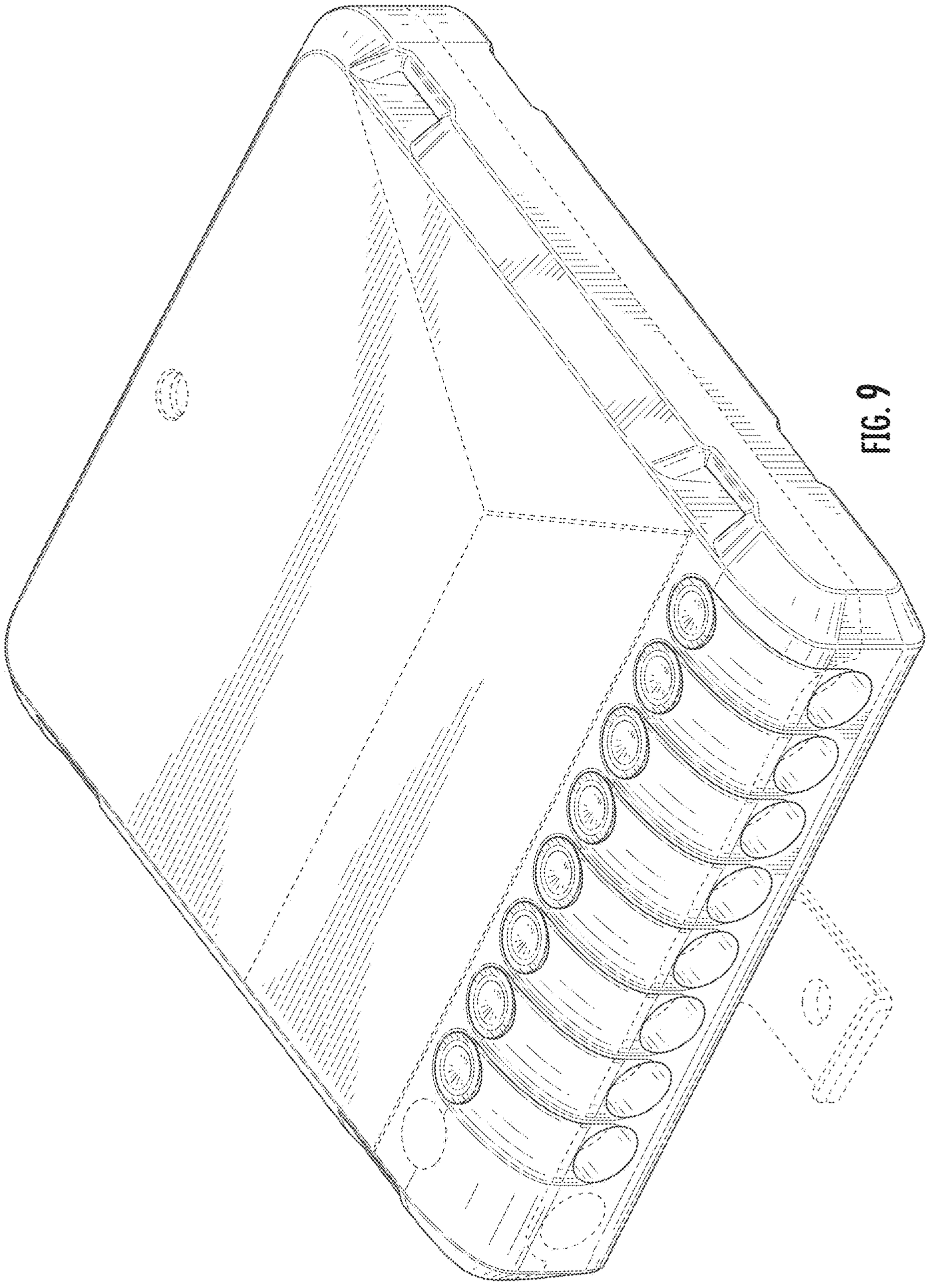


FIG. 9

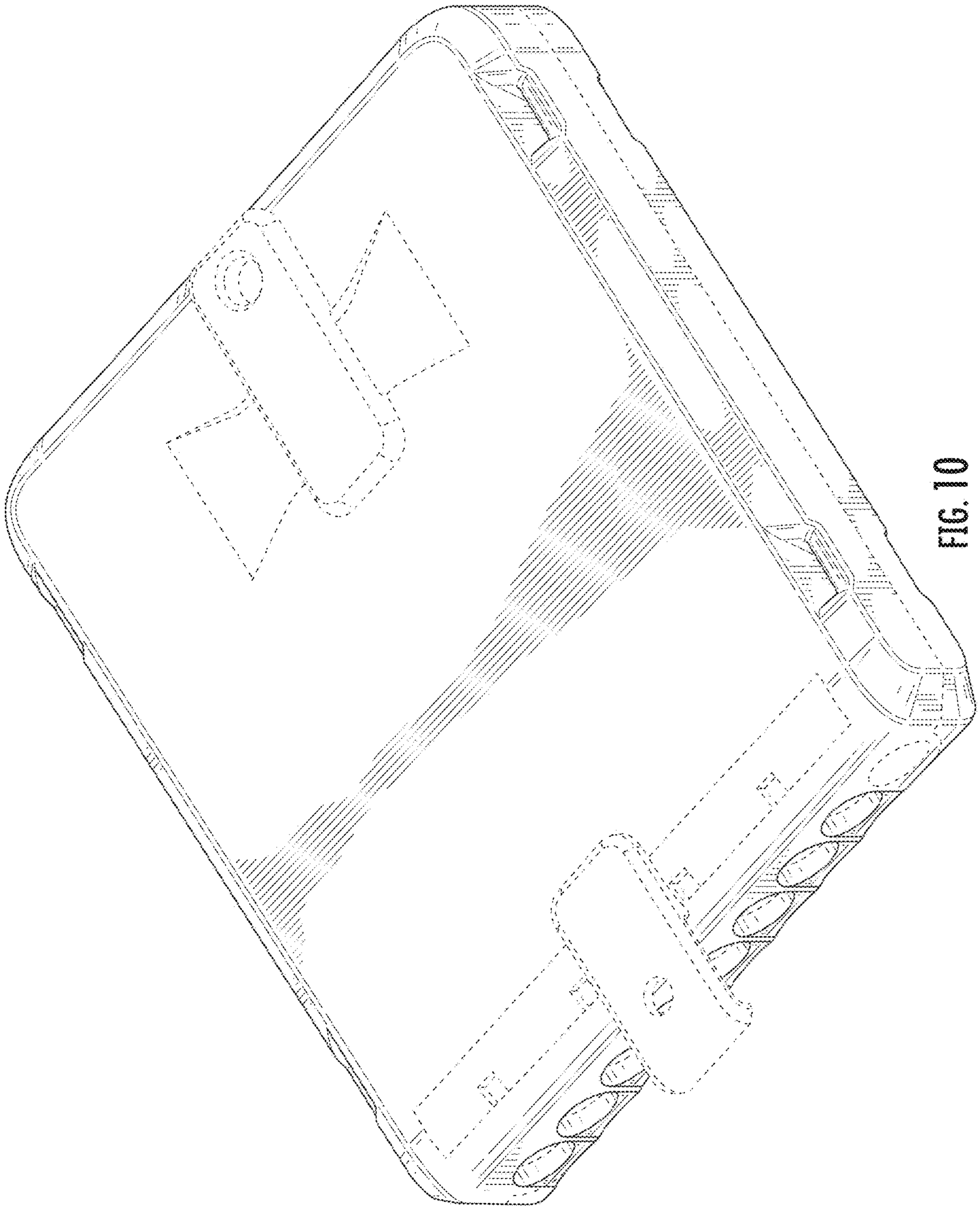
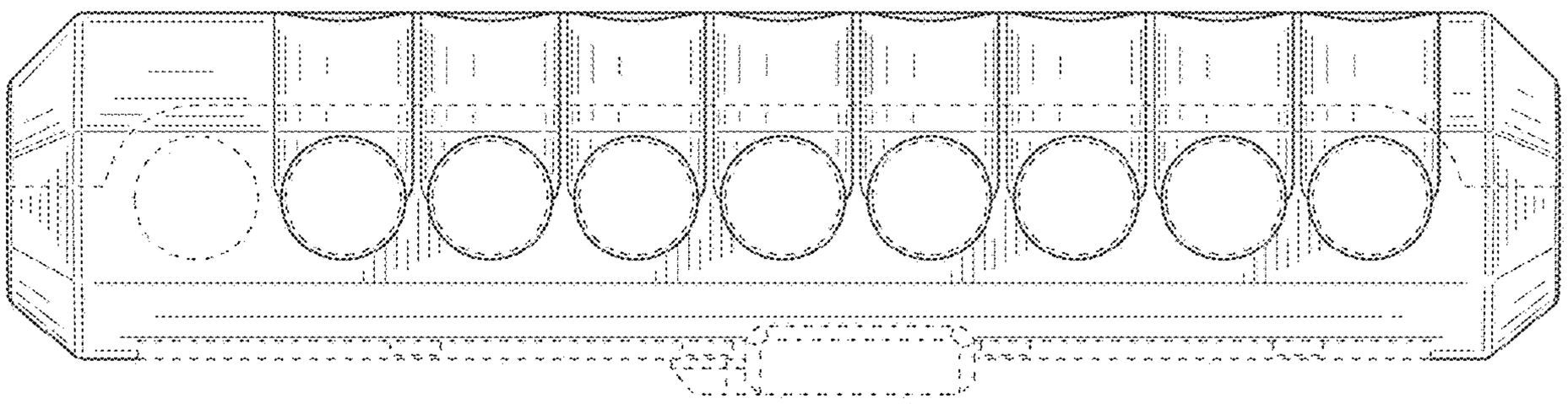


FIG. 10



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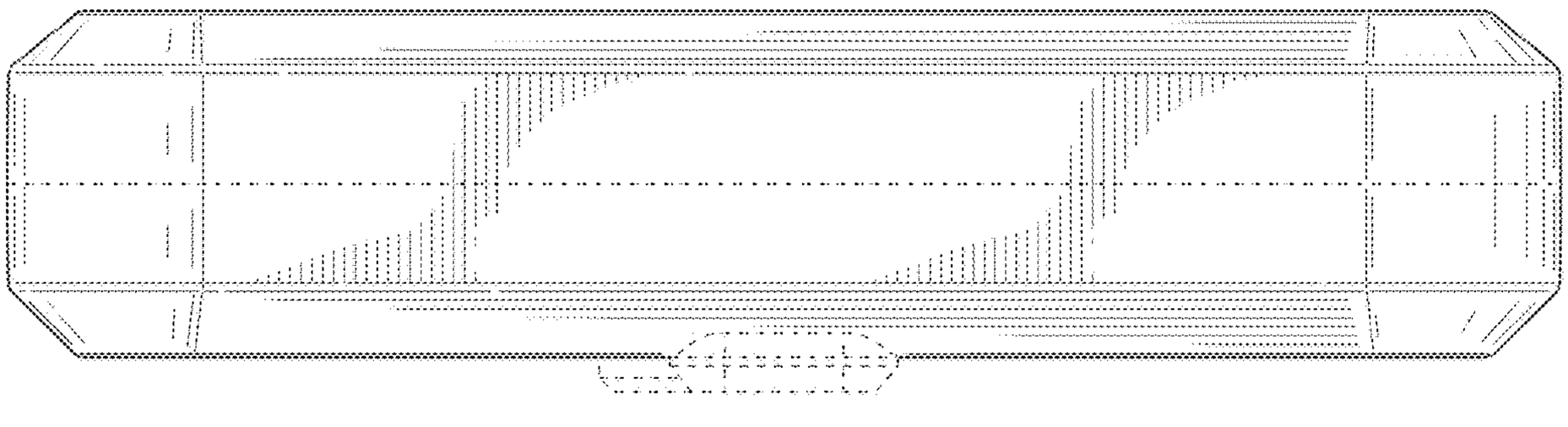

FIG. 12

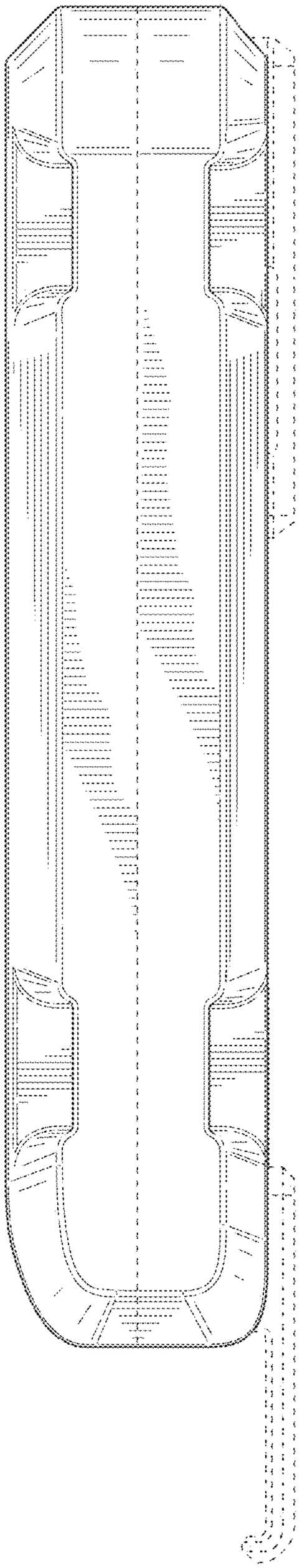

FIG. 13

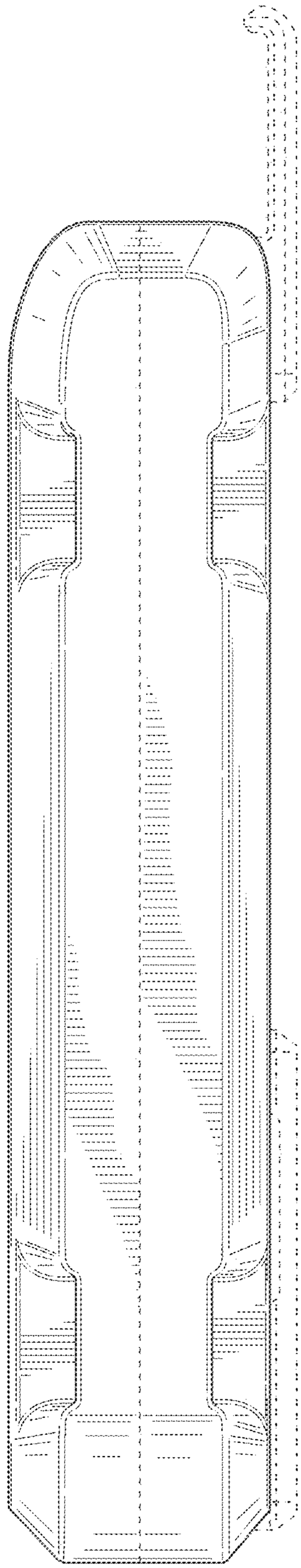

FIG. 14

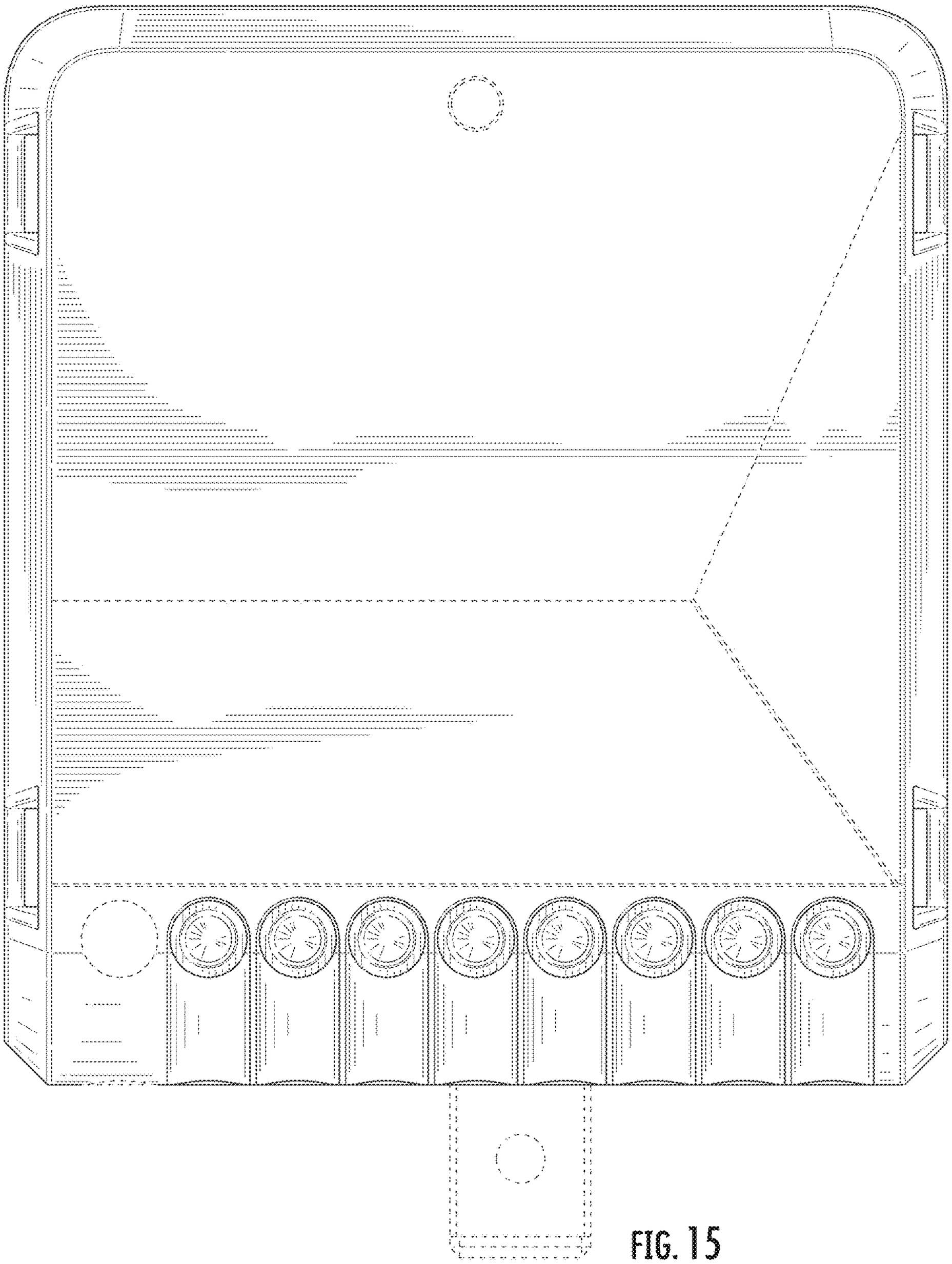

FIG. 15

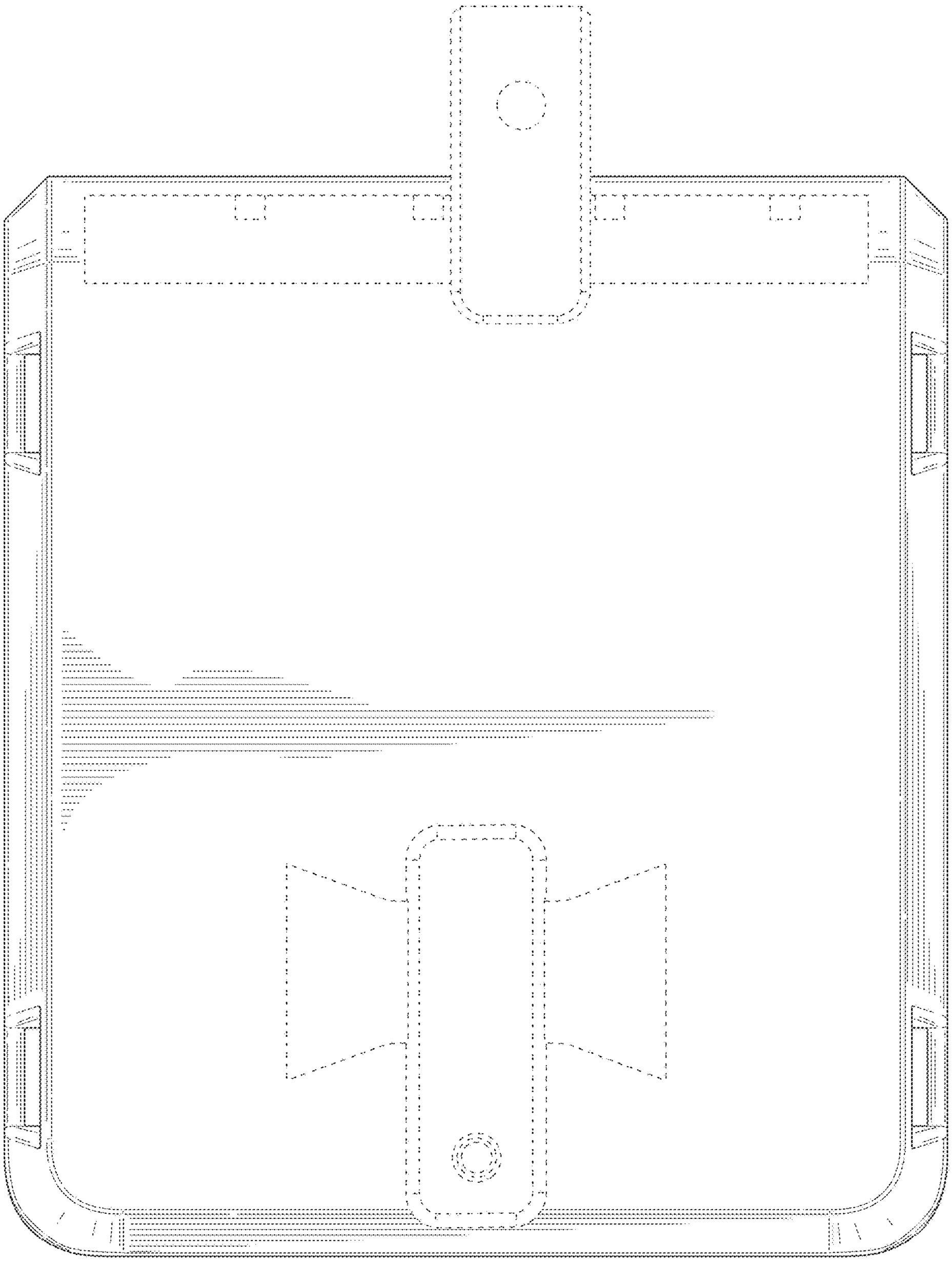

FIG. 16

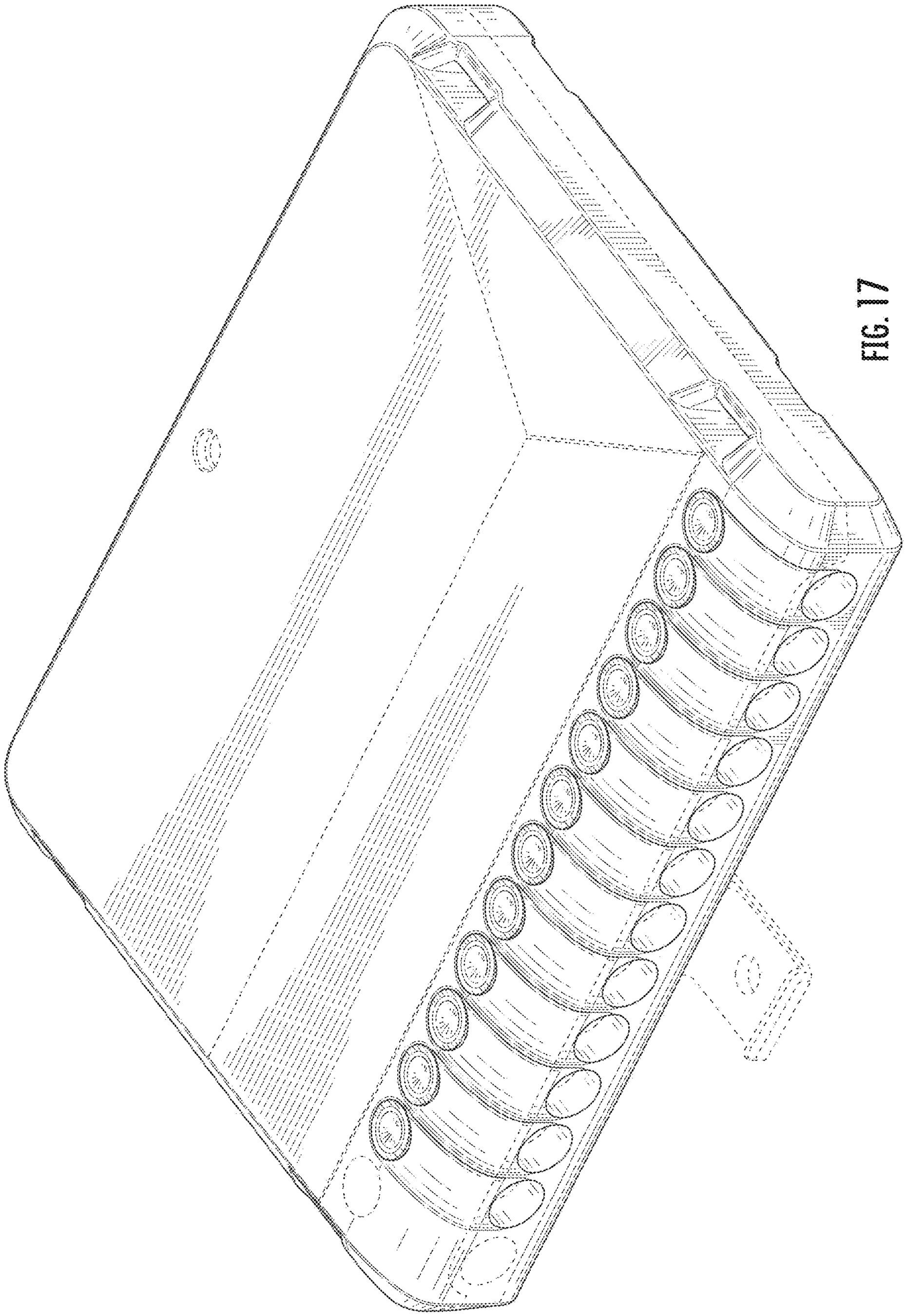

FIG. 17

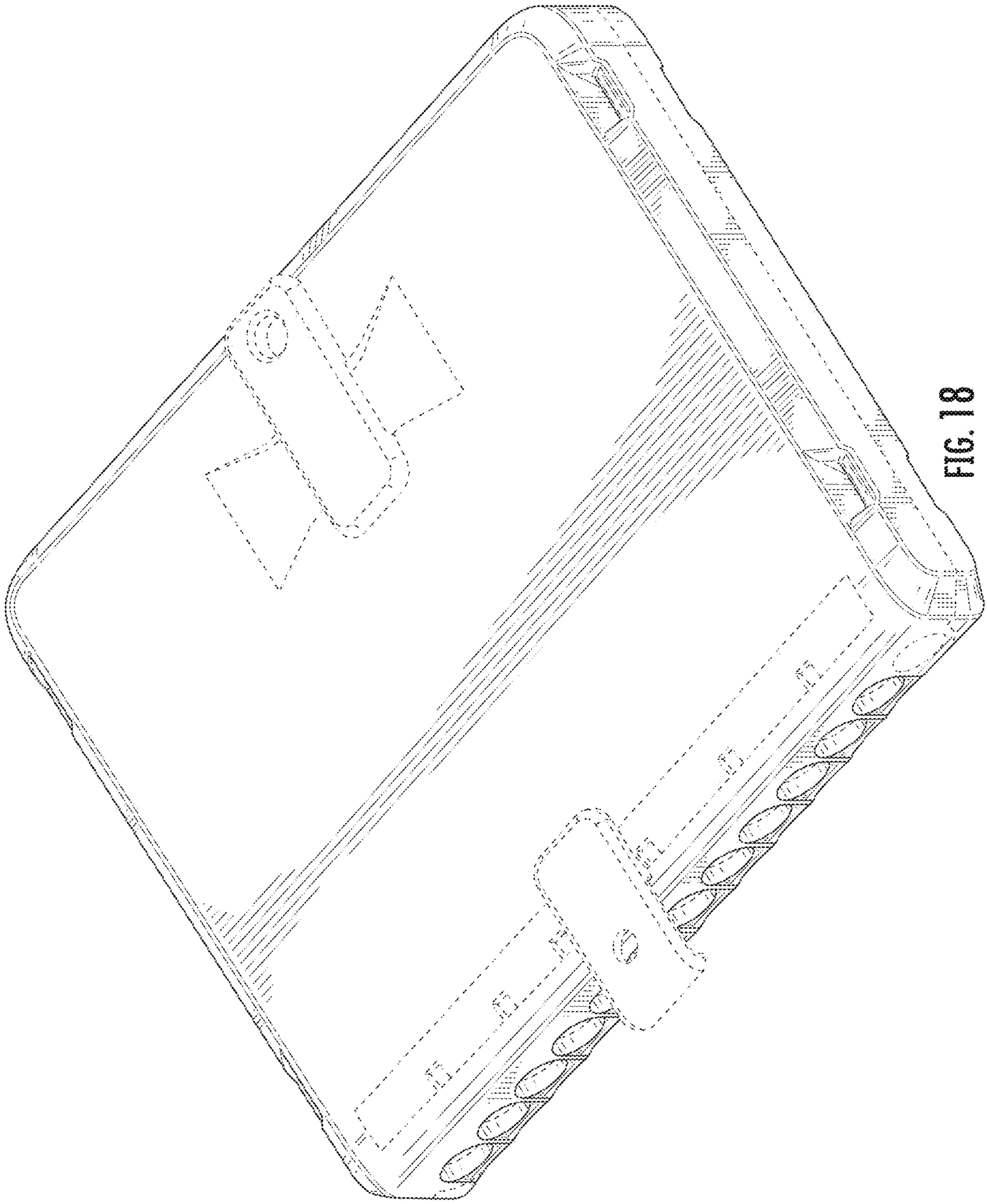

FIG. 18

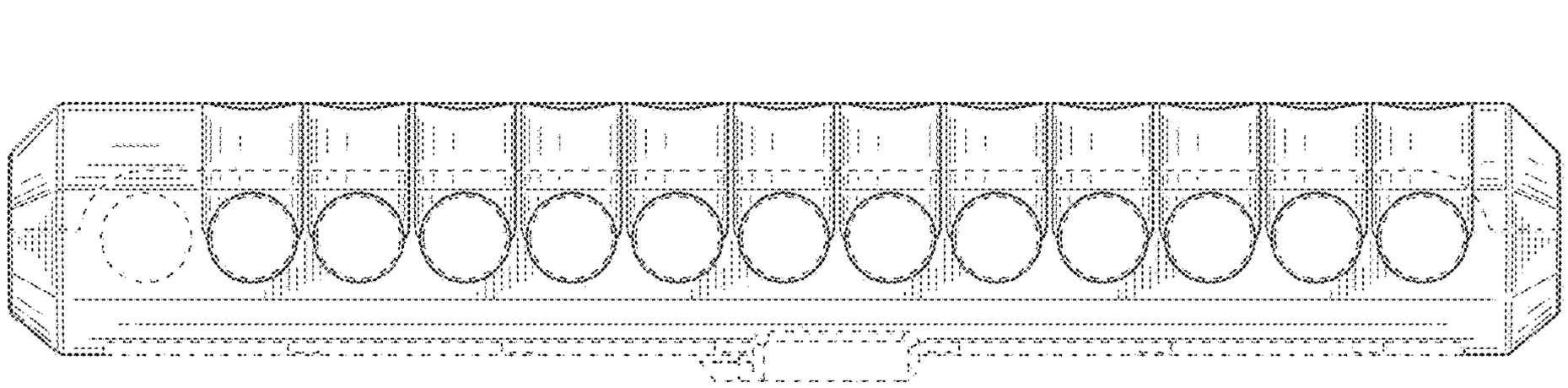

FIG. 19

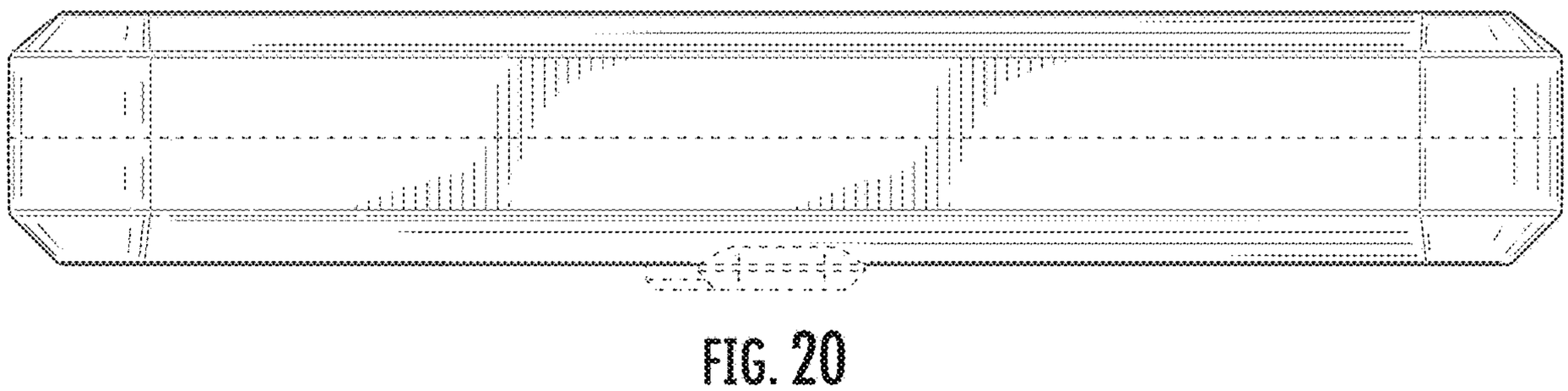

FIG. 20

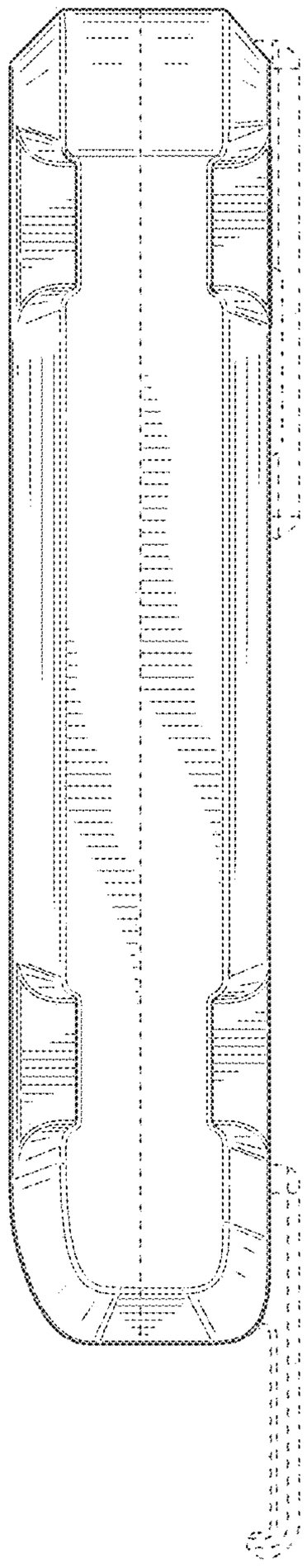

FIG. 21

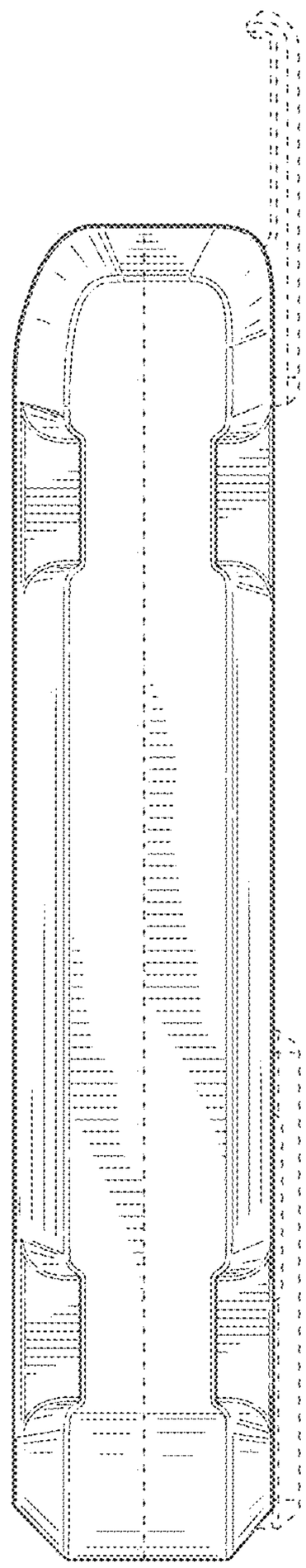

FIG. 22

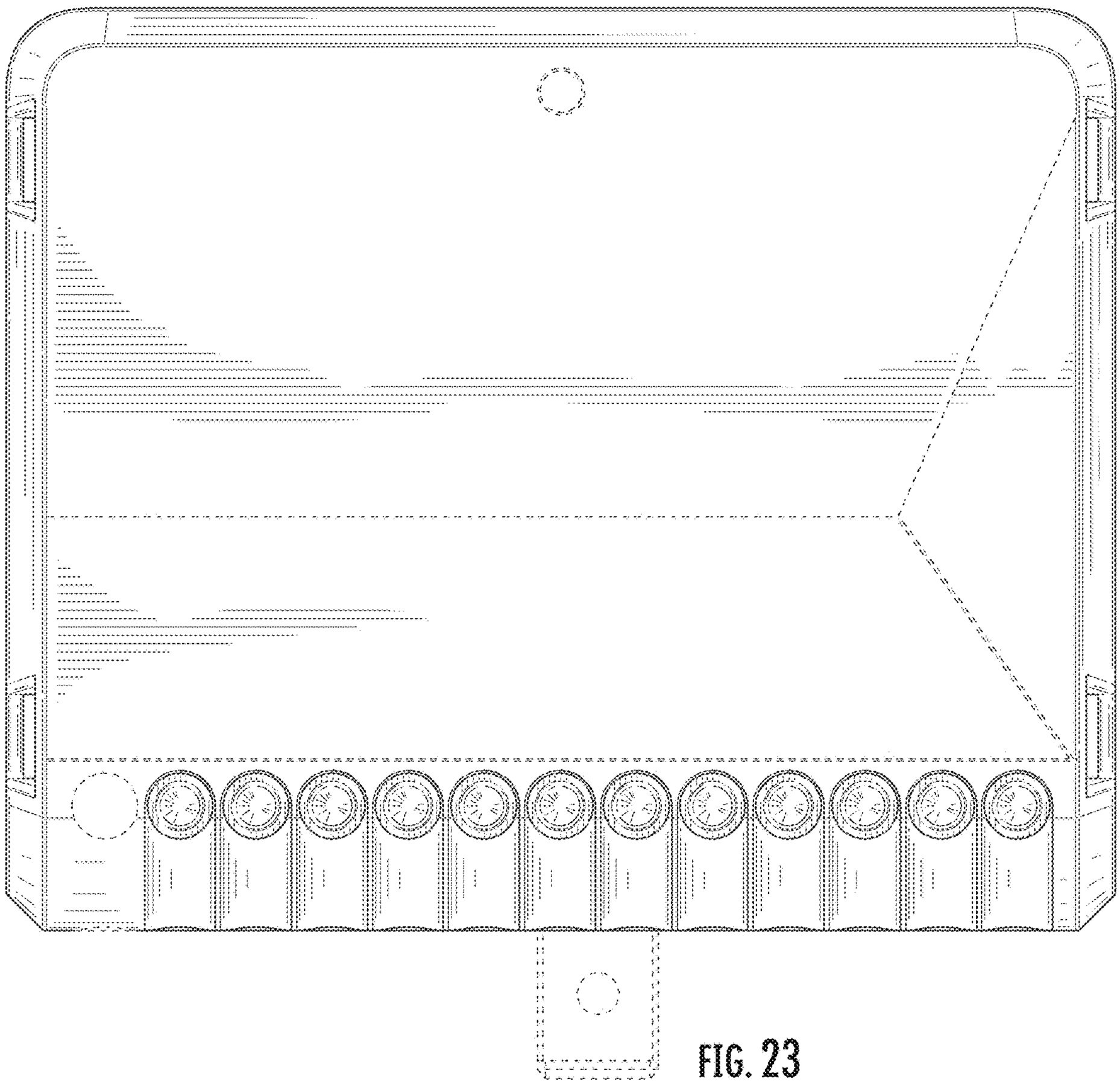

FIG. 23

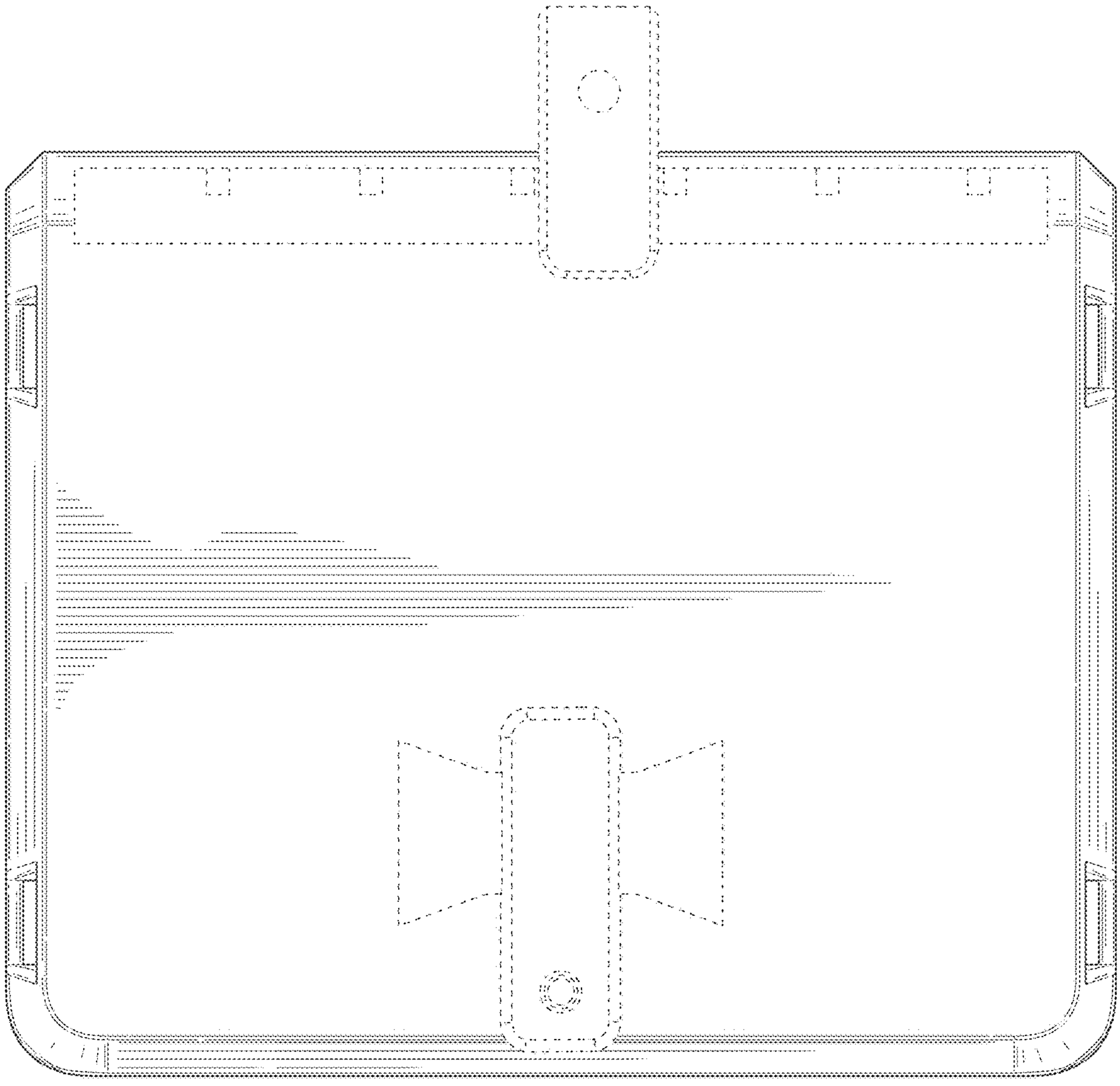

FIG. 24

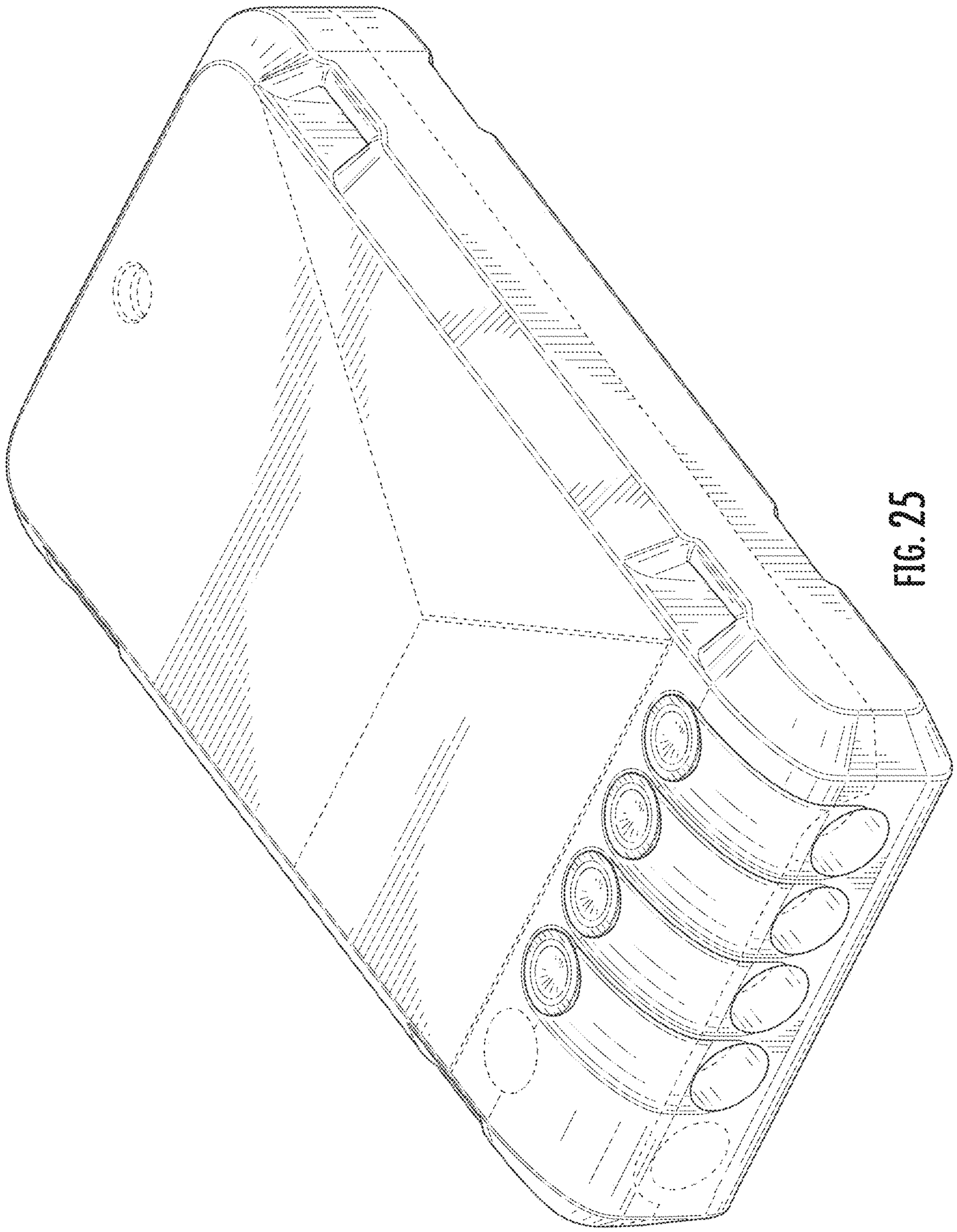

FIG. 25

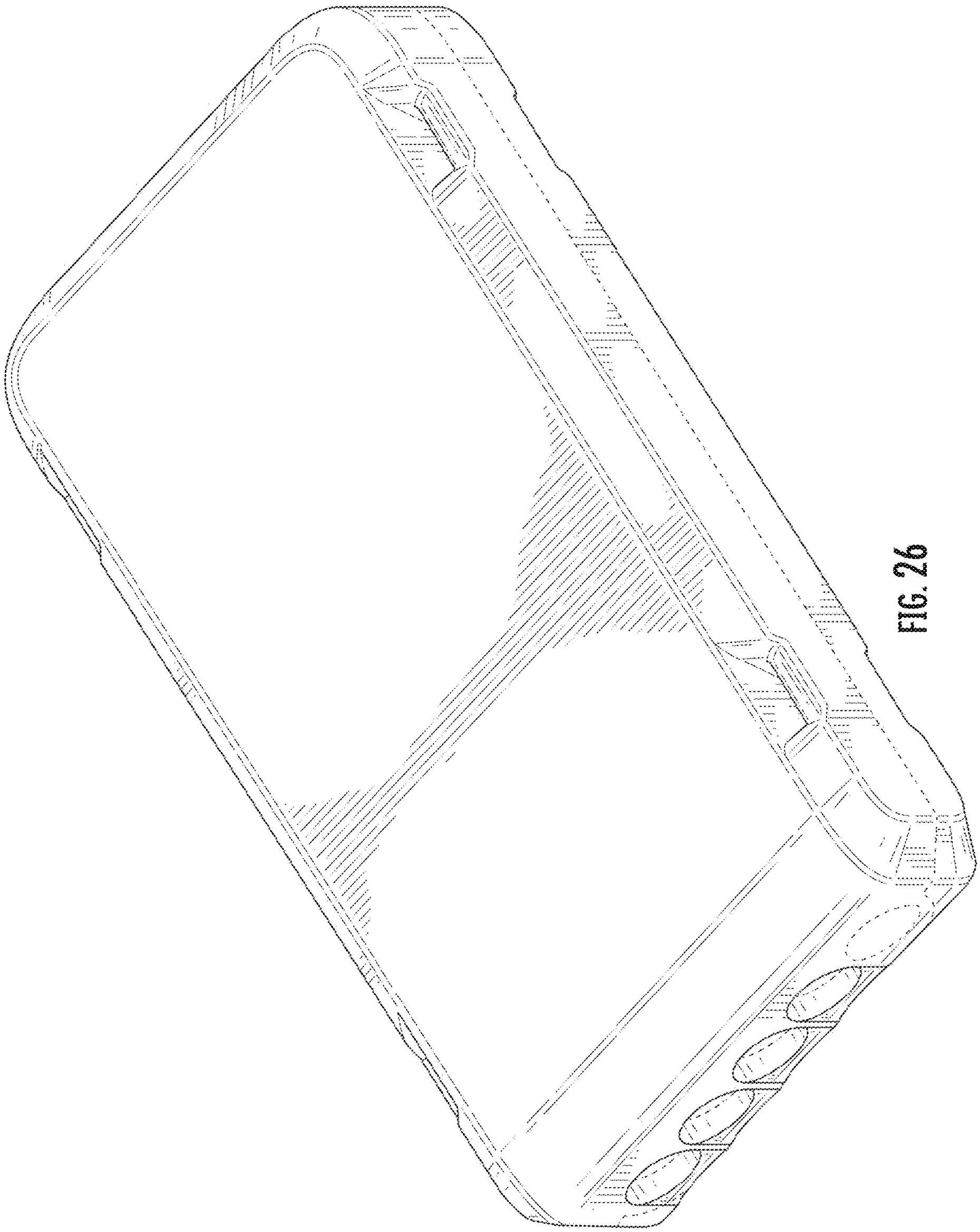

FIG. 26

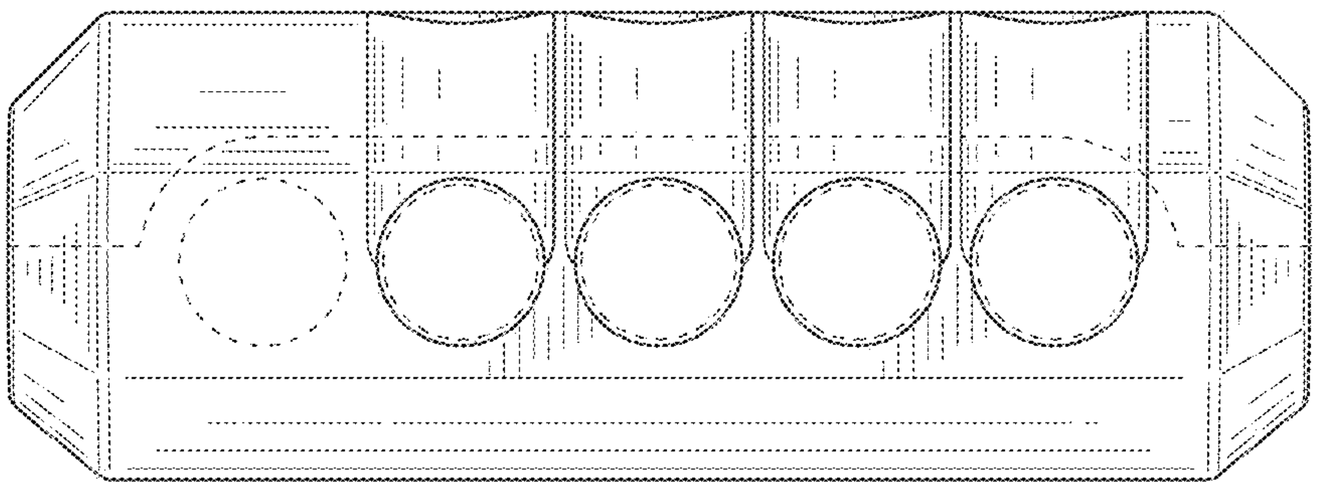

FIG. 27

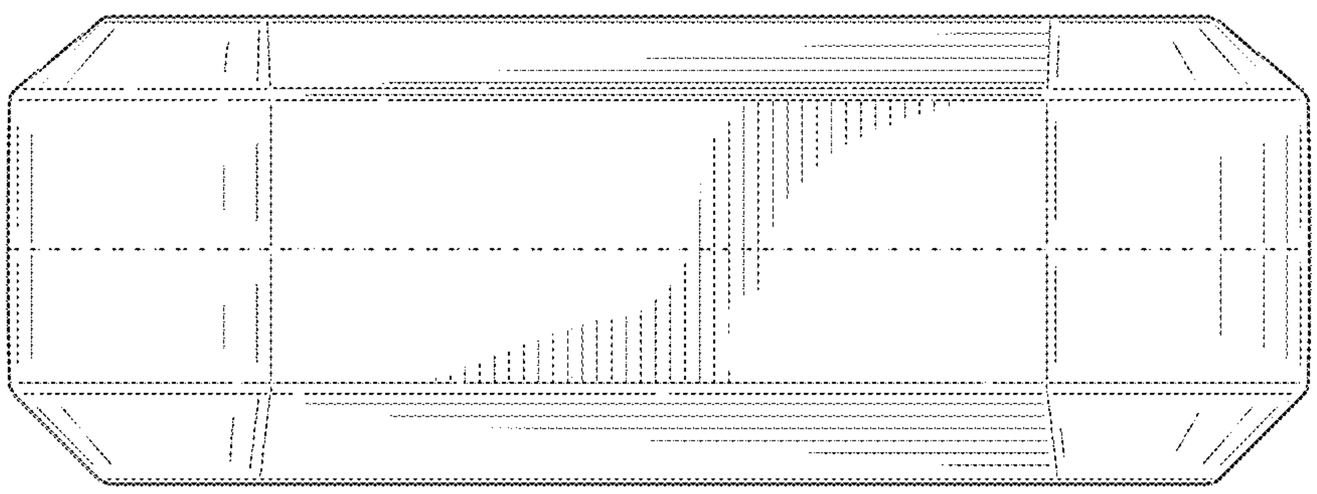

FIG. 28

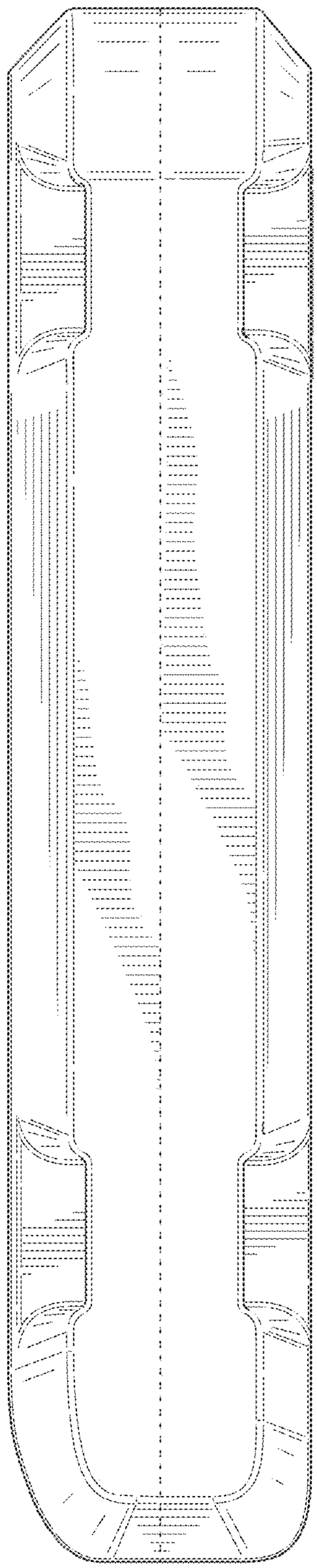

FIG. 29

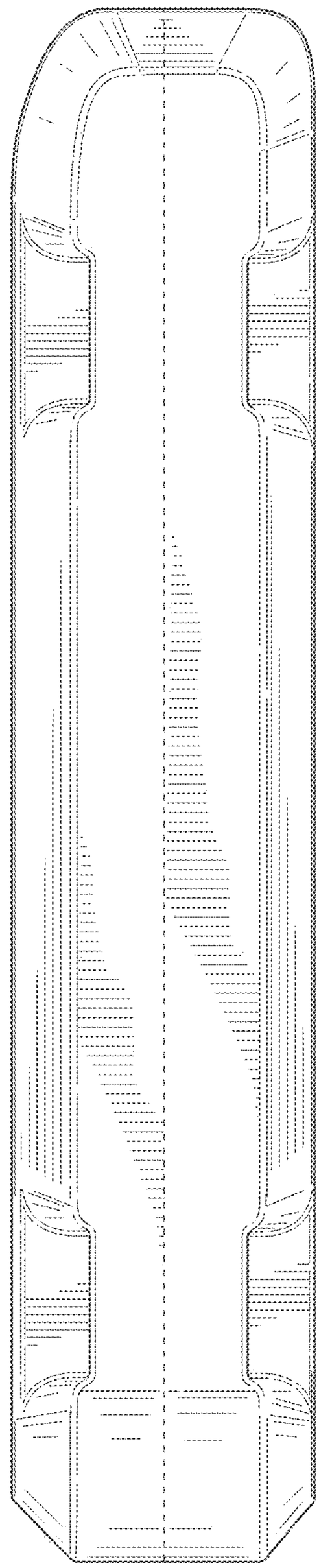

FIG. 30

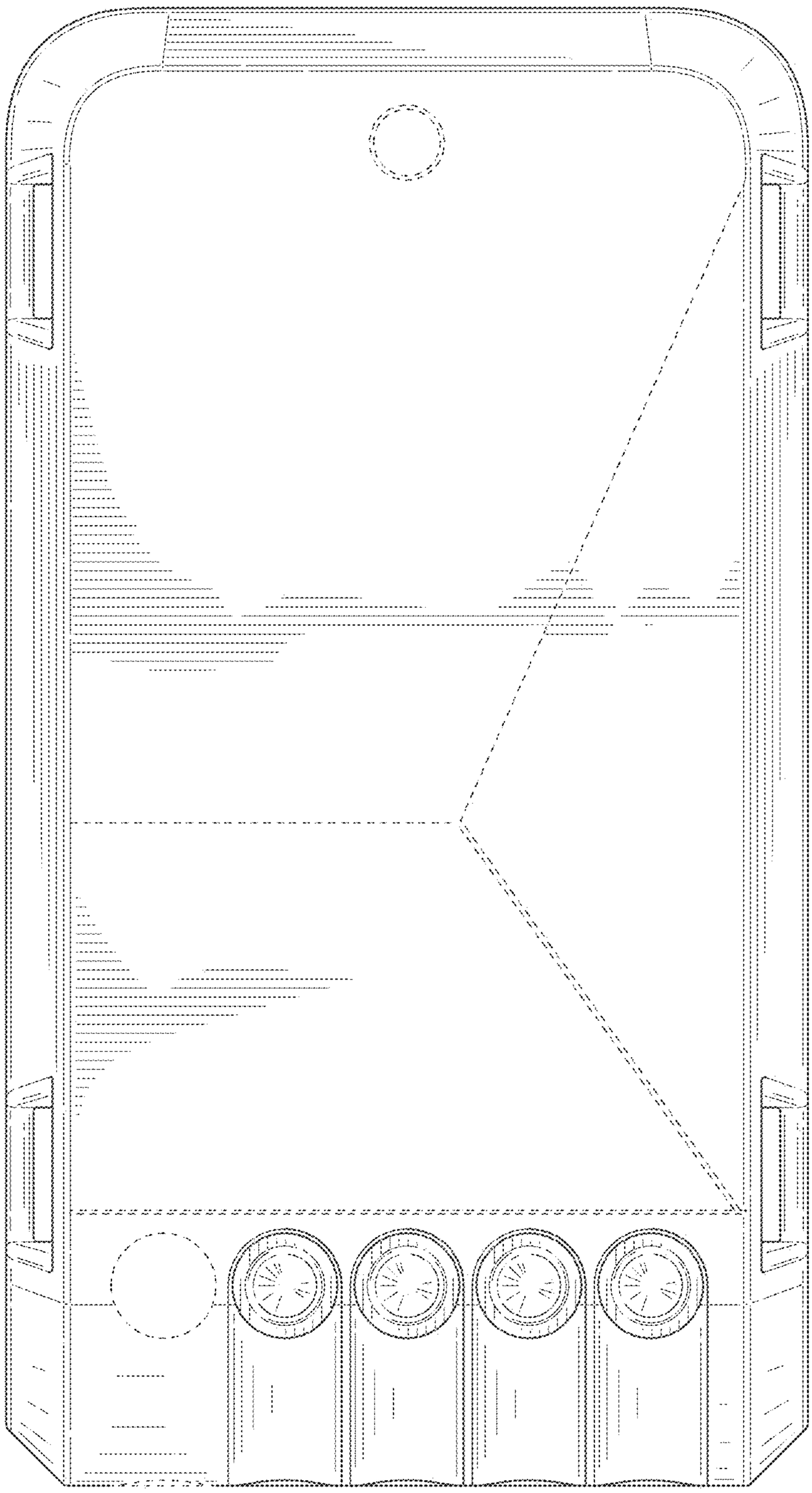

FIG. 31

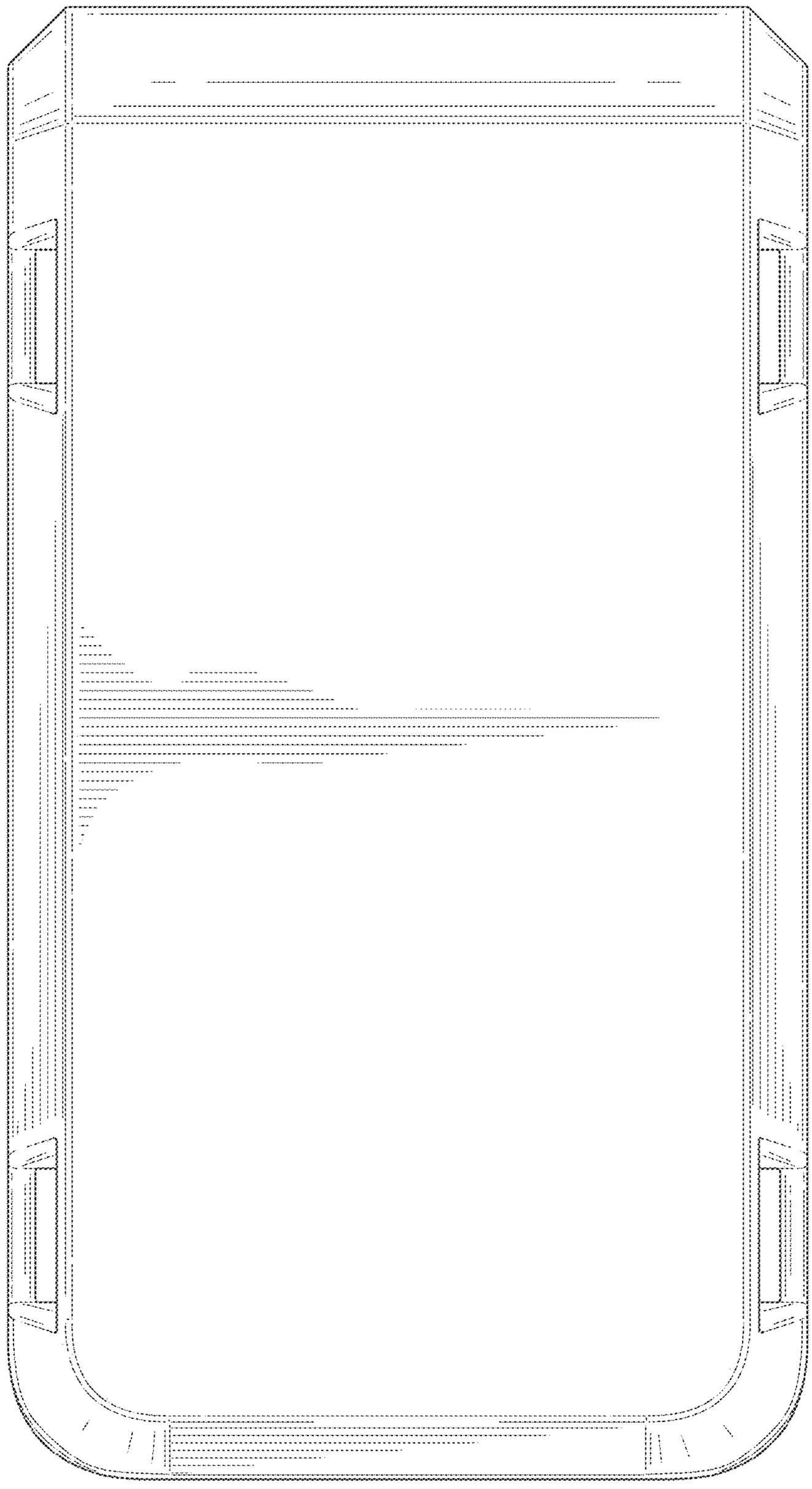

FIG. 32

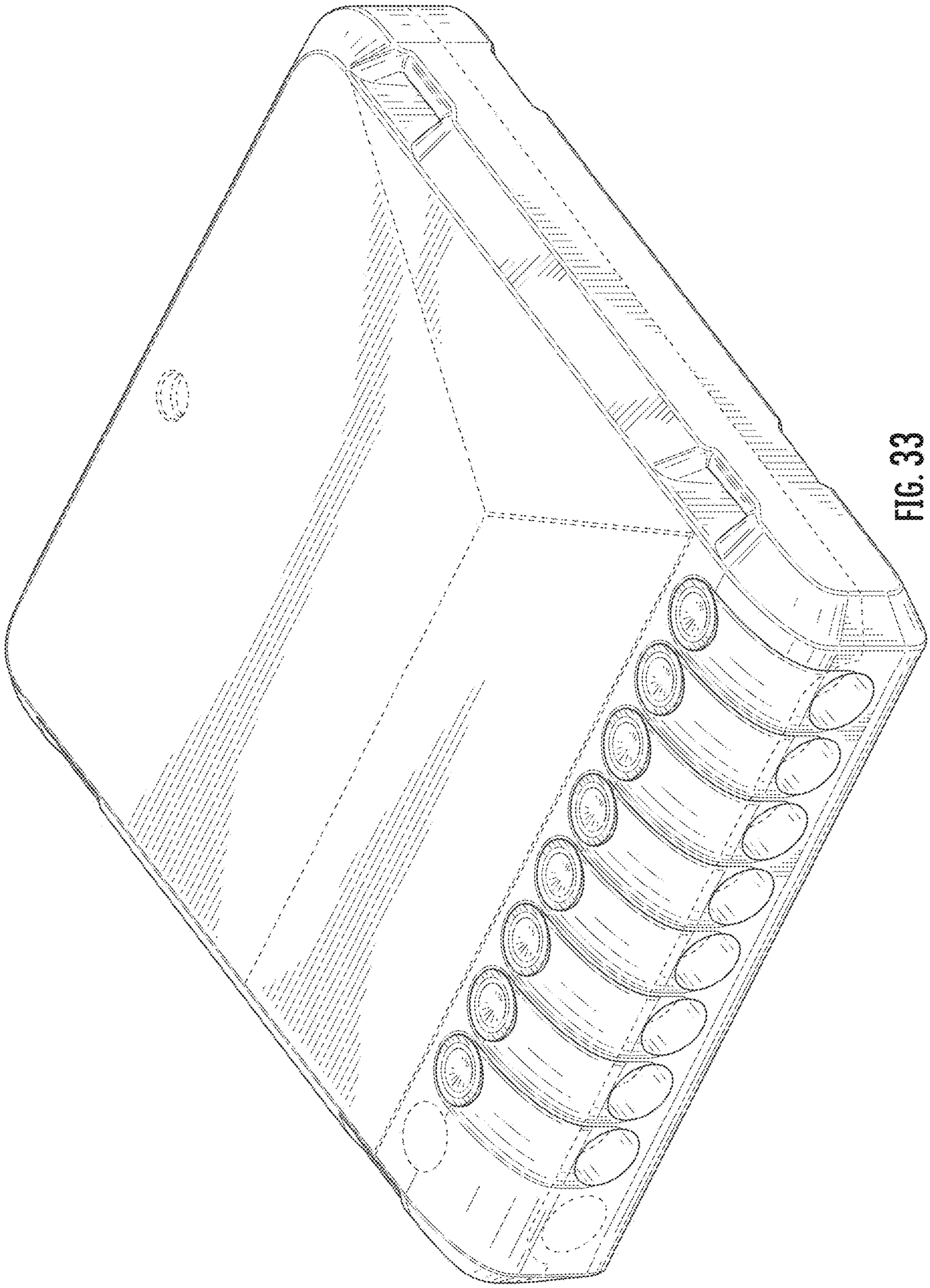

FIG. 33

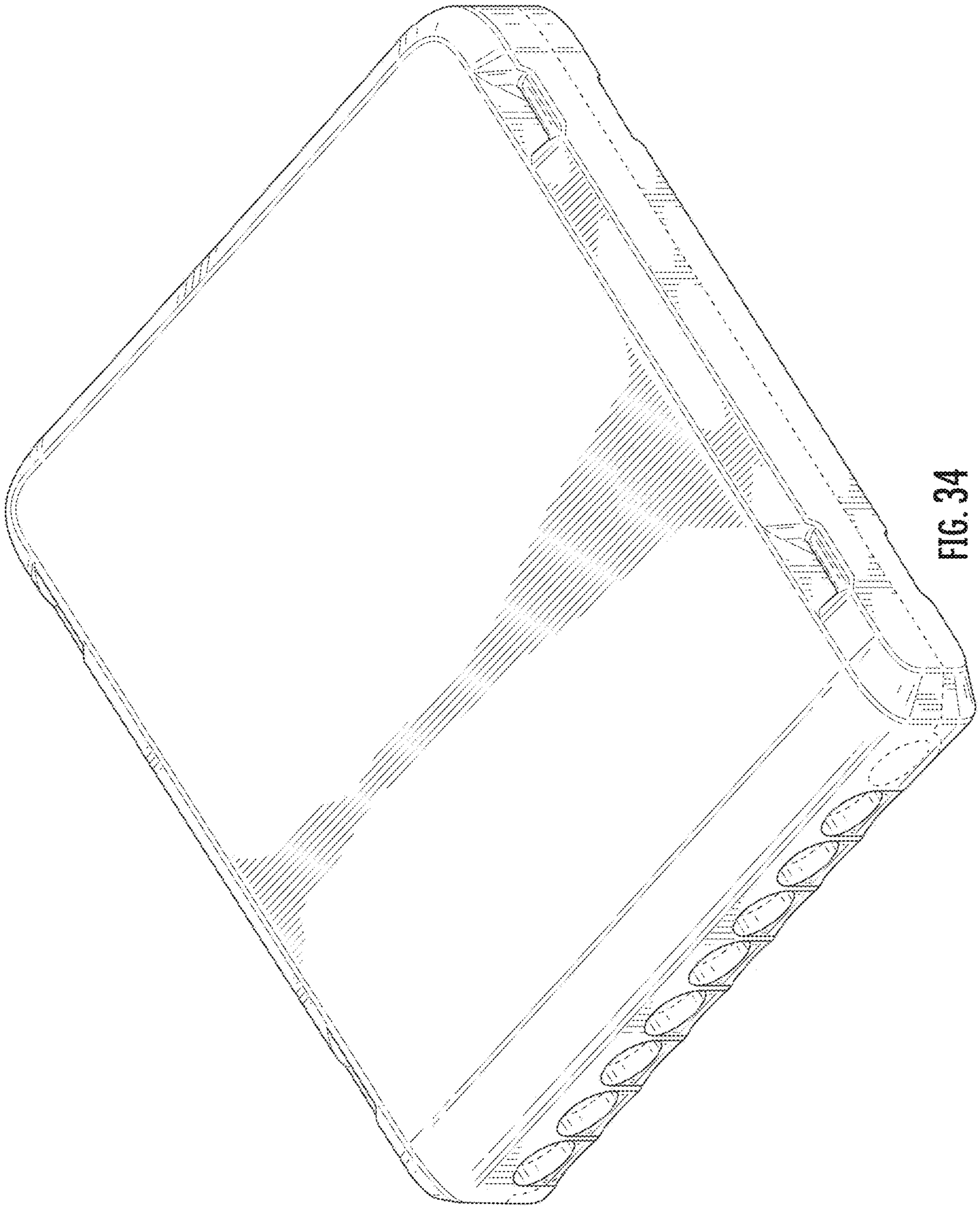

FIG. 34

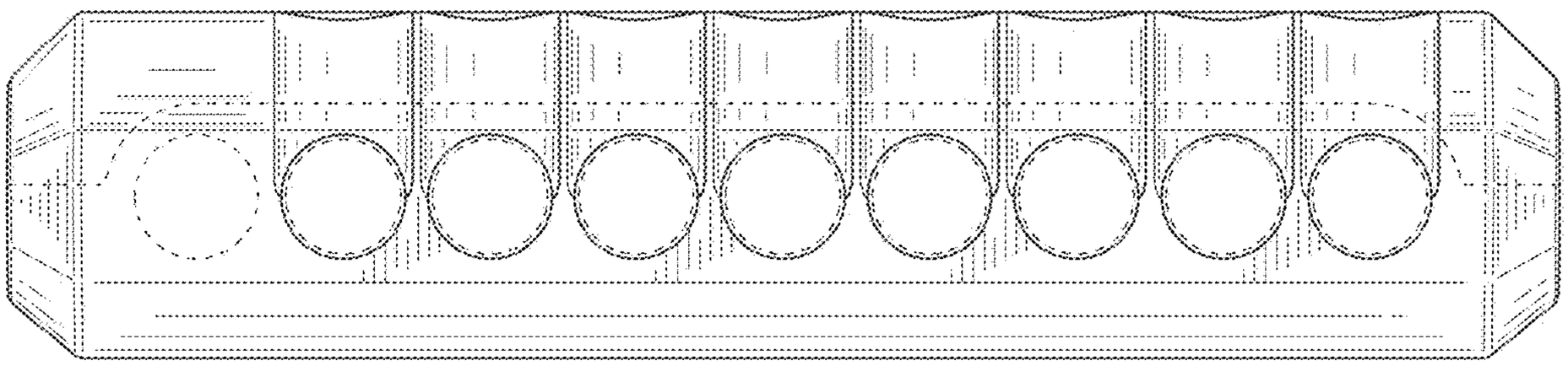

FIG. 35

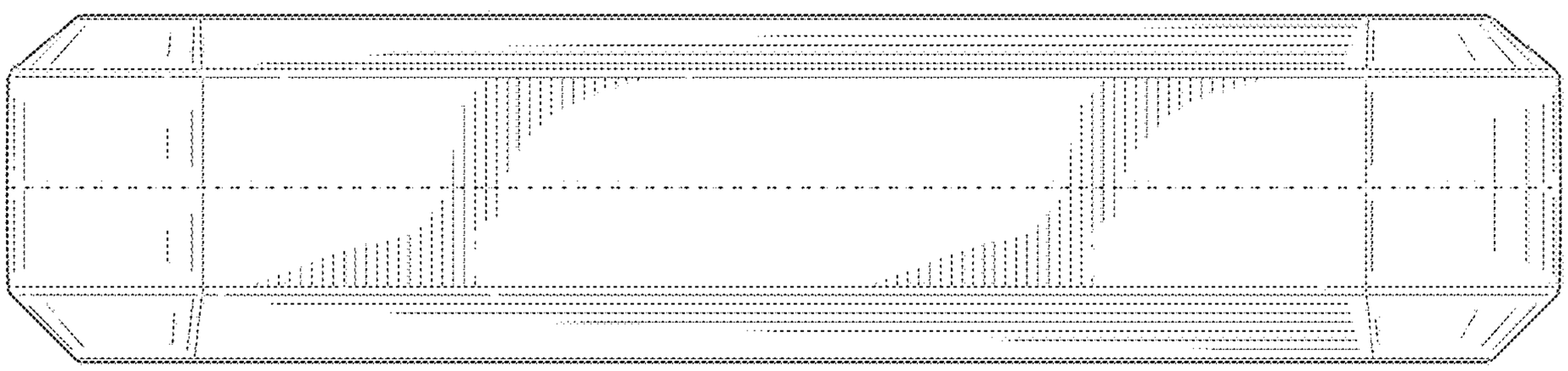

FIG. 36

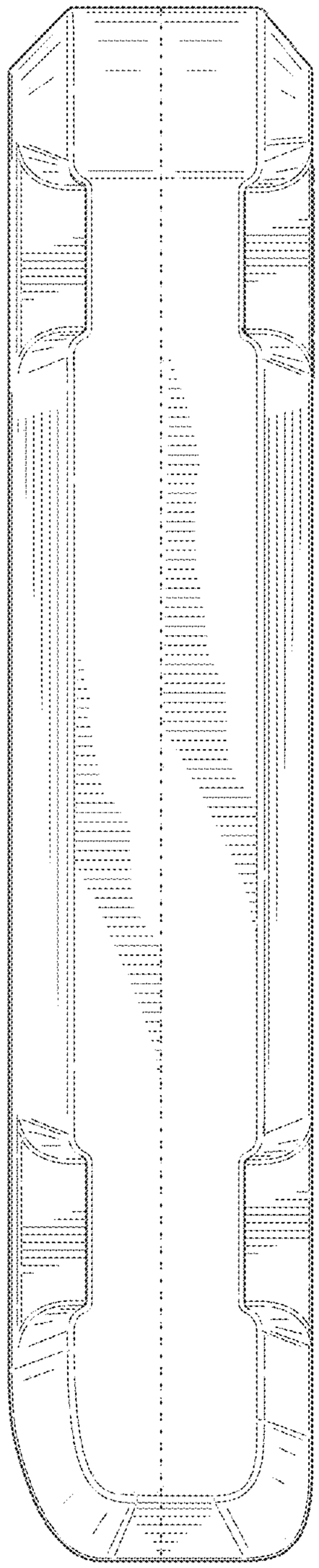

FIG. 37

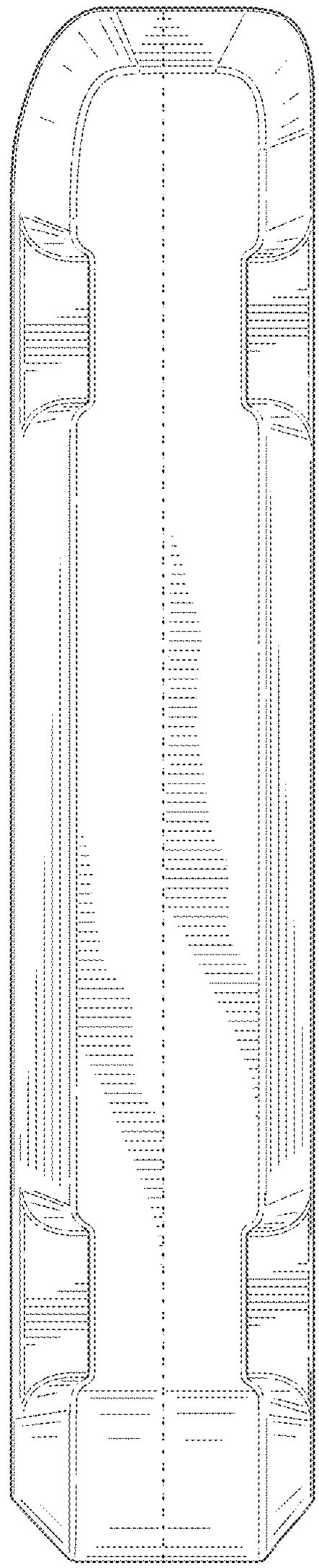

FIG. 38

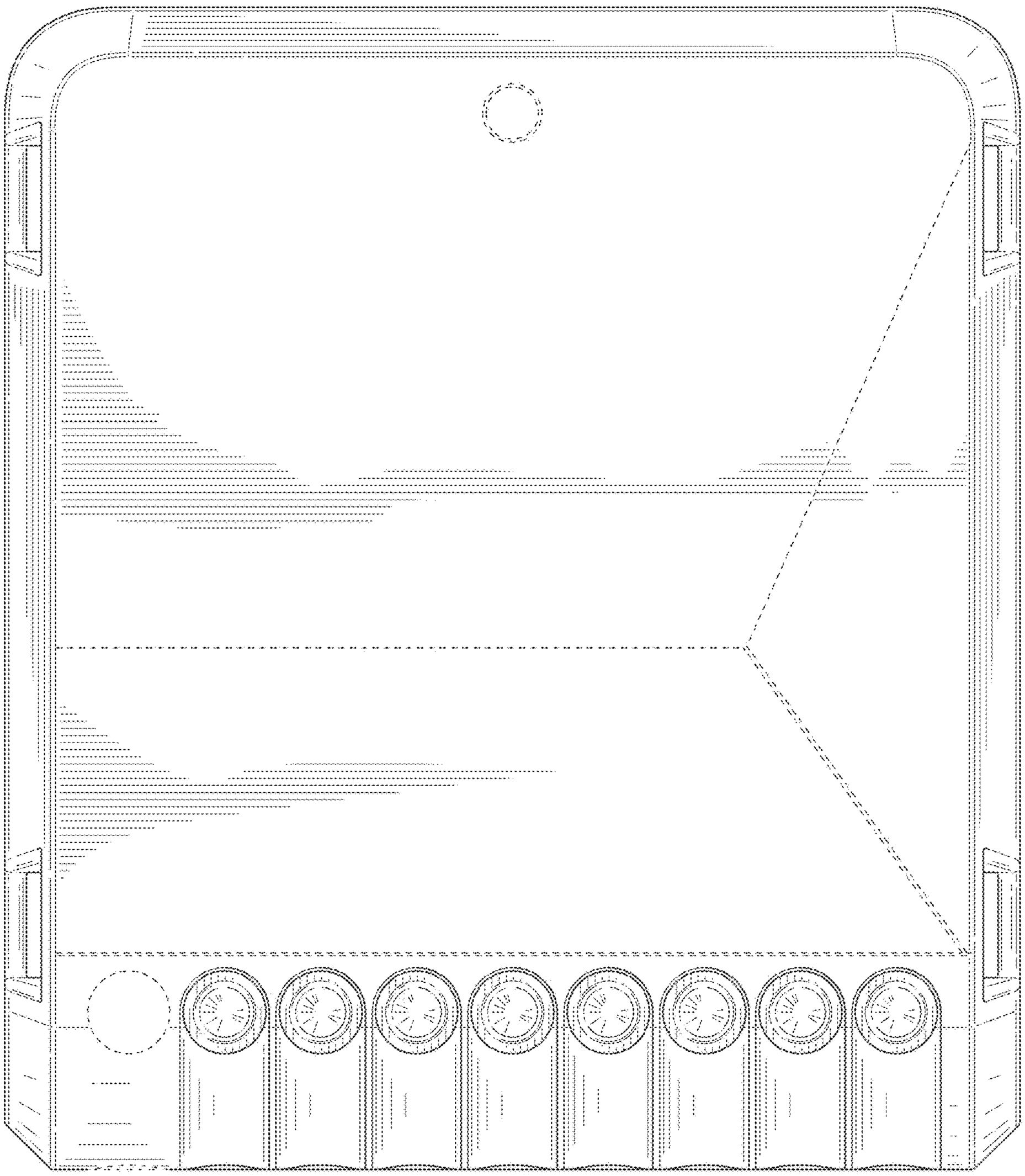

FIG. 39

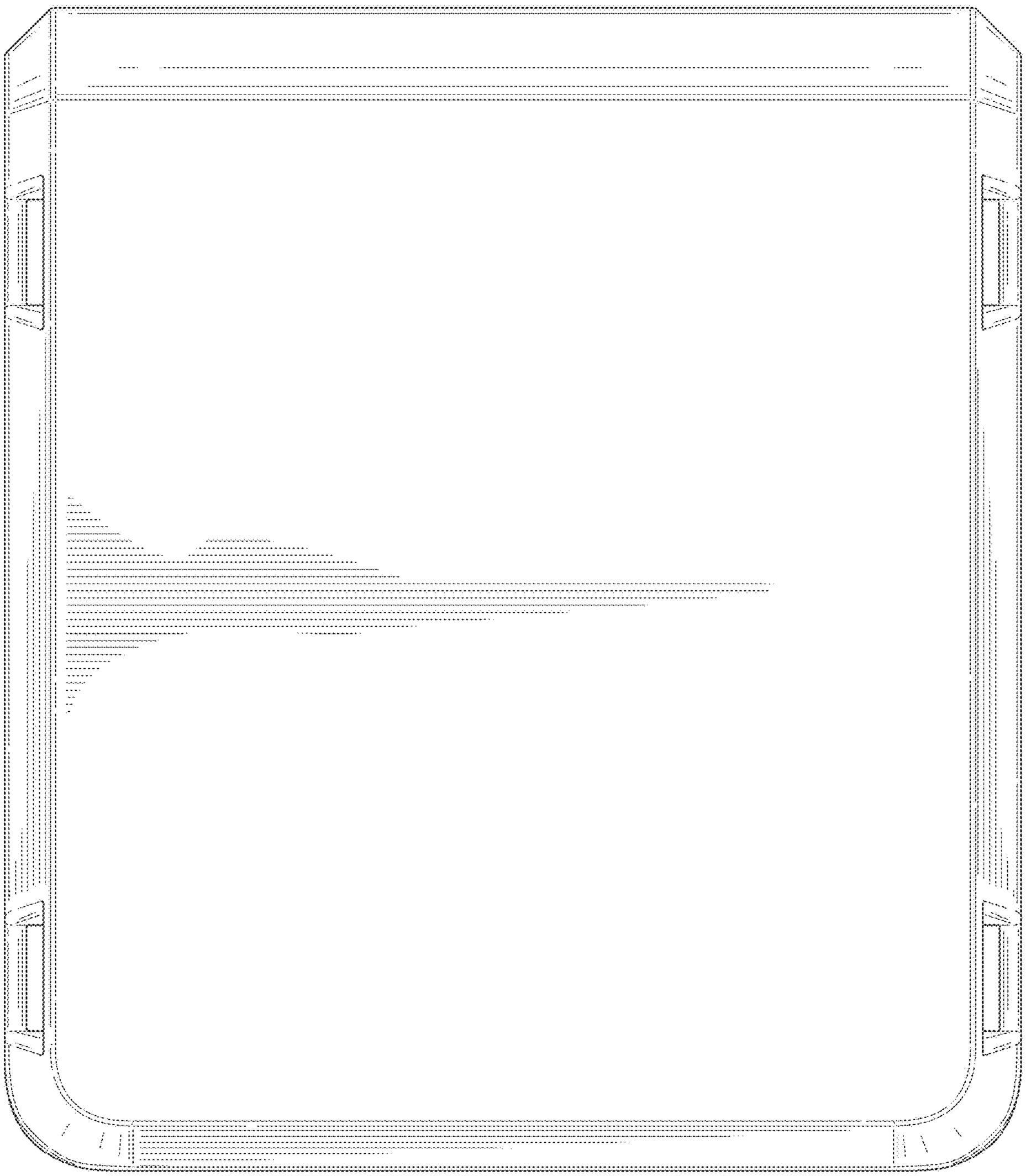

FIG. 40

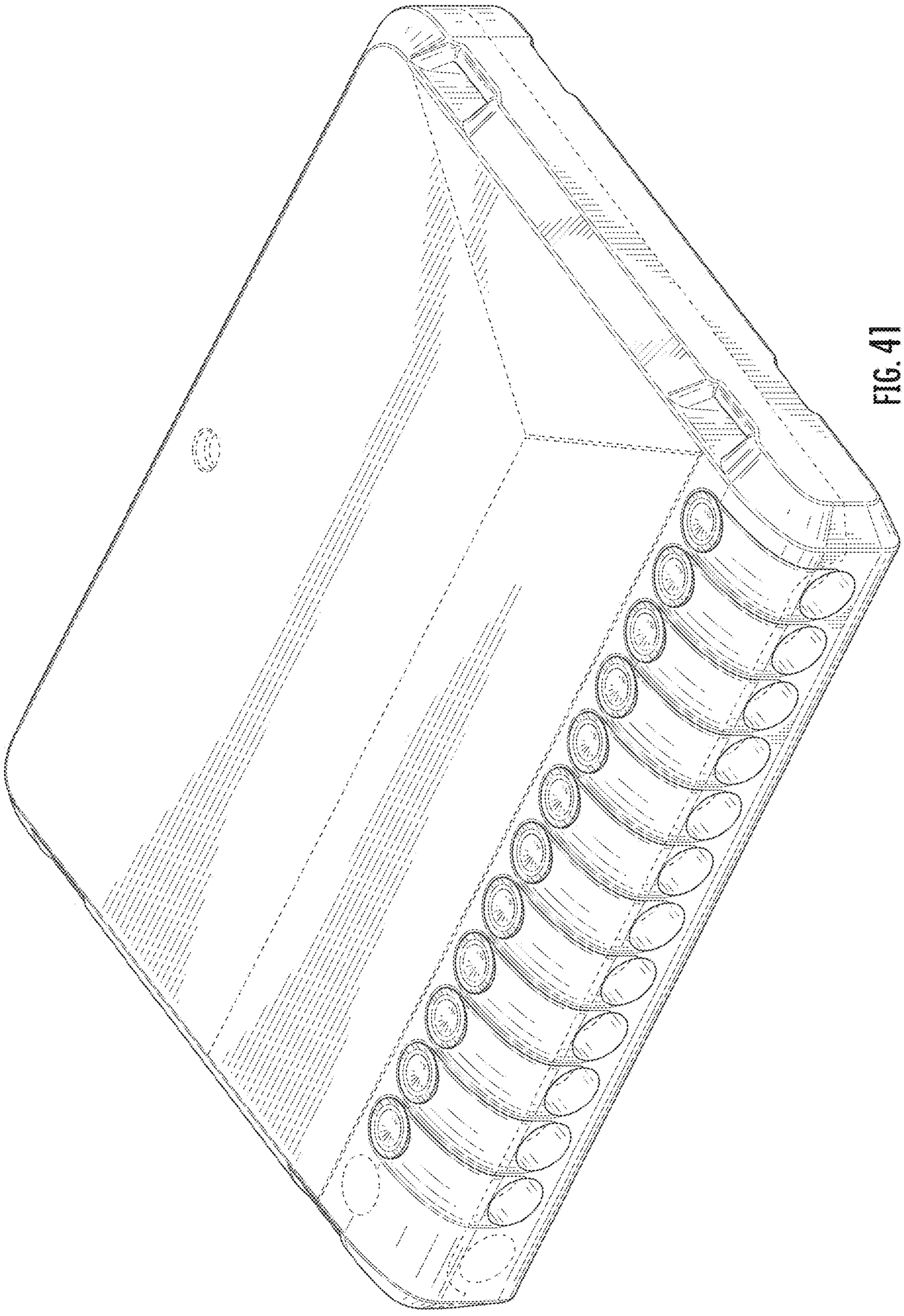

FIG. 41

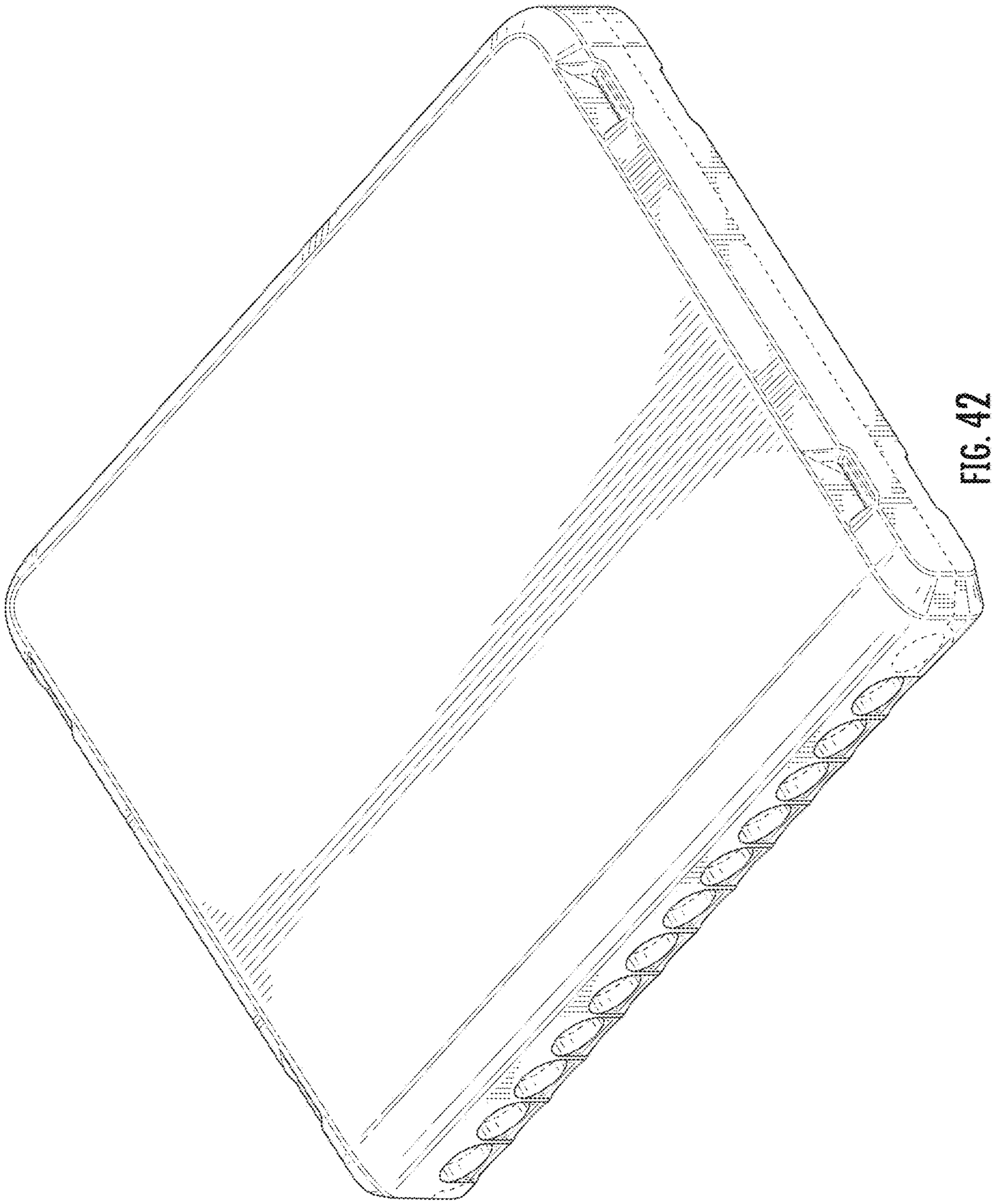

FIG. 42

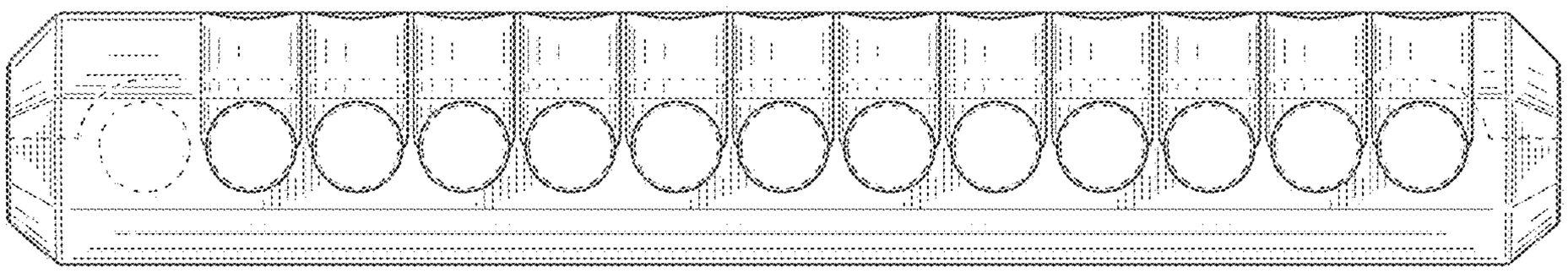

FIG. 43

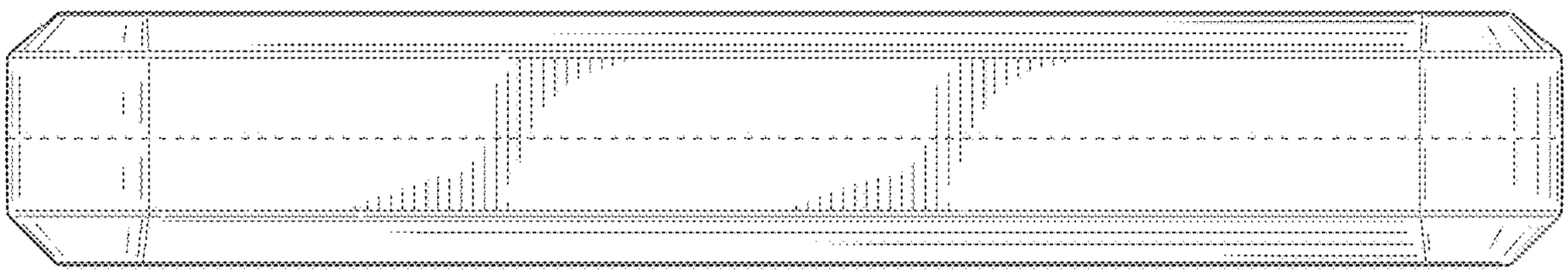

FIG. 44

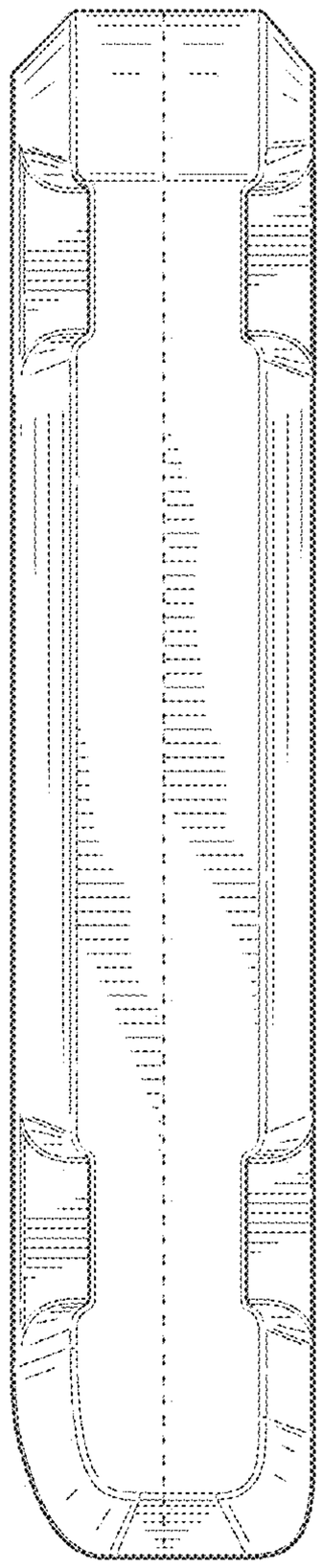

FIG. 45

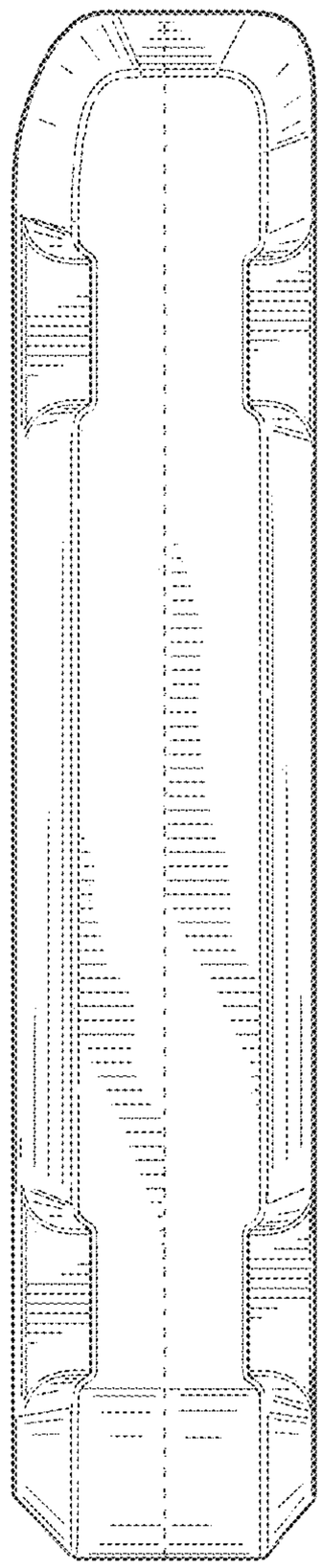

FIG. 46

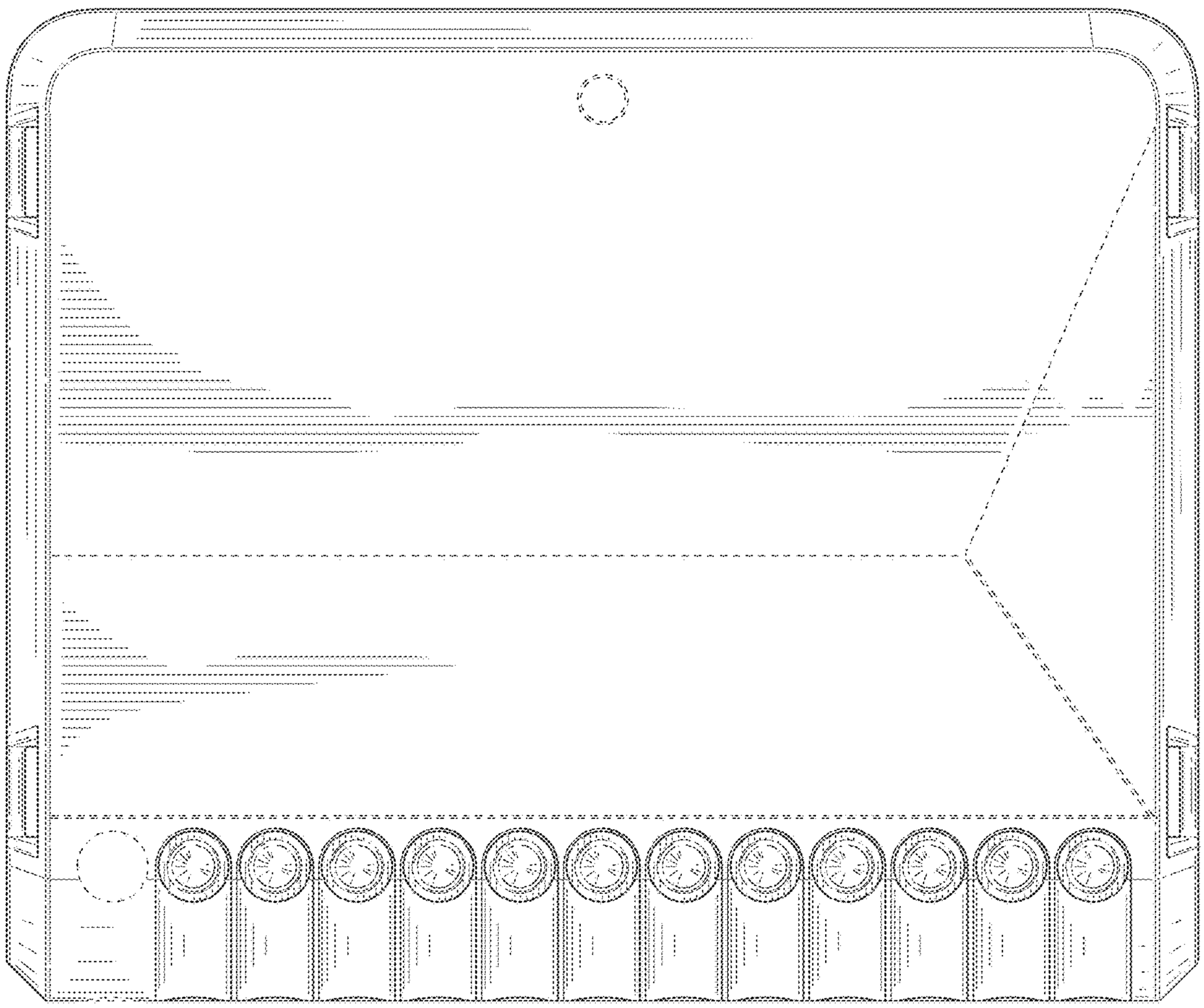

FIG. 47

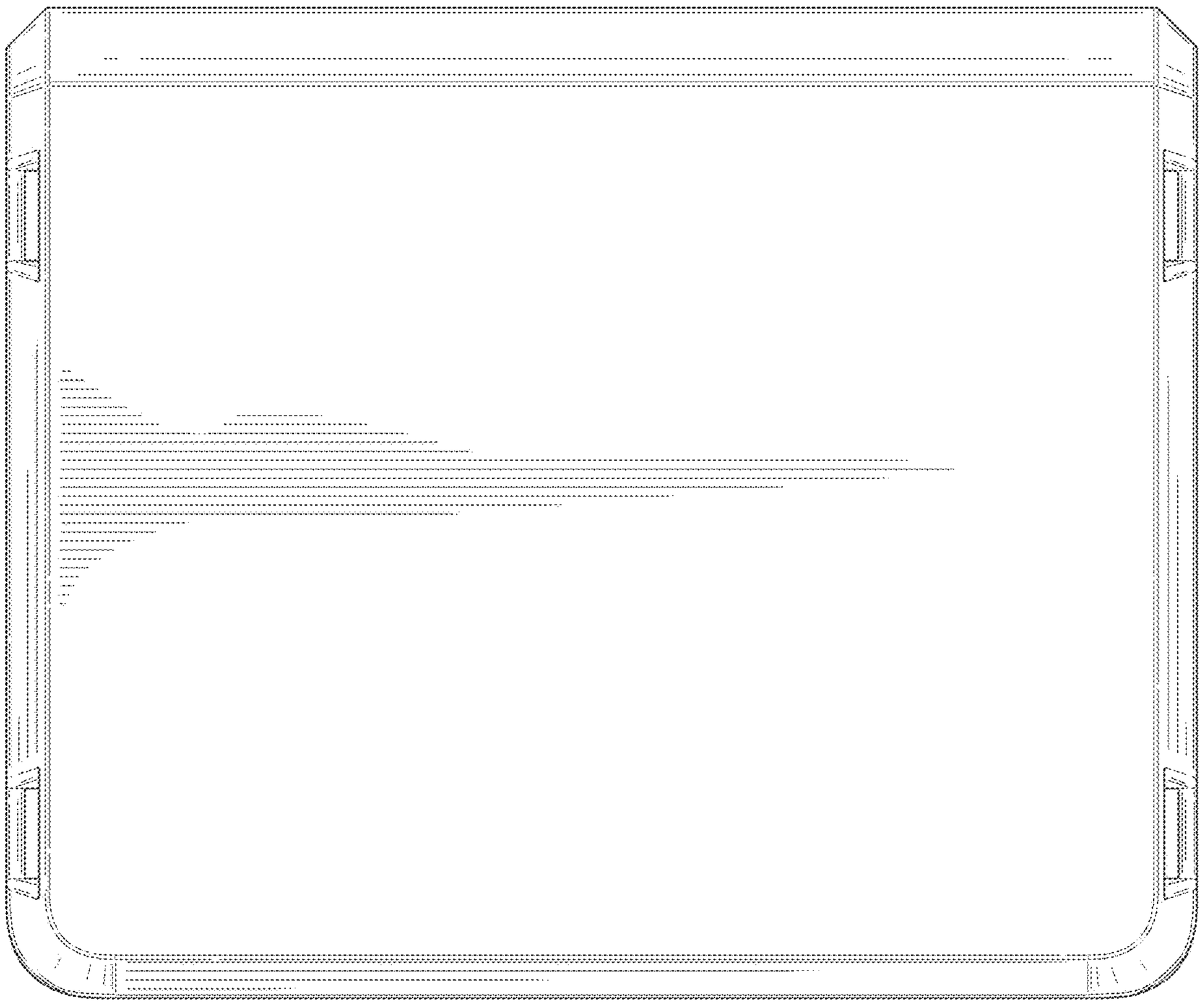

FIG. 48

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : D975,023 S
APPLICATION NO. : 29/770223
DATED : January 10, 2023
INVENTOR(S) : Joel Christopher Rosson et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page

On the page 2, in Column 2, under item (56) "Other Publications", Line 14, delete "dumber" and insert -- jumber --.

Signed and Sealed this
Eighteenth Day of April, 2023

Katherine Kelly Vidal

Katherine Kelly Vidal
Director of the United States Patent and Trademark Office