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(12) **United States Design Patent** (10) **Patent No.:** **US D968,639 S**
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(54) **TEST CHIP**
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(**) Term: **15 Years**

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Primary Examiner — Anhdao Doan

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(30) **Foreign Application Priority Data**

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(51) **LOC (13) Cl.** **24-01**

(57) **CLAIM**

(52) **U.S. Cl.**
USPC **D24/216**; D24/225

The ornamental design for a test chip, as shown and described.

(58) **Field of Classification Search**
USPC D24/107, 186, 216, 232, 233, 234, 121,
D24/122, 223, 224, 225, 227, 231, 119,
D24/133; D10/81
CPC B01L 3/5023; B01L 3/523; B01L 3/565;
B01L 2300/0809; B01L 3/50229; B01L
2300/0816; B01L 2300/0819; B01L
2300/0822; B01L 2300/0825; A61B
10/0038; A61B 10/0216; G01N 2001/028
See application file for complete search history.

DESCRIPTION

FIG. 1 is a front view of a test chip;
FIG. 2 is a top view thereof;
FIG. 3 is a bottom view thereof;
FIG. 4 is a right side view thereof;
FIG. 5 is a left side view thereof; and,
FIG. 6 is a cross-sectional view taken along line 6-6 shown in FIG. 5.

The rear view is omitted since it is symmetrical to the front view. The broken lines represent portions of the test chip and form no part of the claimed design.

(56) **References Cited**

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1 Claim, 6 Drawing Sheets

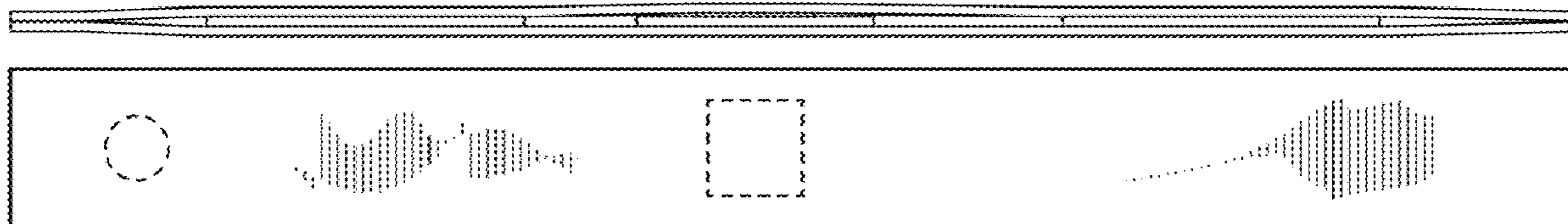


Fig. 1

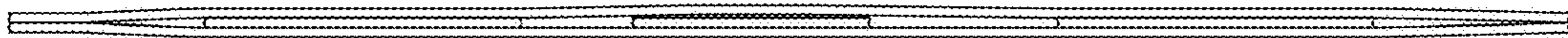


Fig. 2

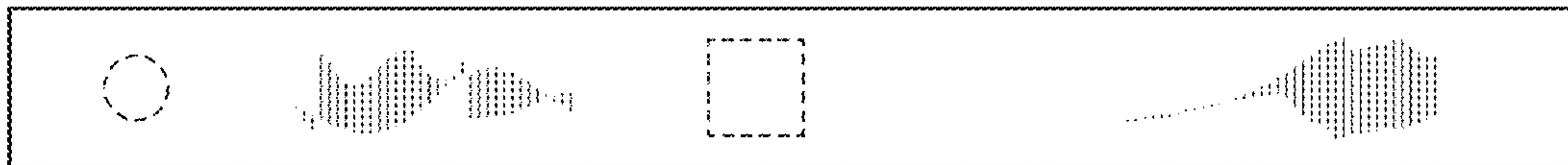


Fig. 3

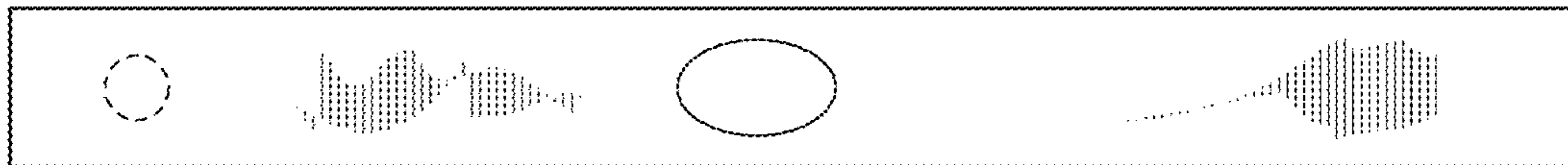


Fig. 4

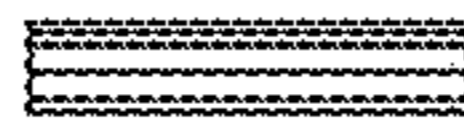


Fig. 5

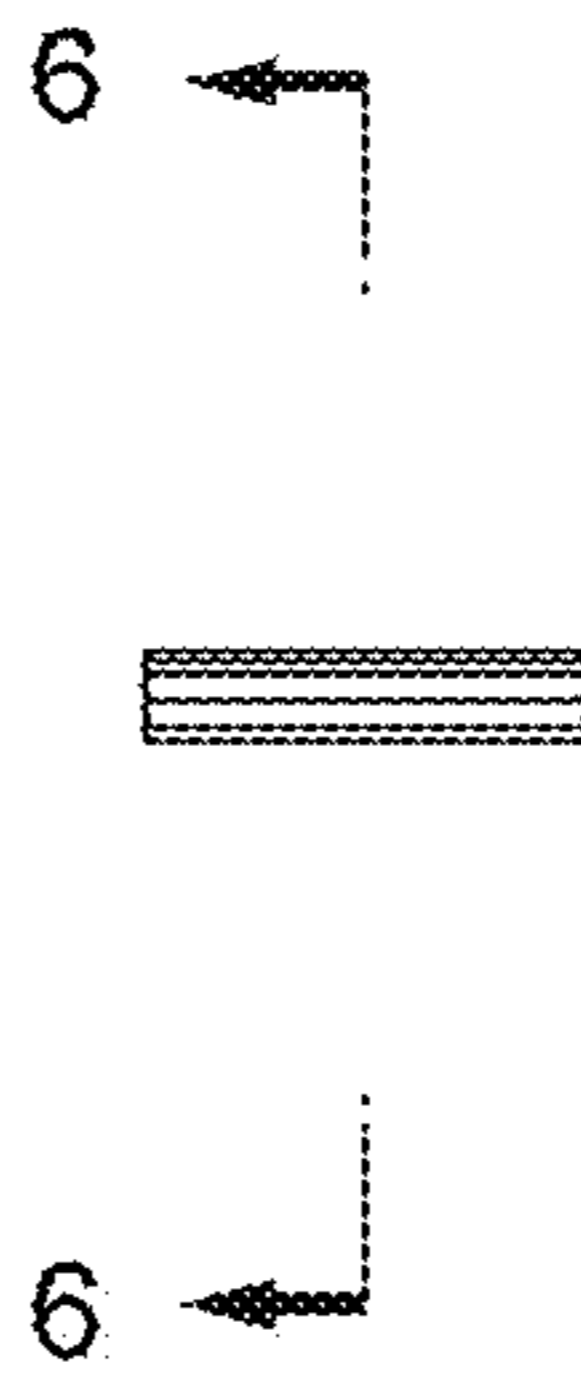


Fig. 6

