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(12) **United States Design Patent**
Crisp et al.

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(54) **INFORMATION HANDLING SYSTEM STORAGE DEVICE**

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(**) Term: **15 Years**

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(51) **LOC (13) Cl.** **14-02**

(52) **U.S. Cl.**

USPC **D14/436; D14/435**

(58) **Field of Classification Search**

USPC 439/946, 948, 159, 77; 174/250, 260, 174/52.1, 52.2, 16.1, 16.3; 710/300-303; 438/121; 376/191; 165/185, 80.2; 711/104, 164; 365/191, 51, 200, 201, 365/230.1, 230.03, 233.11, 212.63; 714/733, 744, 763, 718, 723, 766, 6.13, 714/773; 360/69, 97.02, 53; 361/679.33-679.45, 732, 737, 752, 736, 361/728, 796, 797, 729, 730, 725-727, 361/749, 760, 784, 789, 771, 792, 764, 361/765, 704, 715, 710, 711, 719, 767,

(Continued)

(56) **References Cited**

U.S. PATENT DOCUMENTS

D376,591 S * 12/1996 Drucker D14/446
6,008,994 A * 12/1999 Bates H05K 5/0269
361/728

(Continued)

OTHER PUBLICATIONS

Hewlett Packard, "HPE Dual 240GB SATA 6G Mixed Use M.2—UFF to SFF SCM 3yr Wty Digitally Signed Firmware SSD," downloaded from <https://www.hpe.com/be/en/product-catalog/servers/server-solid-state-drives/pip.hpe-server-mixed-use-mu-solid-state-drives.1010930696.html>, Nov. 8, 2019, 6 pages.

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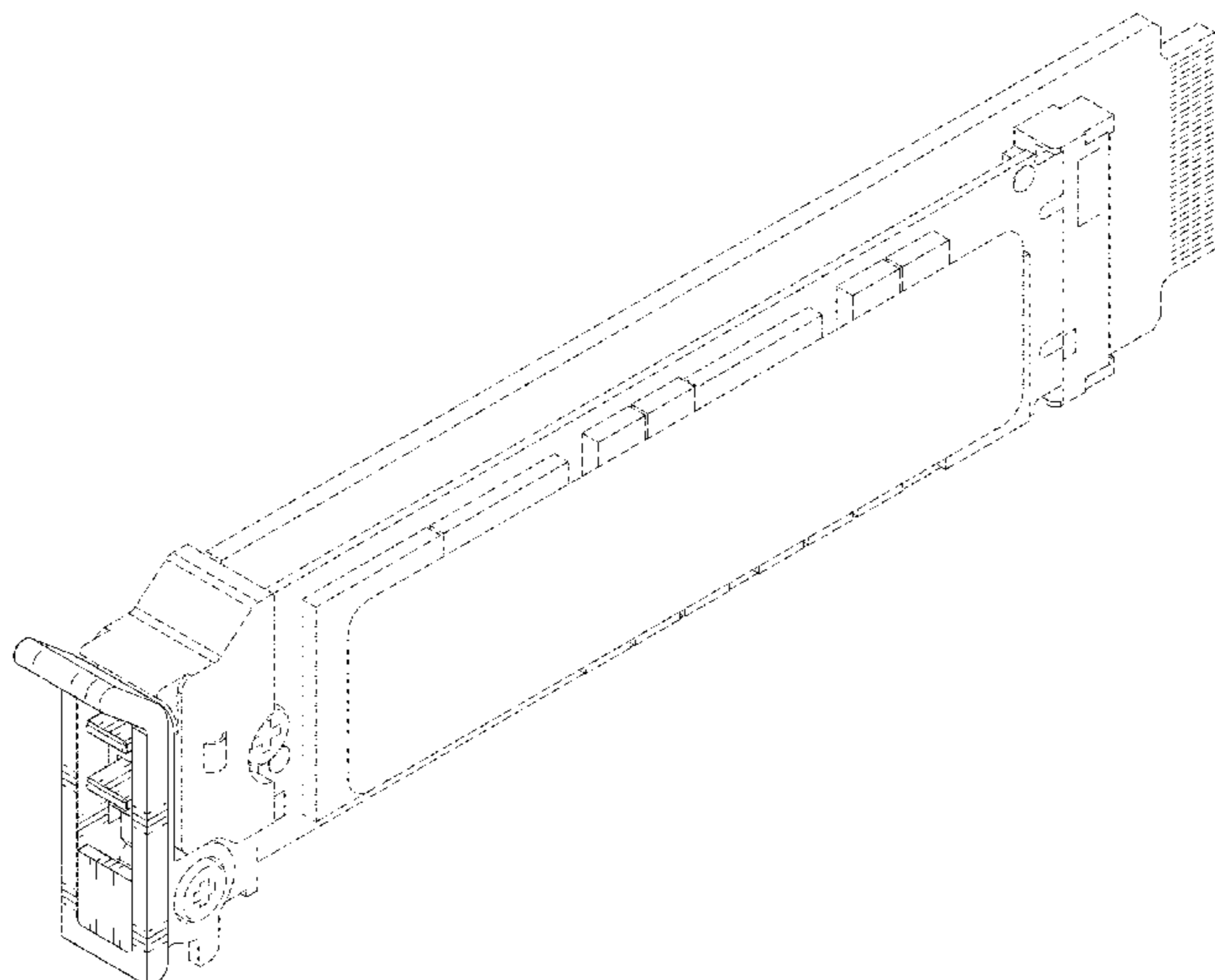
(57) **CLAIM**

We claim the ornamental design for an information handling system storage device, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of an information handling system storage device comprising the present invention; FIG. 2 is a front elevation view thereof; FIG. 3 is a rear elevation view thereof; FIG. 4 is a right side view thereof; FIG. 5 is a left side view thereof; FIG. 6 is a top view thereof; FIG. 7 is a bottom view thereof; FIG. 8 is a front perspective view thereof in an open position; and, FIG. 9 is a front elevation view thereof in an open position. Broken lines shown in the drawings illustrate portions of the information handling system storage device, and form no part of the claimed design.

1 Claim, 5 Drawing Sheets



(58) **Field of Classification Search**

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CPC G06K 19/06196; G06K 19/072; G06K
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 19/0726; G06K 19/0727; G06K 19/0728;
 G06K 19/073; G06K 19/07309; G06K
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 19/07724; G06K 19/07726; G06K
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 G06F 21/77–80; G06F 21/85–88; G06F
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 G06F 13/00; G06Q 20/3229; G06Q
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 H05K 5/026; H05K 5/0256; H05K
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 H05K 5/03; H05K 5/04; H05K 5/06;
 H05K 5/061; H05K 5/062; H05K 5/063;
 H05K 5/064; H05K 5/065; H05K 5/066;
 H05K 5/067; H05K 5/068; H05K 5/069;
 H05K 7/00; H05K 7/005; H05K 7/02;
 H05K 7/023; H05K 7/026; H05K 7/04;
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 7/1485–1488; H05K 2201/09745; H05K
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 2201/056; H05K 2201/1056; H05K
 2201/10734; H05K 2201/2018; H05K

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 2924/00; H01L 25/0652; H01L 25/105

See application file for complete search history.

(56)

References Cited

U.S. PATENT DOCUMENTS

6,400,931	B1 *	6/2002	Inkinen	H01Q 1/2275 455/575.1
6,565,163	B2 *	5/2003	Behl	G11B 33/148
7,265,989	B2 *	9/2007	Son	H05K 5/0269 439/945
7,321,489	B2 *	1/2008	McAlister	G06F 1/187 361/679.33
D575,787	S *	8/2008	Su	D14/436
D588,137	S *	3/2009	Lawrence	D14/444
D593,101	S *	5/2009	Alfonso	D14/444
D593,563	S *	6/2009	Alfonso	D14/444
D643,434	S *	8/2011	Grady, IV	D14/442
D660,841	S *	5/2012	Alfonso	D14/444
D673,570	S *	1/2013	Wallace	D14/444
D673,922	S *	1/2013	Moriai	D13/182
D699,718	S *	2/2014	Kuehn	D14/313
D701,505	S	3/2014	Crisp	
D735,185	S	7/2015	Terwilliger	
D735,209	S	7/2015	Crisp	
D783,623	S	4/2017	Crisp	
9,775,262	B1 *	9/2017	Hsieh	H05K 5/0208
D826,944	S *	8/2018	Lim	D14/435
D834,025	S *	11/2018	Lim	D14/435
D850,448	S *	6/2019	Lewis	D14/432
D862,475	S *	10/2019	Lim	D14/435
D868,787	S *	12/2019	Magi	D14/436
D880,472	S *	4/2020	Dearborn	D14/313
D888,675	S *	6/2020	Lim	D13/184
D888,676	S *	6/2020	Lim	D13/184
D928,161	S *	8/2021	Fan	D13/179
2006/0261449	A1 *	11/2006	Rapport	H05K 1/147 257/666
2007/0223198	A1 *	9/2007	Lai	H01L 23/3672 361/720
2008/0205026	A1 *	8/2008	Gallarelli	G11B 33/12 361/818
2019/0079565	A1 *	3/2019	Adrian	G06F 1/187

OTHER PUBLICATIONS

Intel, "Intel SSD DC P4500 Series," downloaded from <https://www.gitcom.eu/intel-ssd-dc-p4500-series/>, Nov. 8, 2019, 5 pages.

* cited by examiner

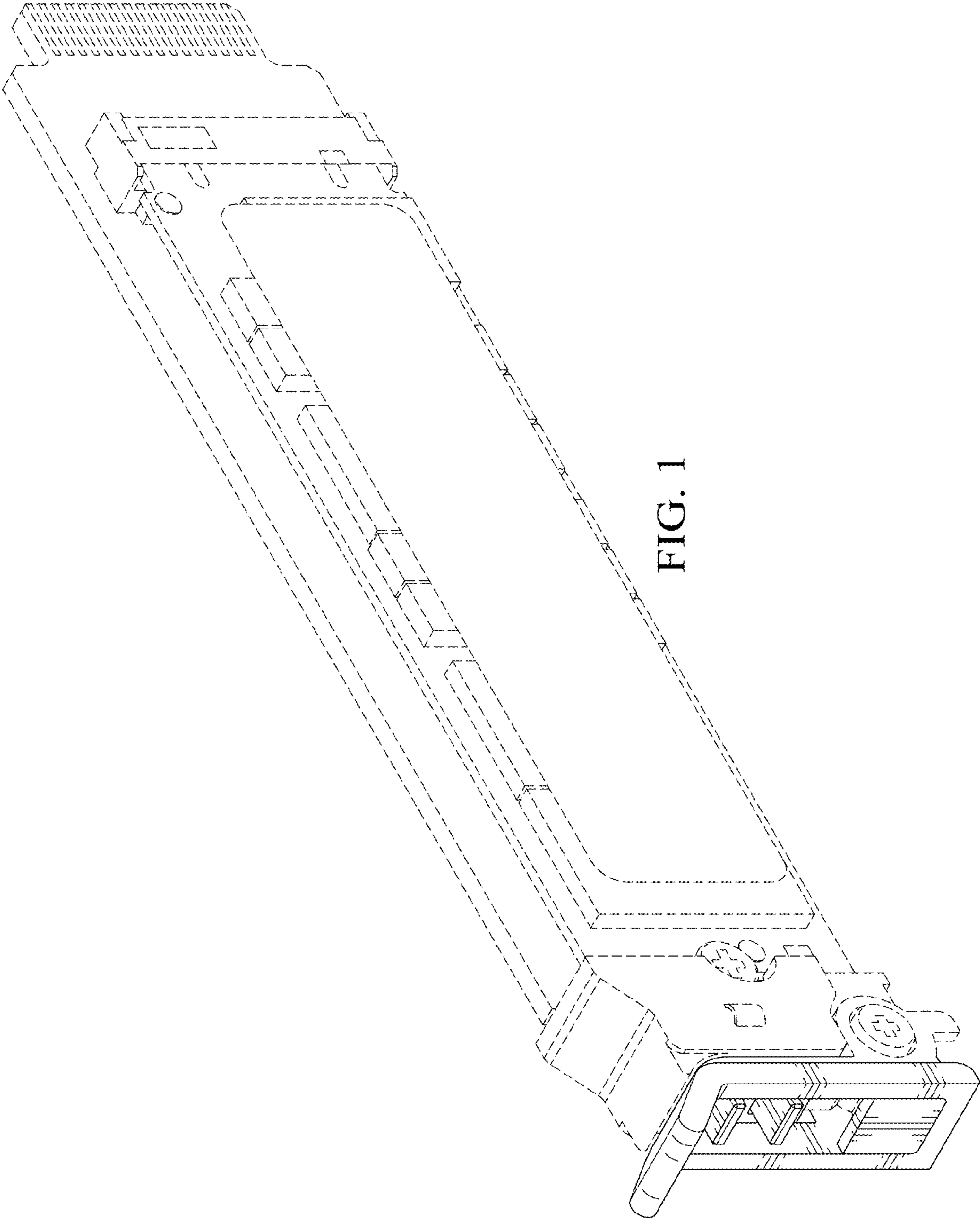


FIG. 1

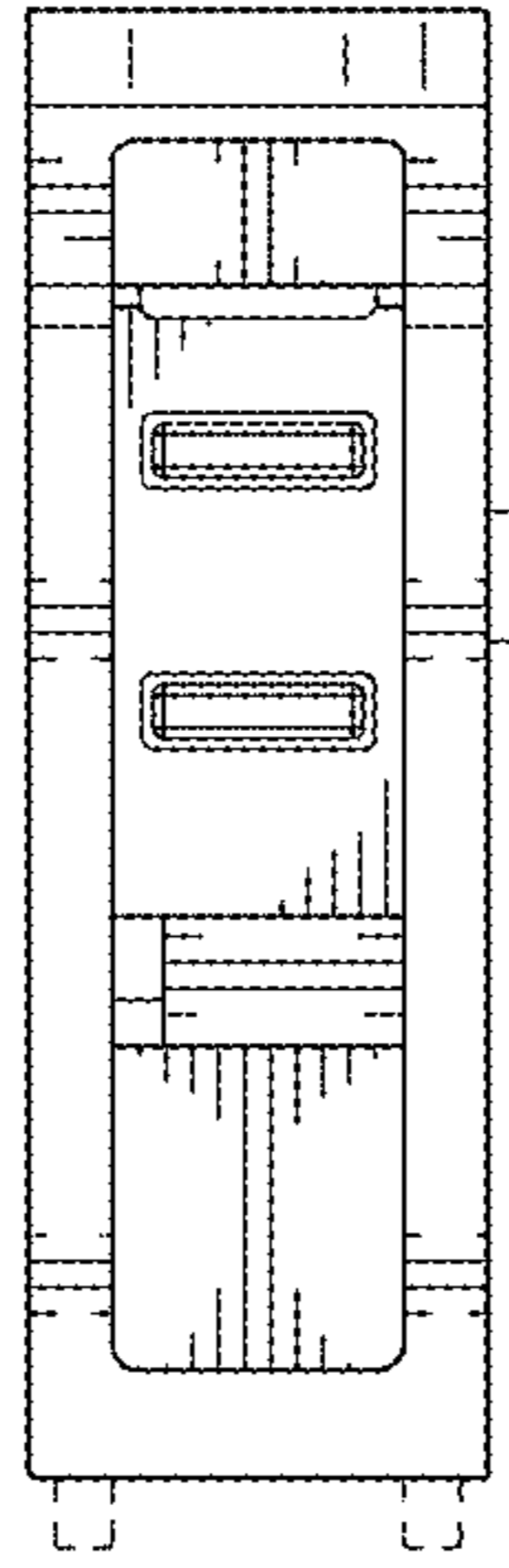


FIG. 2

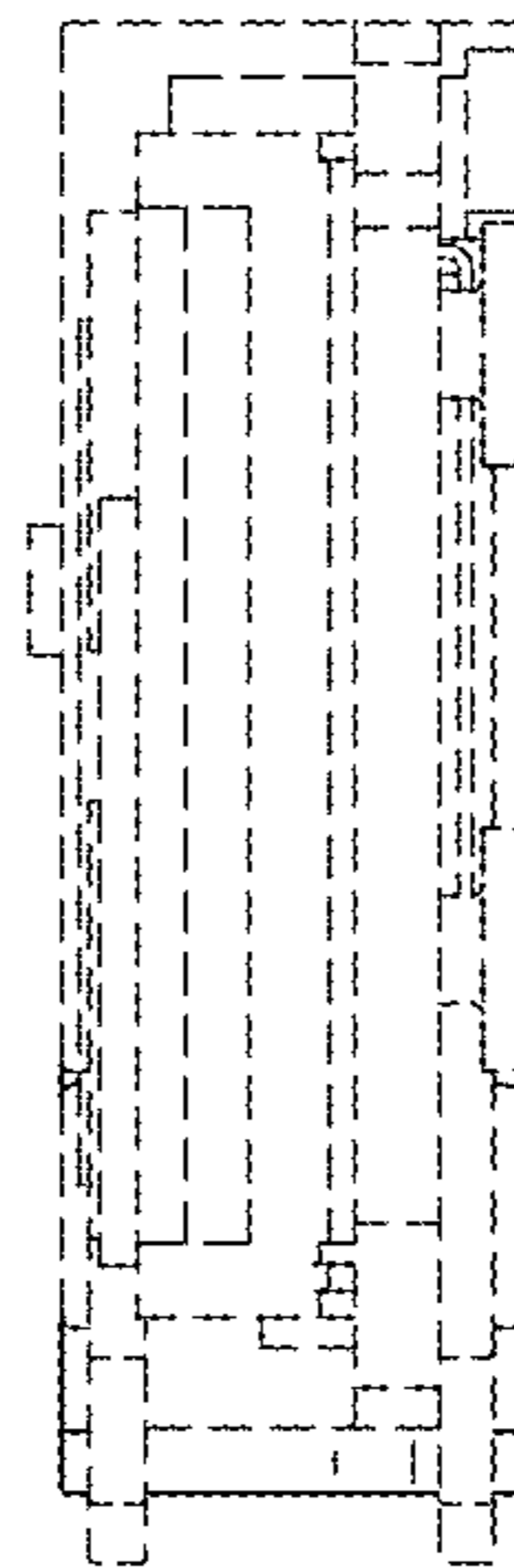


FIG. 3

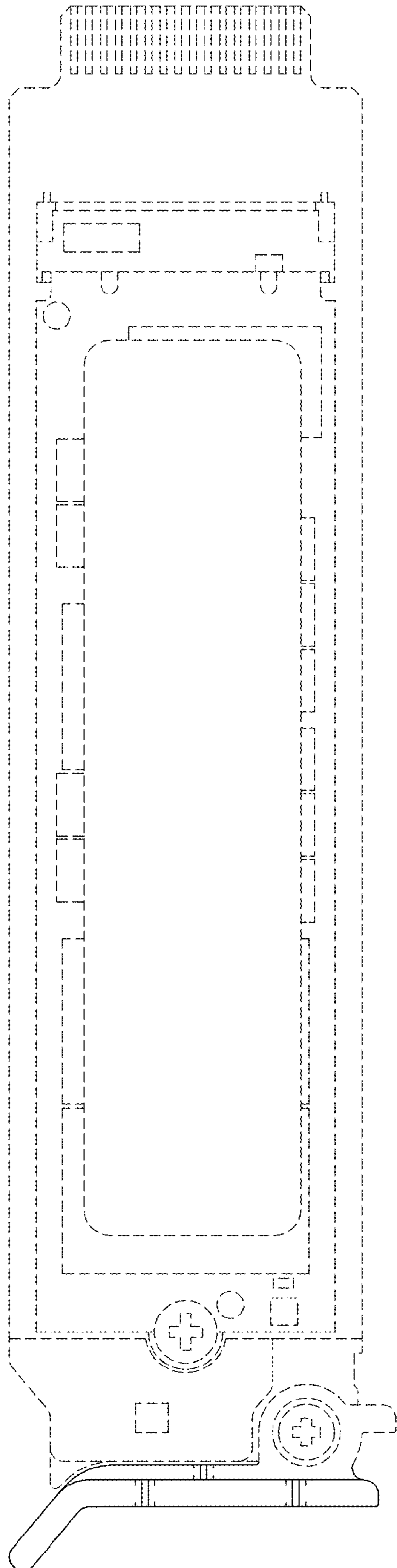


FIG. 4

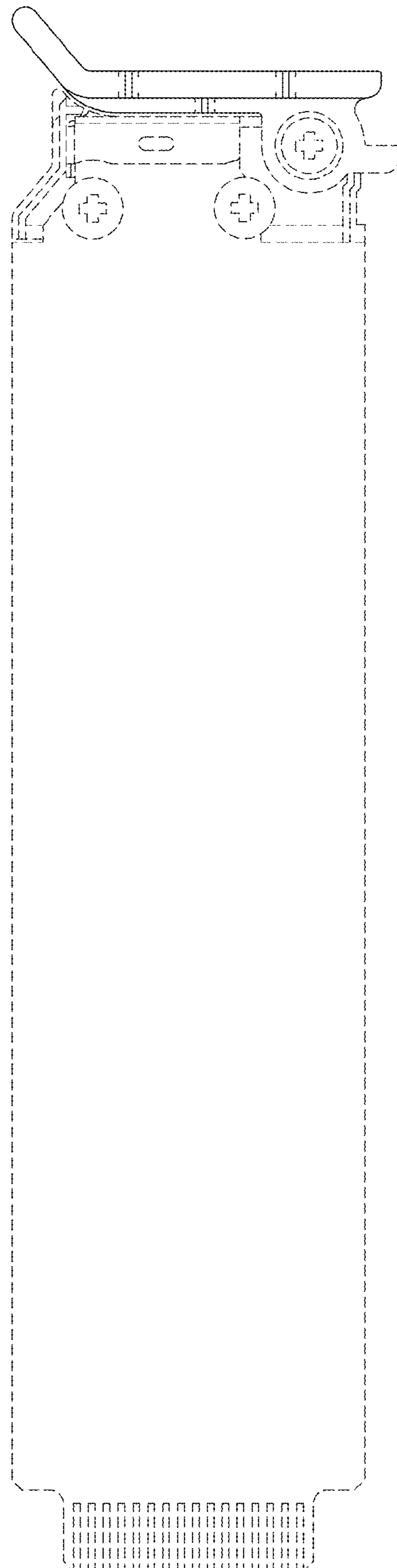


FIG. 5

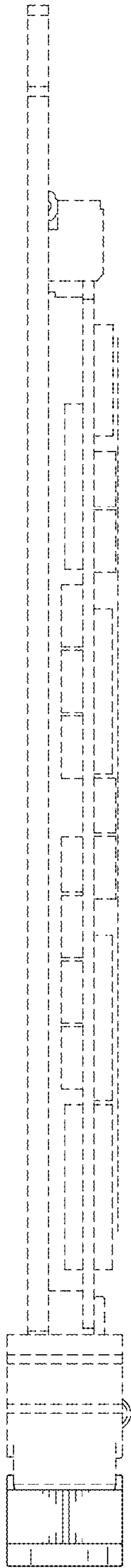


FIG. 6

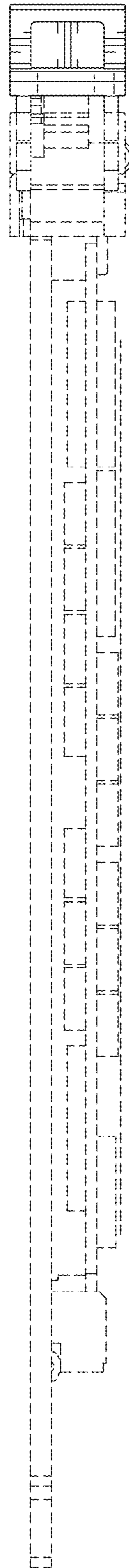


FIG. 7

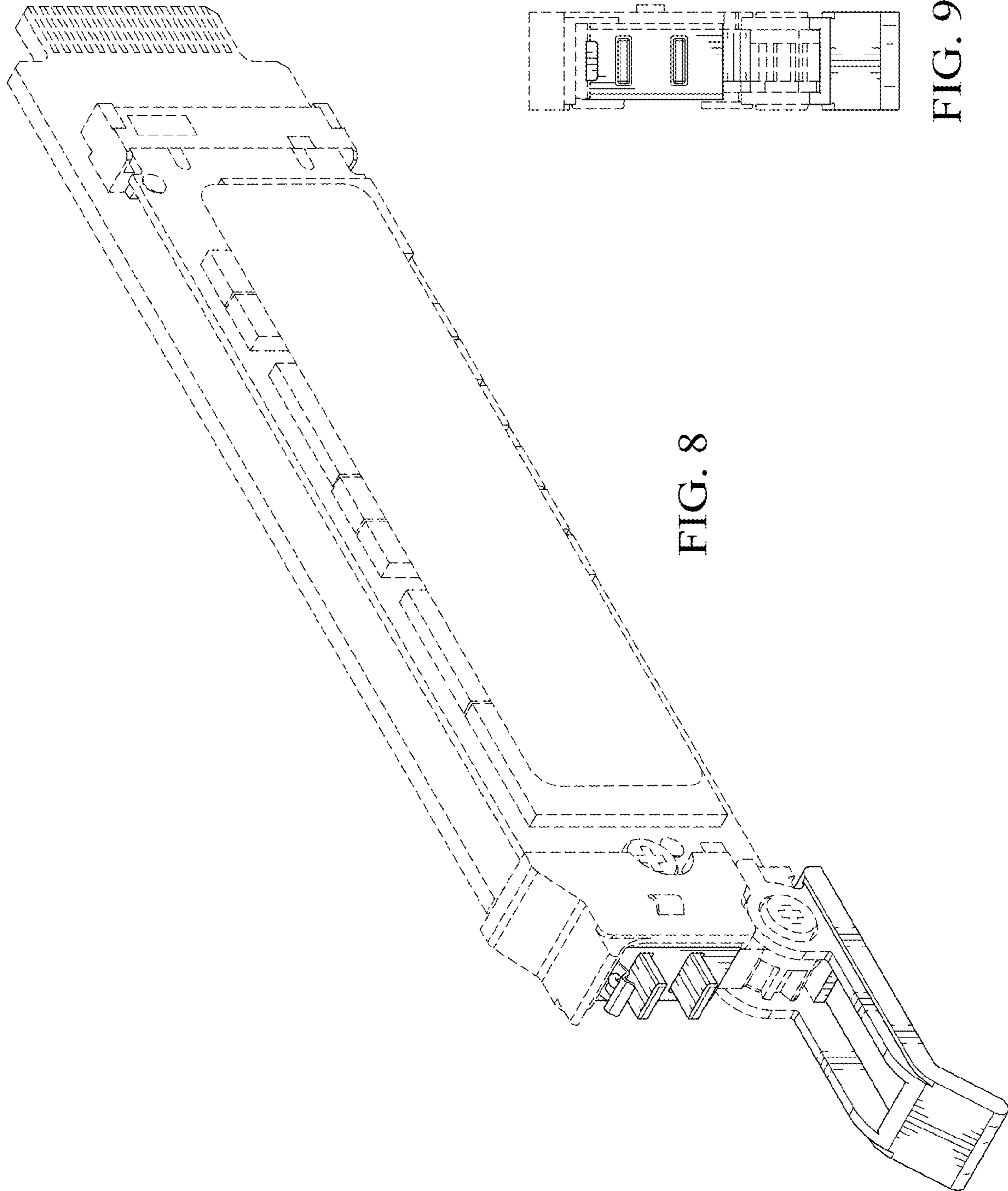


FIG. 8

FIG. 9