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(12) **United States Design Patent** (10) **Patent No.:** **US D967,033 S**
Horino et al. (45) **Date of Patent:** **** Oct. 18, 2022**

(54) **ELECTRICAL CONNECTOR**

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(73) Assignee: **KYOCERA CORPORATION**, Kyoto (JP)

(**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/147**; D13/133; D13/146

(58) **Field of Classification Search**
USPC D10/78; D13/133, 146-147, 149, 152,
D13/154, 184, 199
CPC H01R 12/52; H01R 12/73; H01R 12/707;
H01R 12/716; H01R 13/20; H01R
13/6581; H01R 13/6582; H01R 13/6583;
H01R 13/6585
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an electrical connector, as shown and described.

DESCRIPTION

FIG. 1 is a front and bottom perspective view of an electrical connector showing our new design;
FIG. 2 is a front and top perspective view thereof;
FIG. 3 is a front, bottom and left-side perspective view thereof;
FIG. 4 is a front, top and left-side perspective view thereof;
FIG. 5 is a front elevational view thereof;
FIG. 6 is rear elevational view thereof;
FIG. 7 is a left-side elevational view thereof;
FIG. 8 is a right-side elevational view thereof;
FIG. 9 is a top plan view thereof; and,
FIG. 10 is a bottom plan view thereof.

The broken lines depict portions of the electrical connector that form no part of the claimed design. The dot-dash boundary lines define the boundary of the claimed design and form no part of the claimed design.

1 Claim, 8 Drawing Sheets

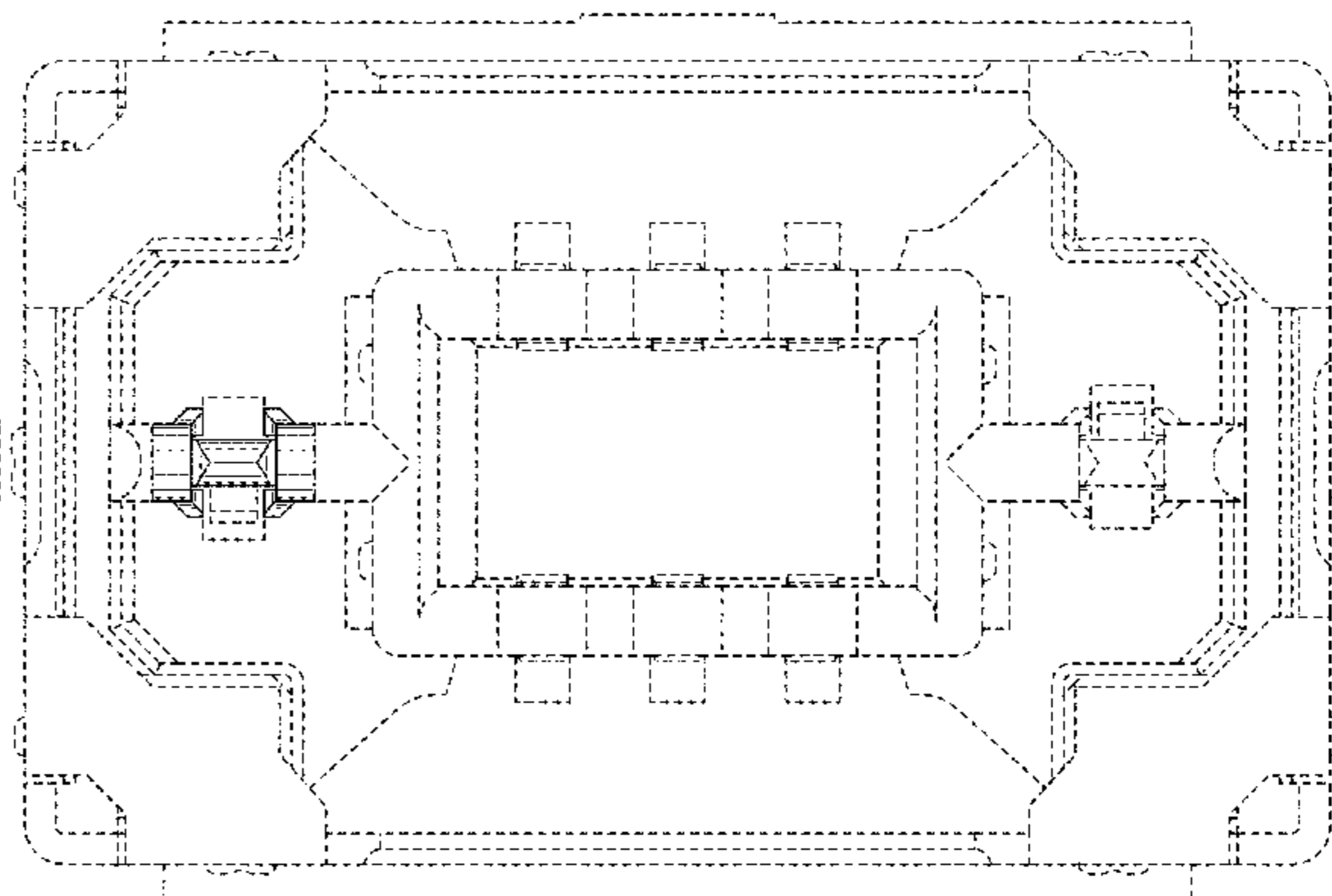
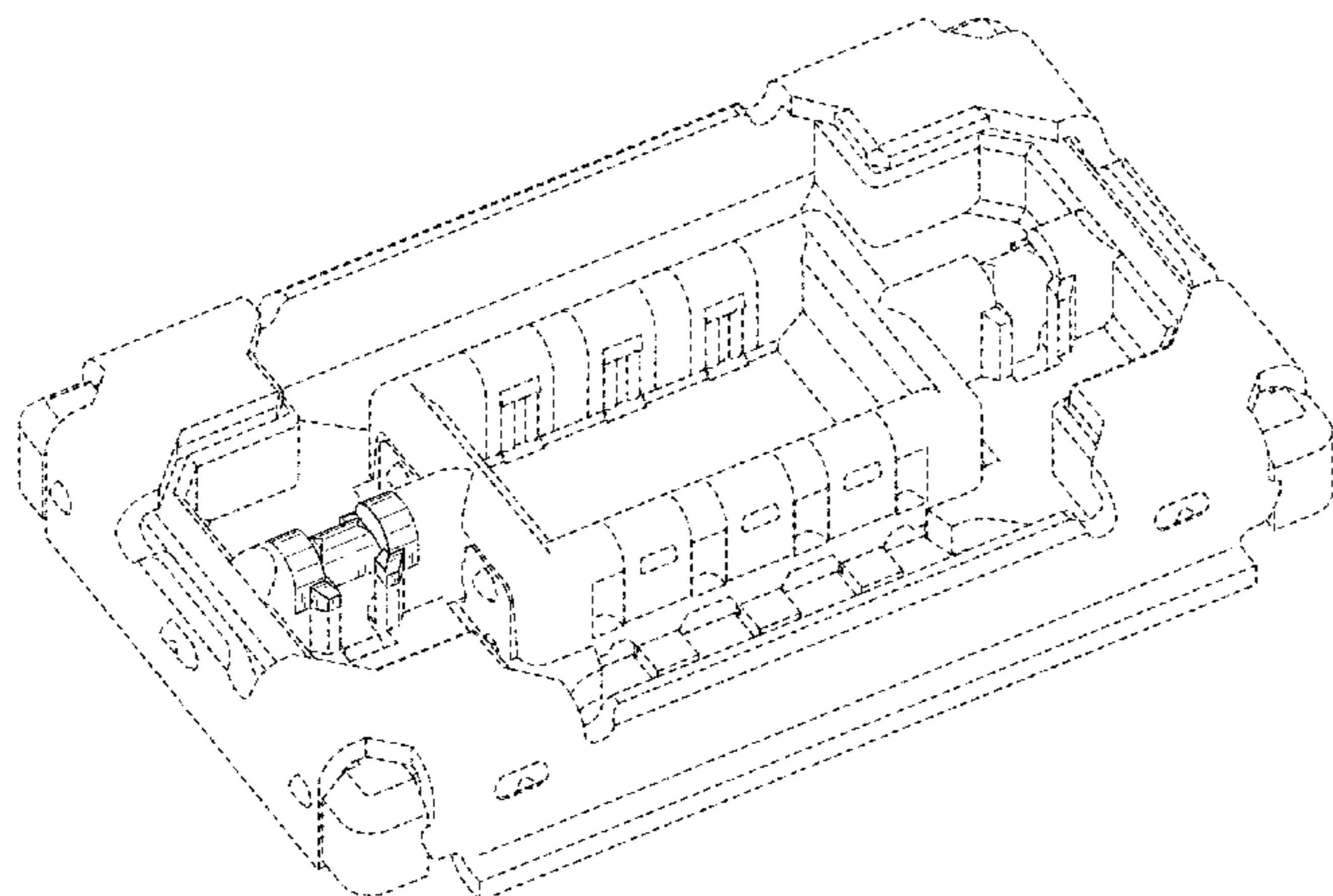


FIG. 1

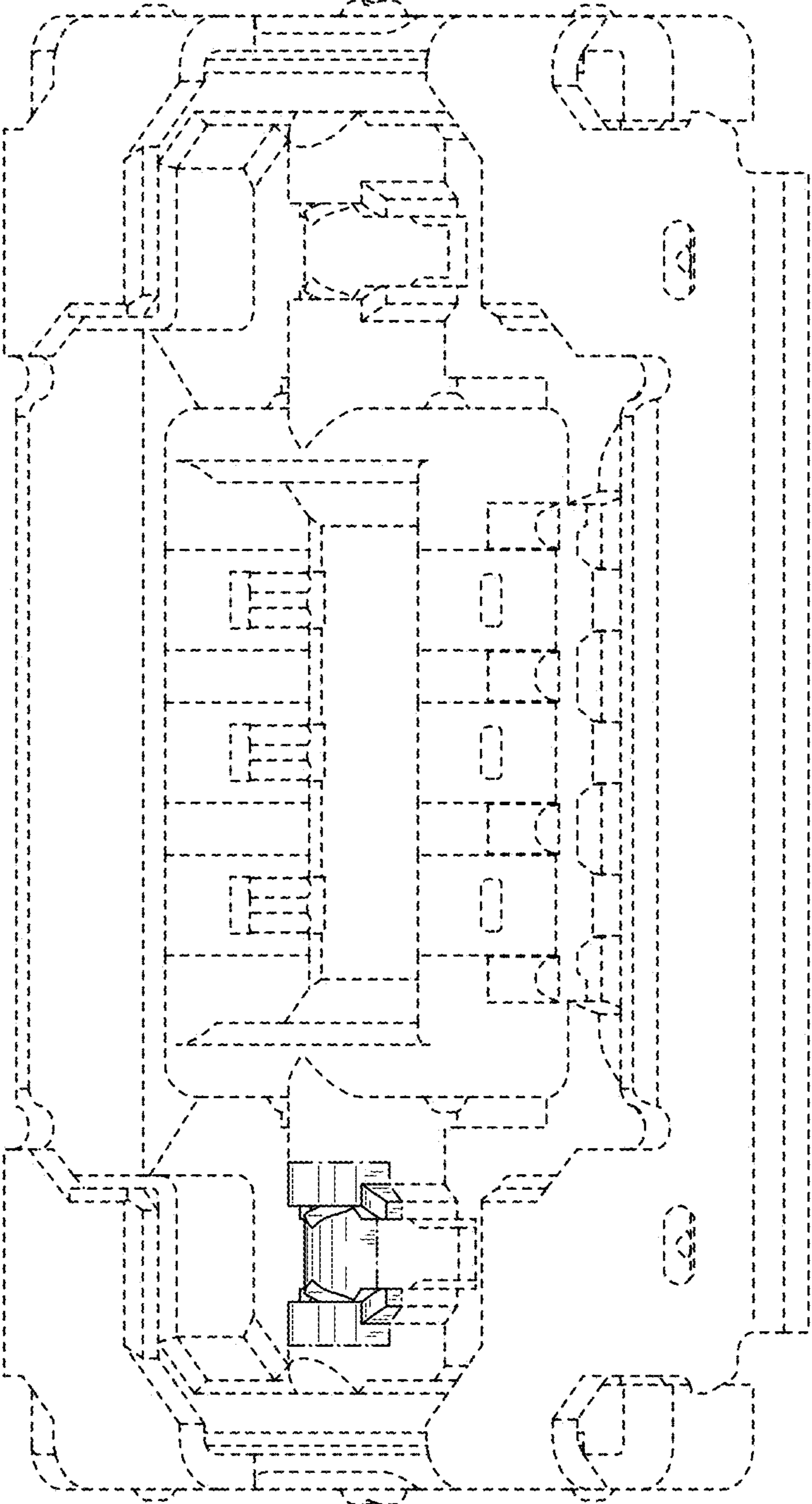
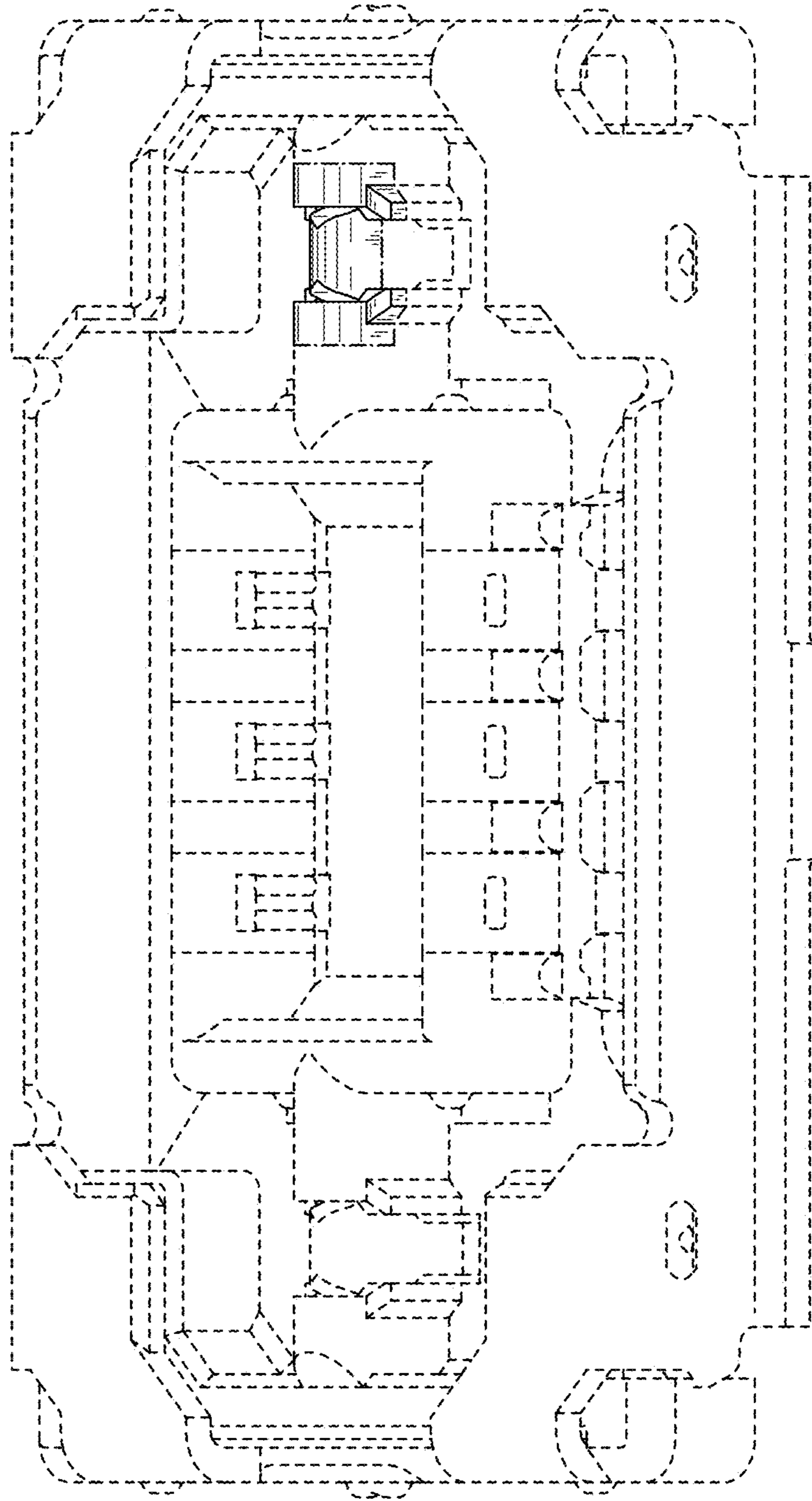


FIG. 2



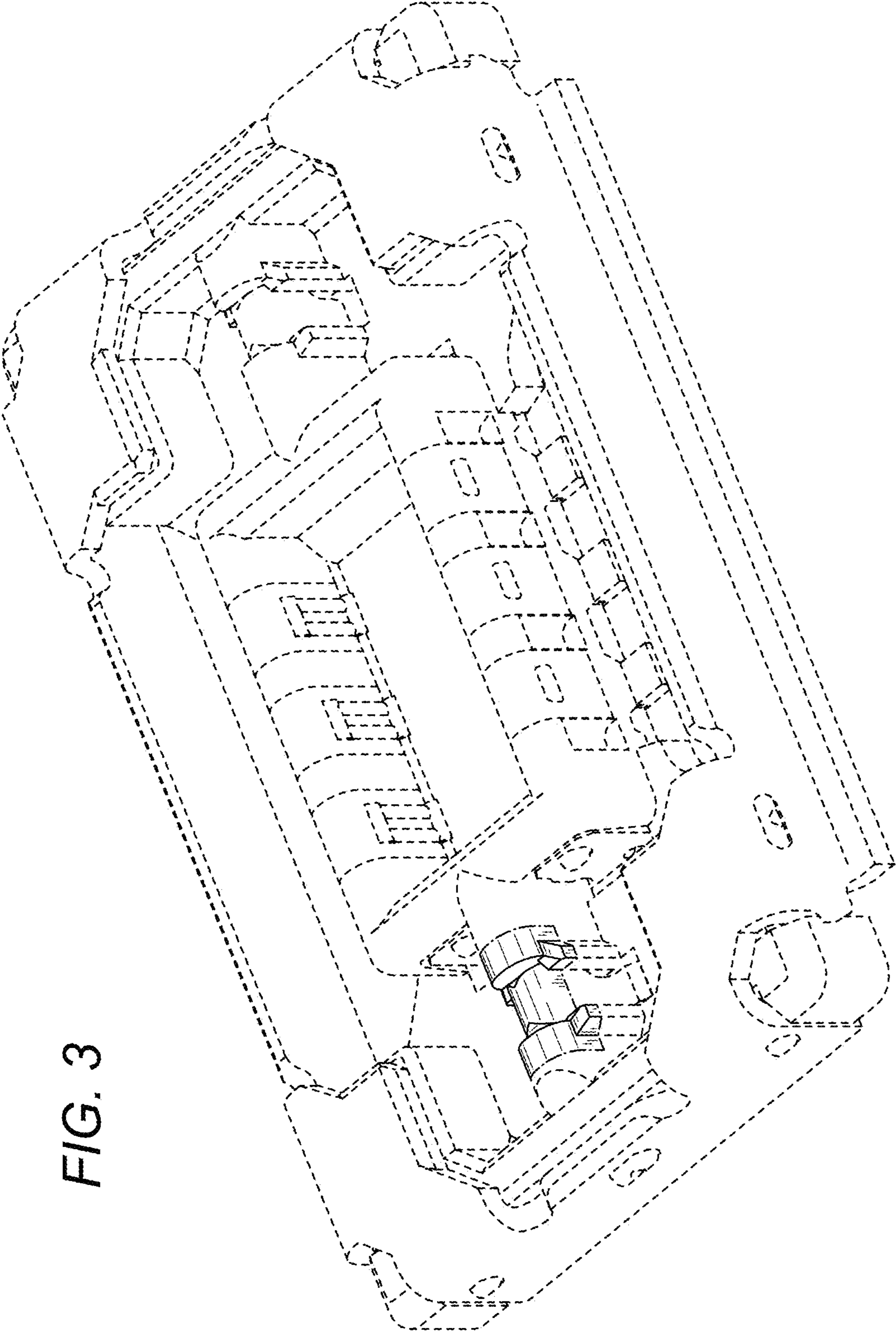


FIG. 3

FIG. 4

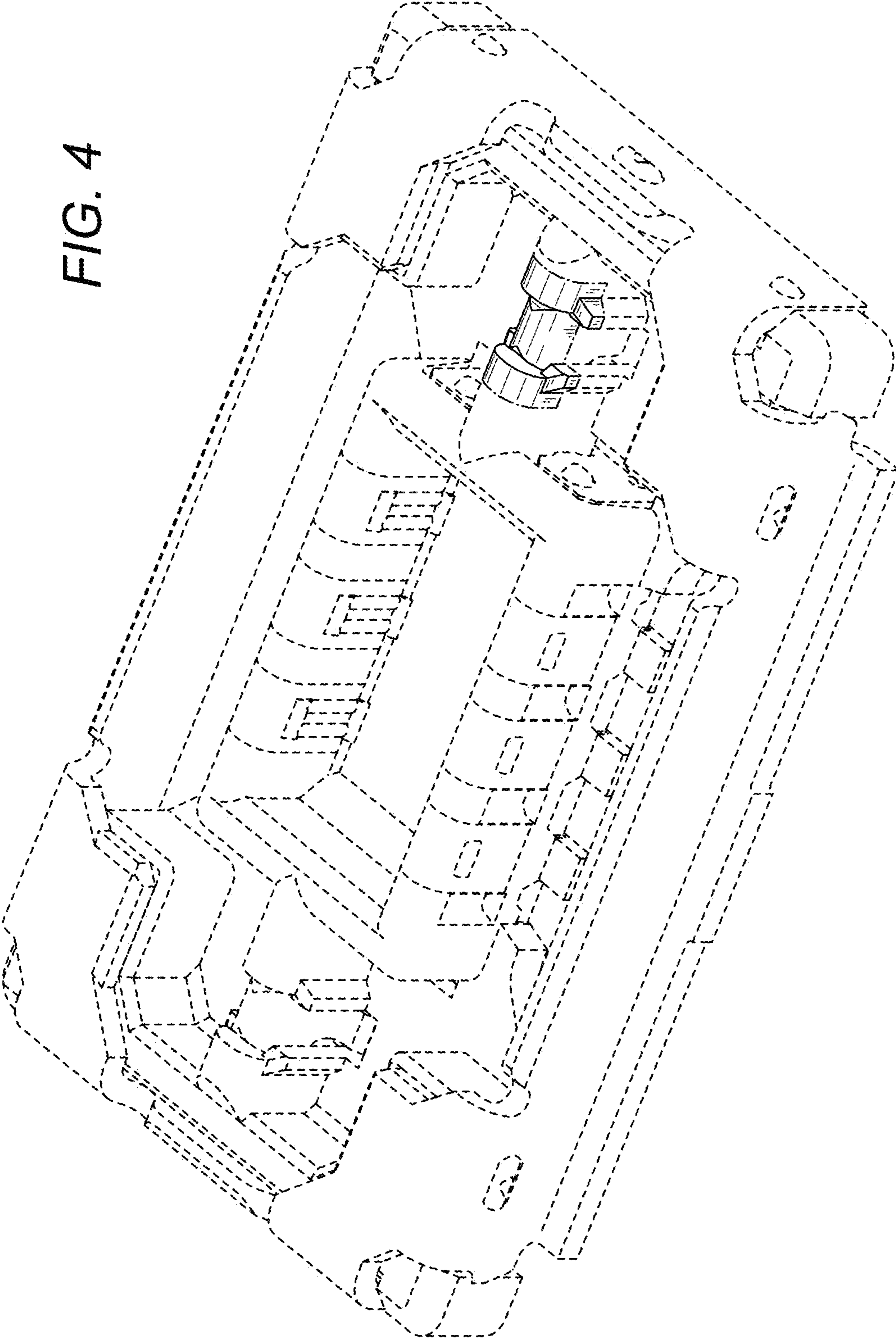


FIG. 5

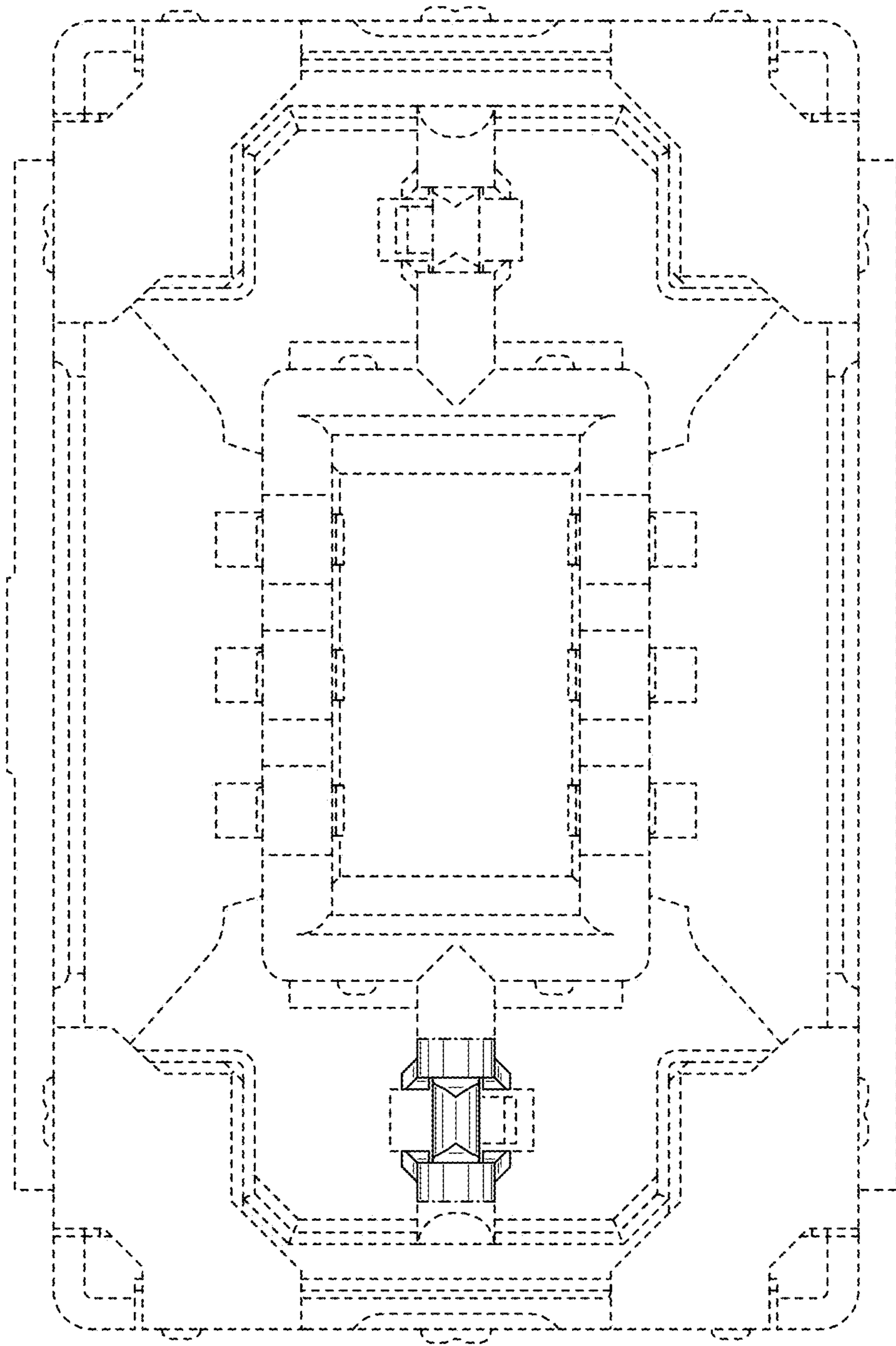


FIG. 6

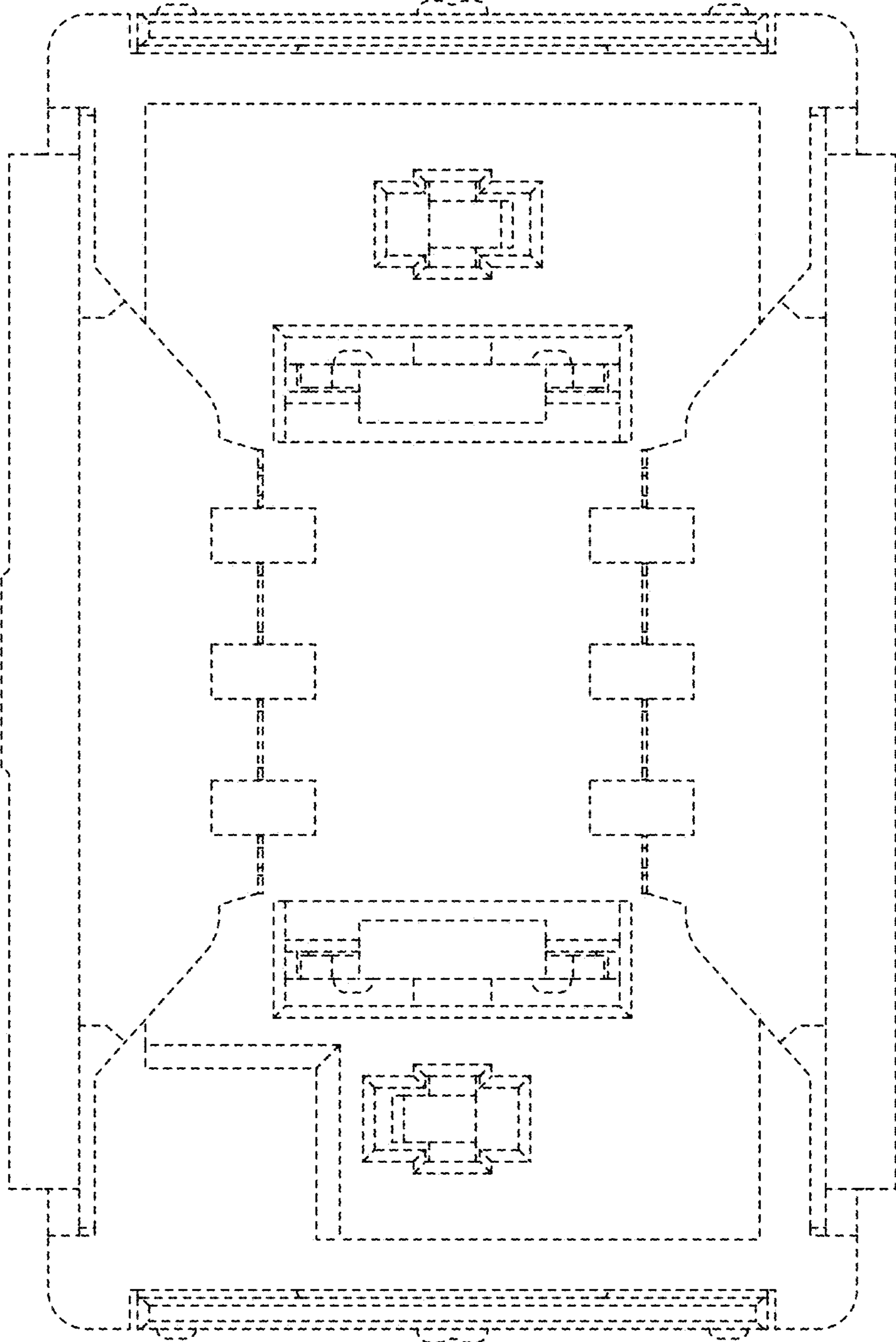


FIG. 7

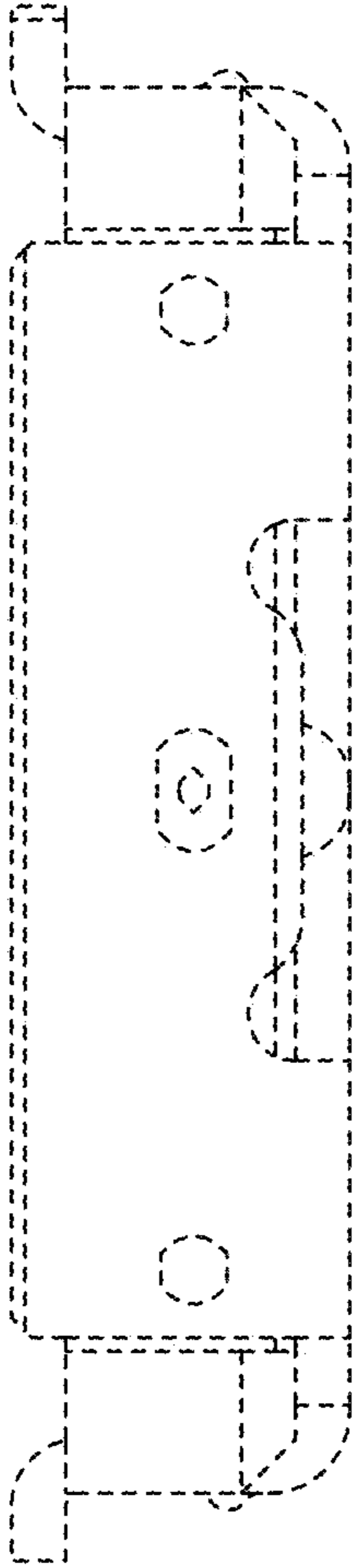


FIG. 8

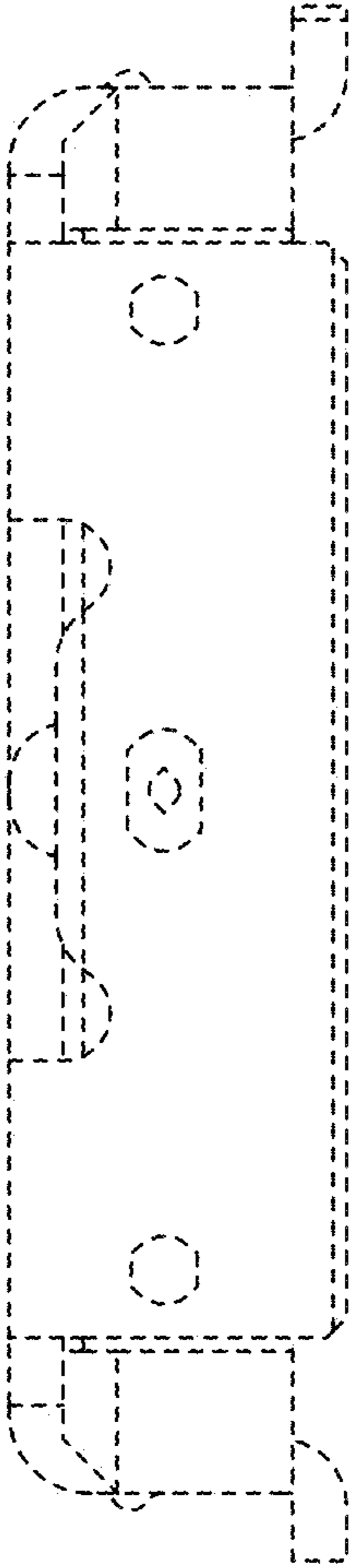


FIG. 9

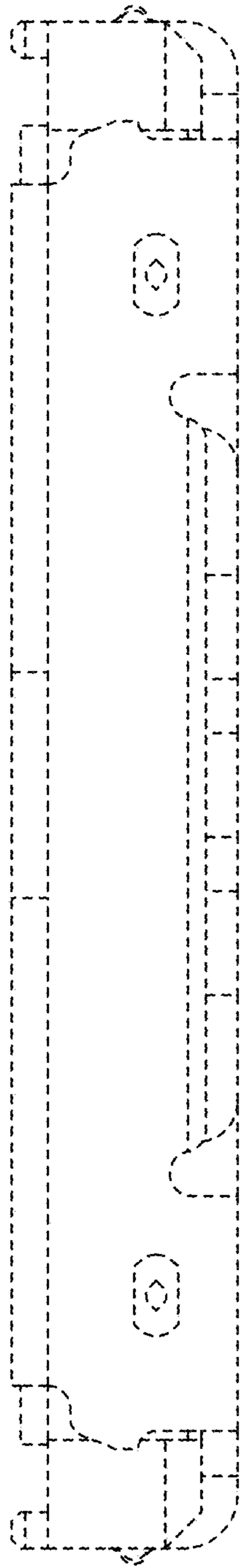


FIG. 10

