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(12) **United States Design Patent** (10) **Patent No.:** **US D963,628 S**
McManigal et al. (45) **Date of Patent:** **** Sep. 13, 2022**

- (54) **ELECTRONICS DEVICE MOUNT**
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(**) Term: **15 Years**

(21) Appl. No.: **29/747,595**

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(51) **LOC (13) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/238**

(58) **Field of Classification Search**
USPC D14/238; D16/203; D22/108
CPC F16M 11/04; G03B 17/561
See application file for complete search history.

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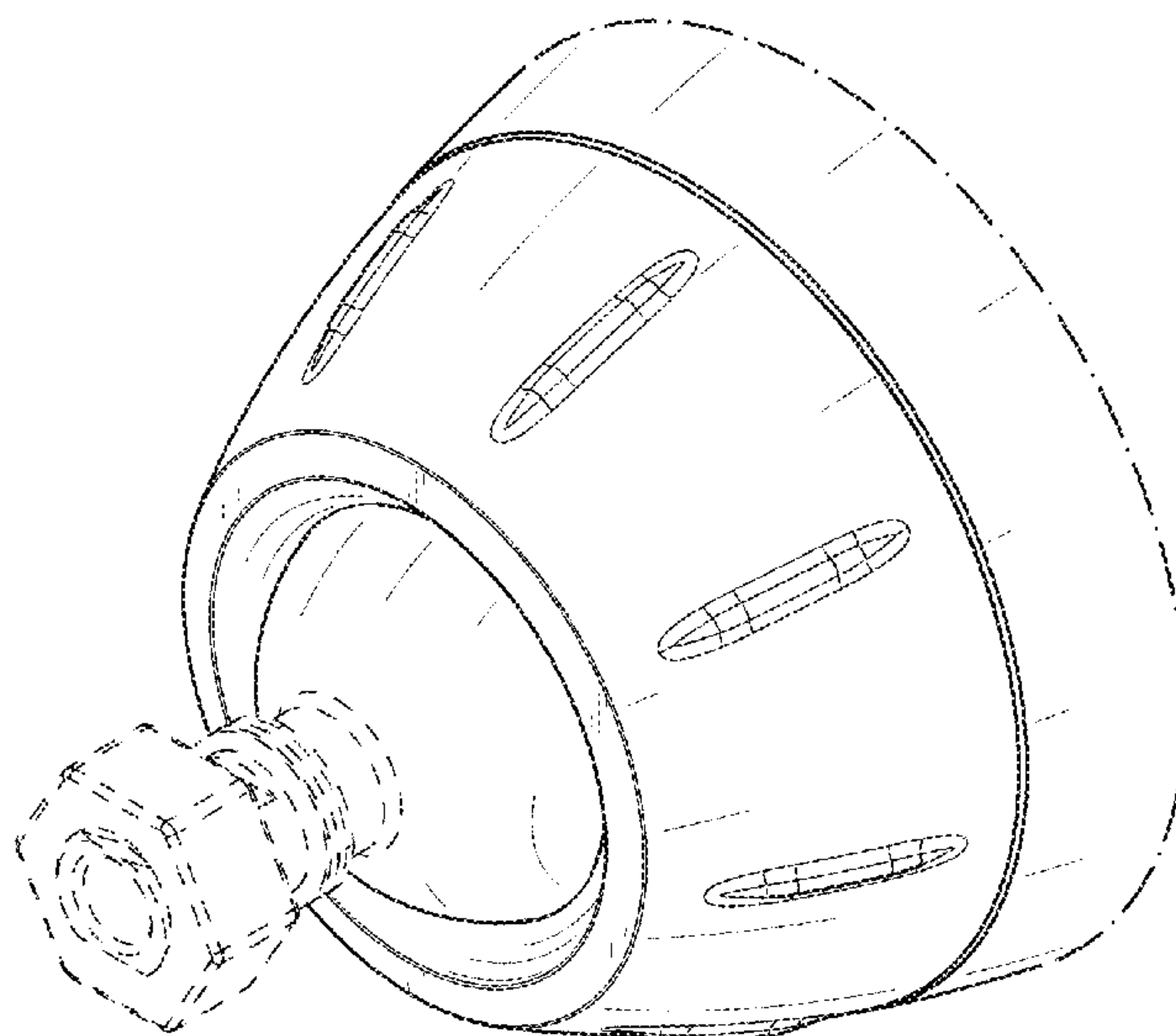
(57) **CLAIM**

The ornamental design for an electronics device mount, as shown and described.

DESCRIPTION

FIG. 1 is a top, front, right side perspective view of an electronics device mount our new design in conjunction of an electronics device in the form of a housing;
 FIG. 2 is a top, front, right side perspective view of an electronics device mount our new design;
 FIG. 3 is a front elevation view thereof;
 FIG. 4 is a rear elevation view thereof;
 FIG. 5 is a right-side elevation view thereof;
 FIG. 6 is a left-side elevation view thereof;
 FIG. 7 is a top plan view thereof; and,
 FIG. 8 is a bottom plan view thereof.
 The dashed broken lines in the drawings represent unclaimed portions of the design or the environment of the claimed design, and the dash-dot broken lines represent boundaries of the claimed design. All of the broken lines form no part of the claimed design.

1 Claim, 8 Drawing Sheets



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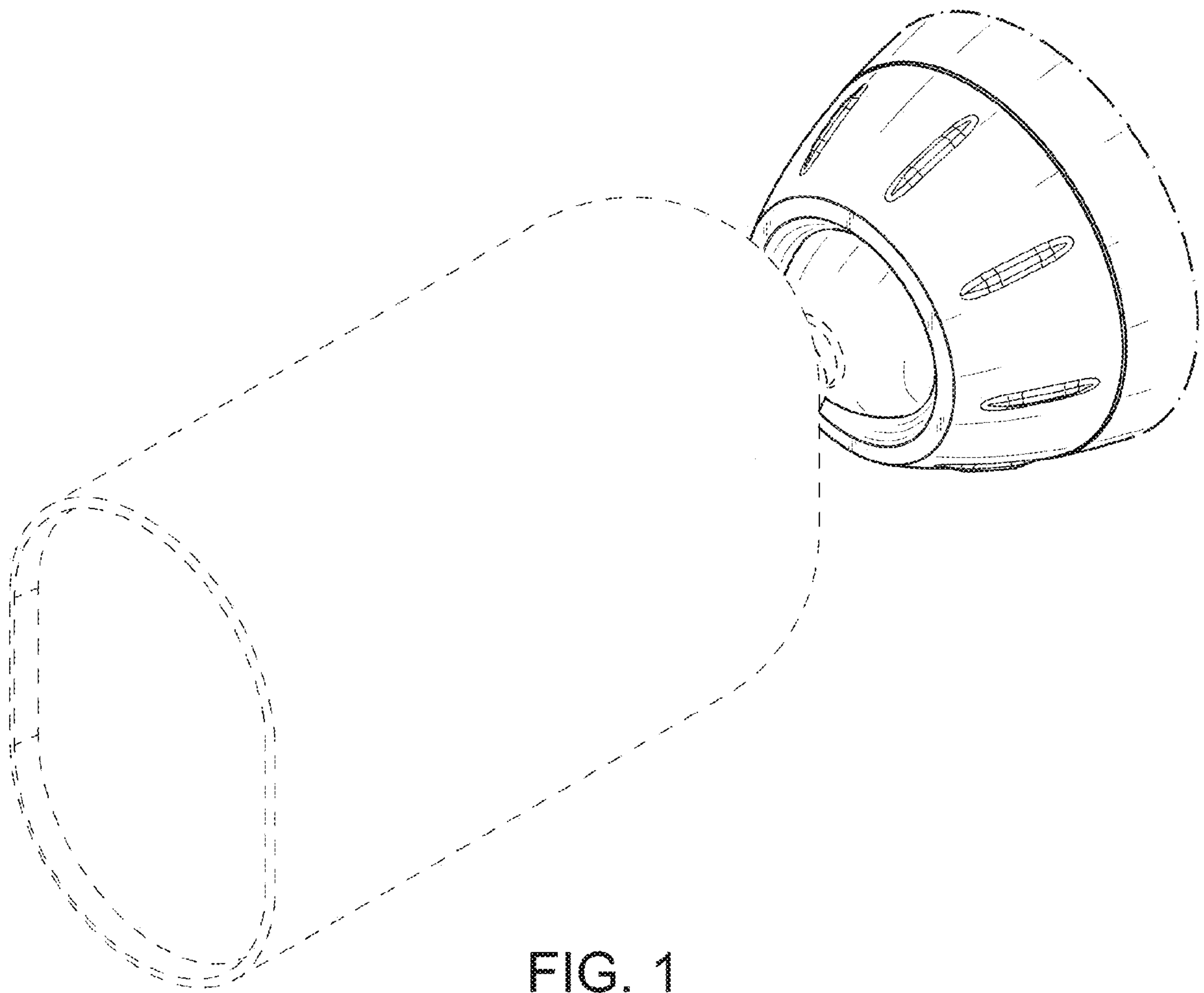


FIG. 1

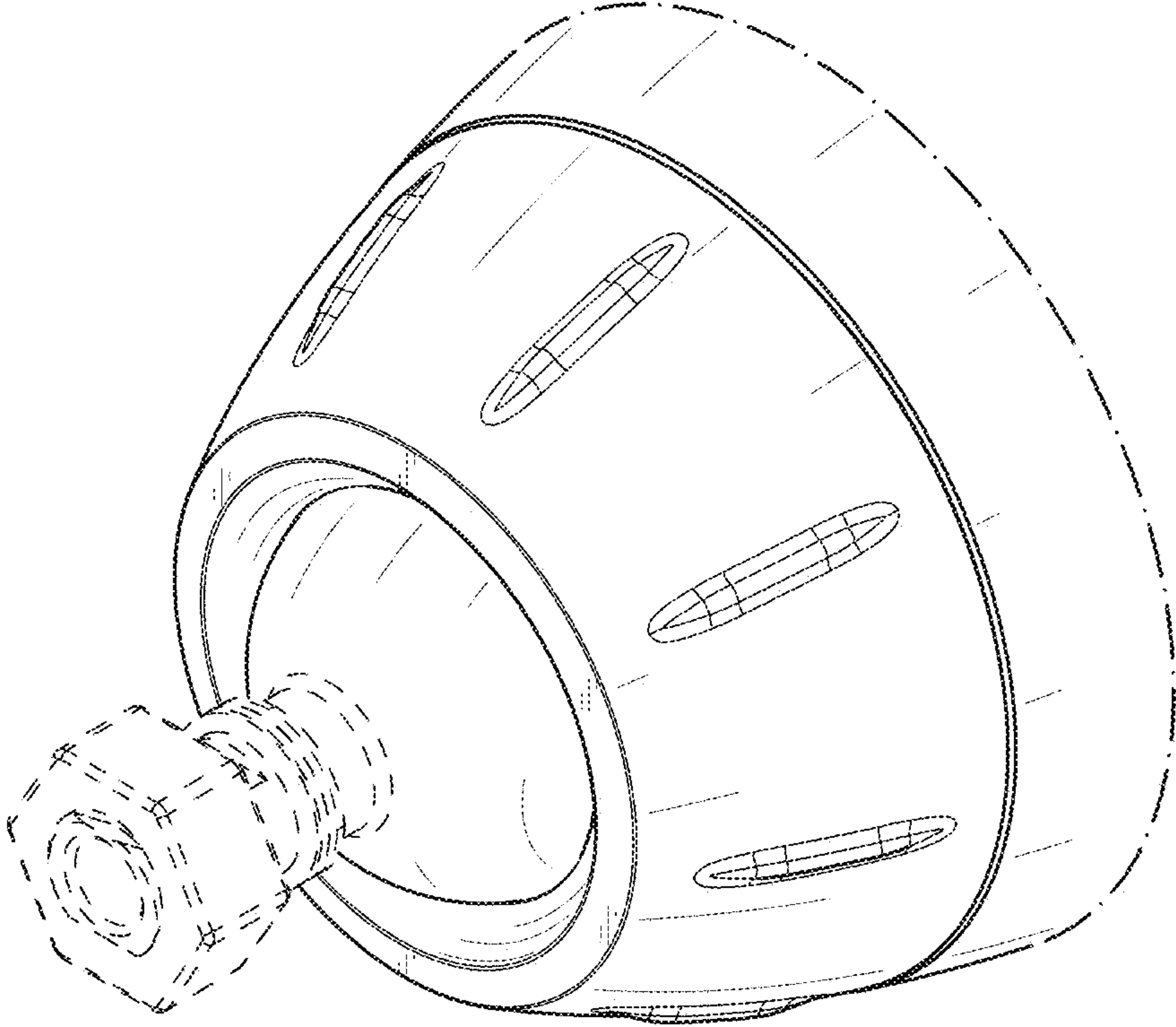


FIG. 2

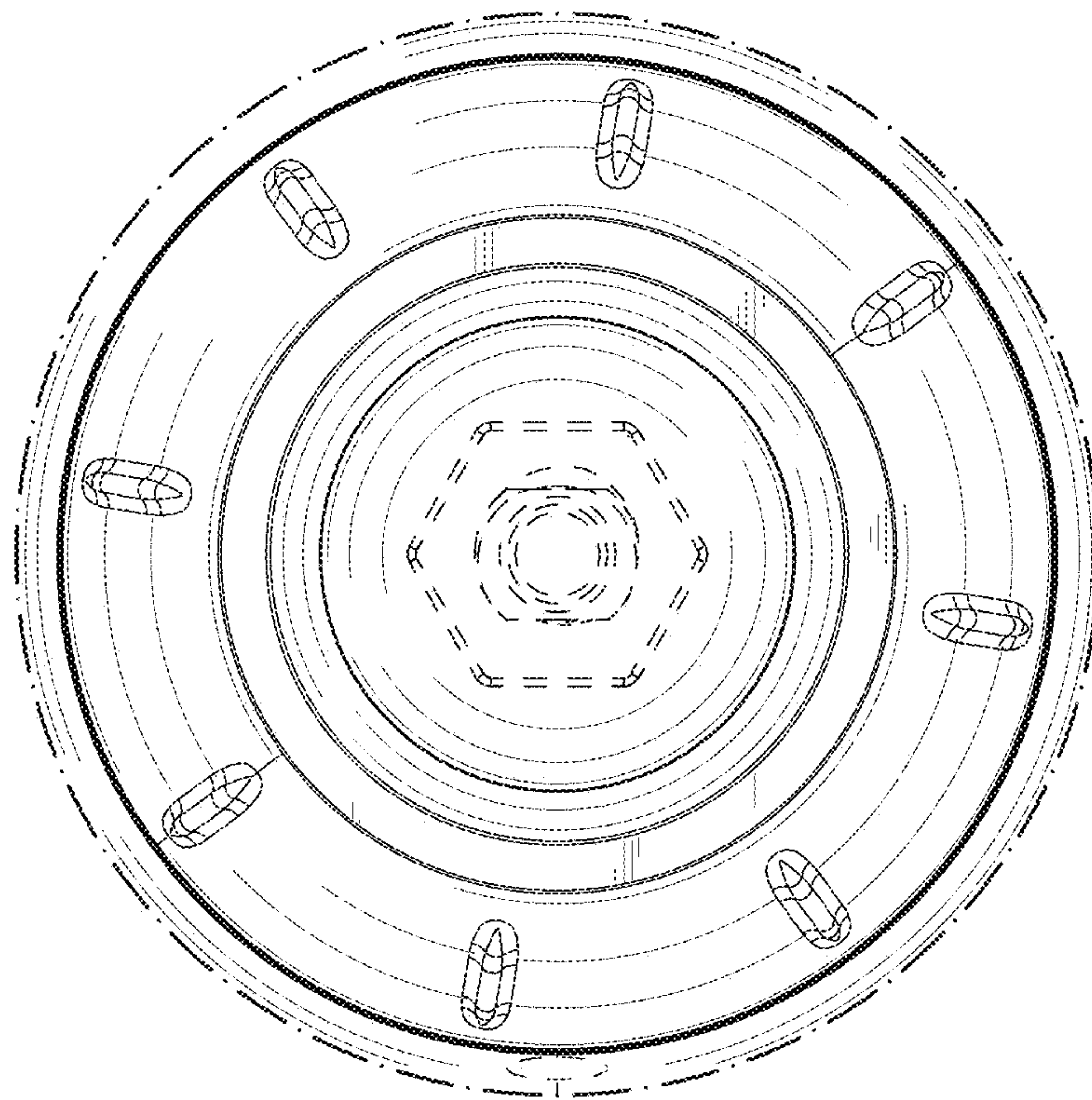


FIG. 3

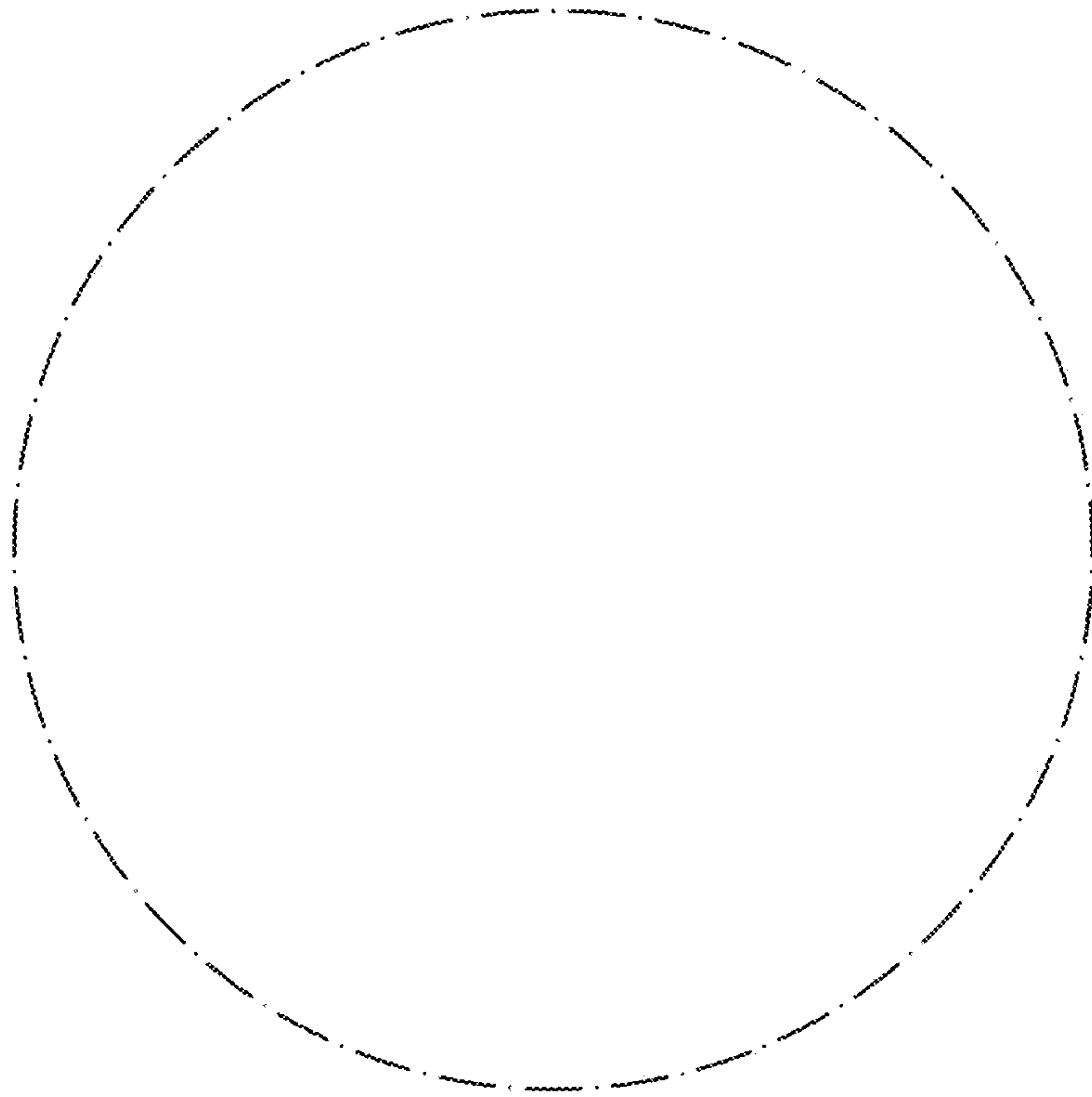


FIG. 4

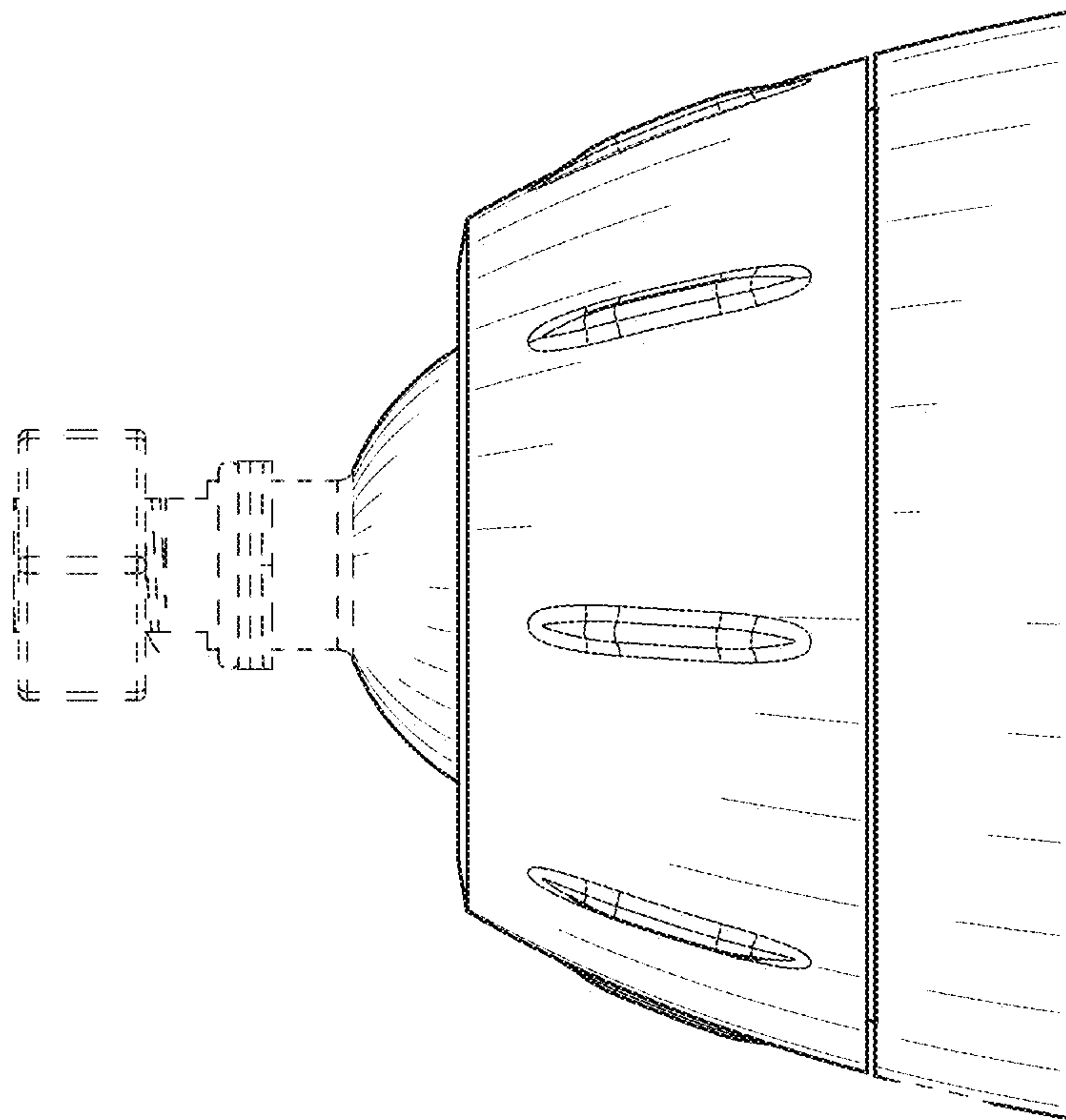


FIG. 5

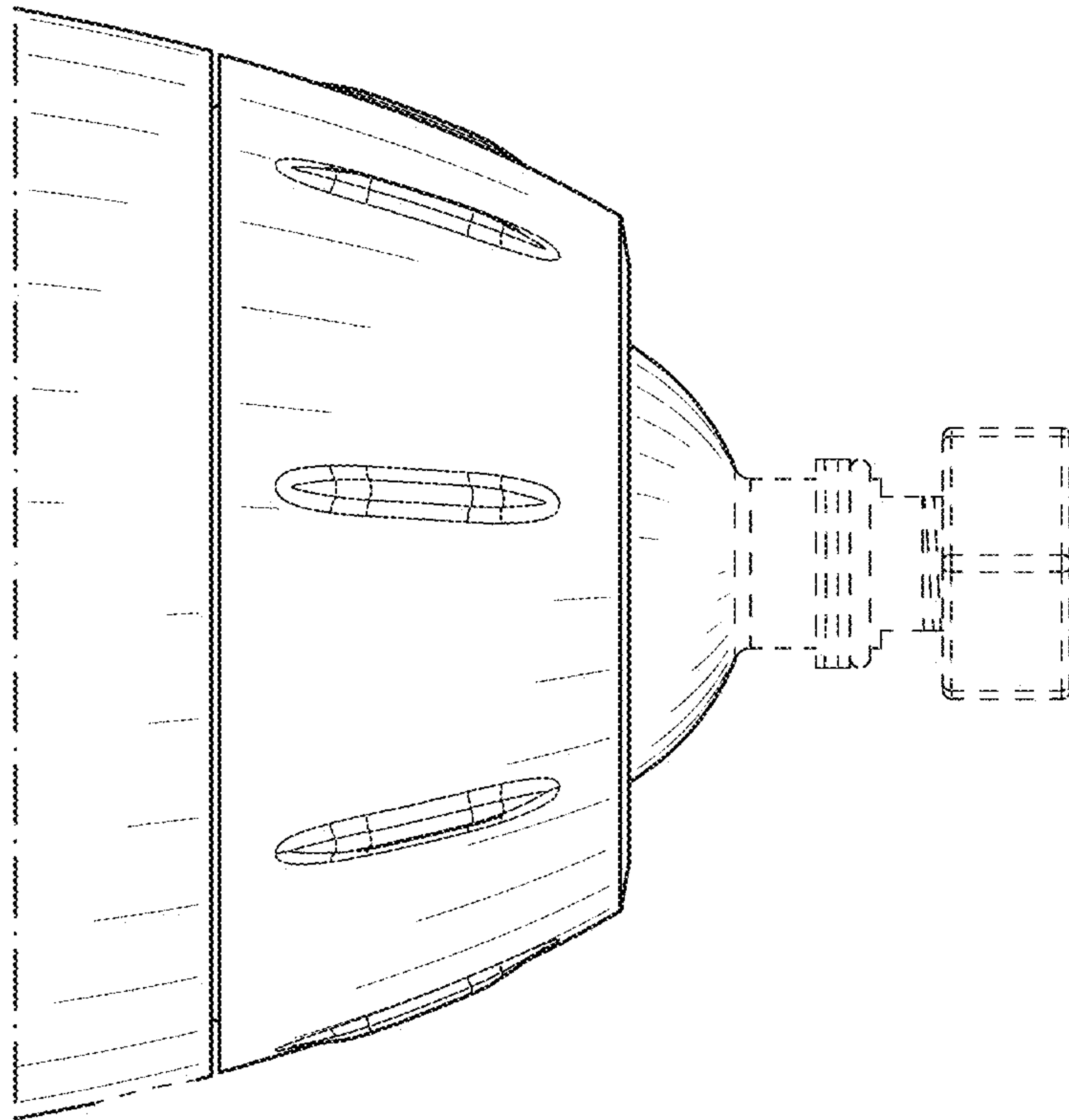


FIG. 6

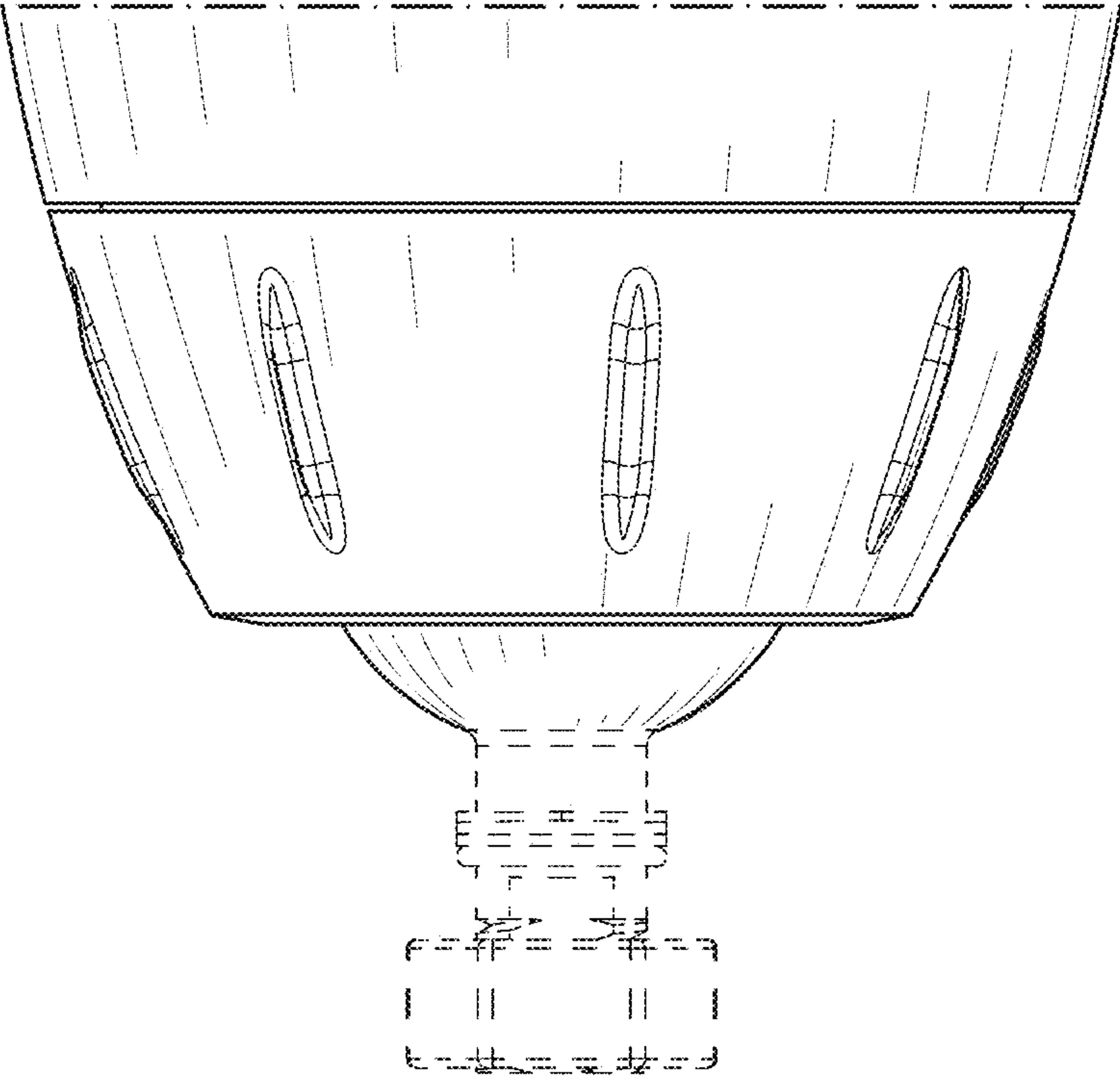


FIG. 7

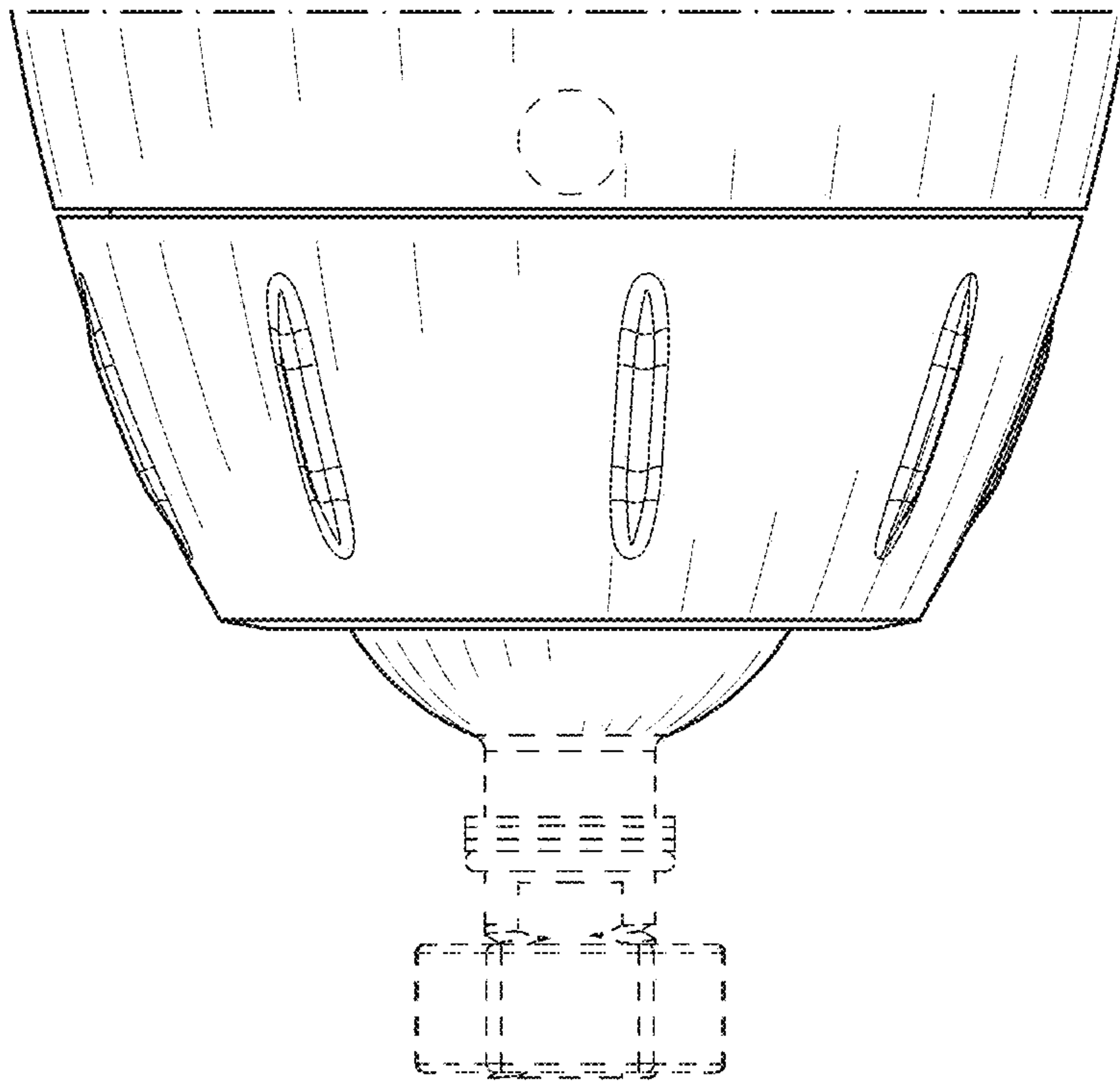


FIG. 8