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(12) **United States Design Patent** (10) **Patent No.:** **US D963,043 S**  
**Rehbock et al.** (45) **Date of Patent:** **\*\* Sep. 6, 2022**

(54) **ELECTRONIC DEVICE**  
(71) Applicant: **GAMEFACE LABS INC.**, San Francisco, CA (US)  
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(73) Assignee: **GAMEFACE LABS INC.**, San Francisco, CA (US)  
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(51) **LOC (13) Cl.** ..... **21-01**  
(52) **U.S. Cl.**  
USPC ..... **D21/332**  
(58) **Field of Classification Search**  
USPC ..... D21/313, 366, 386, 389, 392, 397, 466, D21/493, 496; D9/423, 499, 645, 721, D9/756, 757, 761, 762  
CPC .... A63F 3/00; A63F 3/00694; A63F 3/00176; G07F 17/00  
See application file for complete search history.

D532,052 S *	11/2006	Yamamoto	.....	D21/332
D539,852 S *	4/2007	Hussaini	.....	D14/433
D550,775 S *	9/2007	Fujita	.....	D14/435
D553,692 S *	10/2007	Ehara	.....	D21/333
D556,835 S *	12/2007	Ashida	.....	D21/324
D556,836 S *	12/2007	Ashida	.....	D21/324
D570,888 S *	6/2008	Altnether	.....	D15/199
D586,863 S *	2/2009	Kamei	.....	D21/332
D645,443 S *	9/2011	Maruyama	.....	D14/140.1
D656,555 S *	3/2012	Zenri	.....	D21/333
D682,947 S *	5/2013	Akama	.....	D21/333
D699,791 S *	2/2014	Sugino	.....	D21/333
D705,361 S *	5/2014	Jacobs	.....	D14/496
D805,067 S *	12/2017	Sparks	.....	D14/332
D805,130 S *	12/2017	Sparks	.....	D21/332
D826,337 S *	8/2018	Christie	.....	D14/496
D937,933 S *	12/2021	Tong	.....	D14/401
D940,707 S *	1/2022	Ledbetter	.....	D14/349

\* cited by examiner

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(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

**DESCRIPTION**

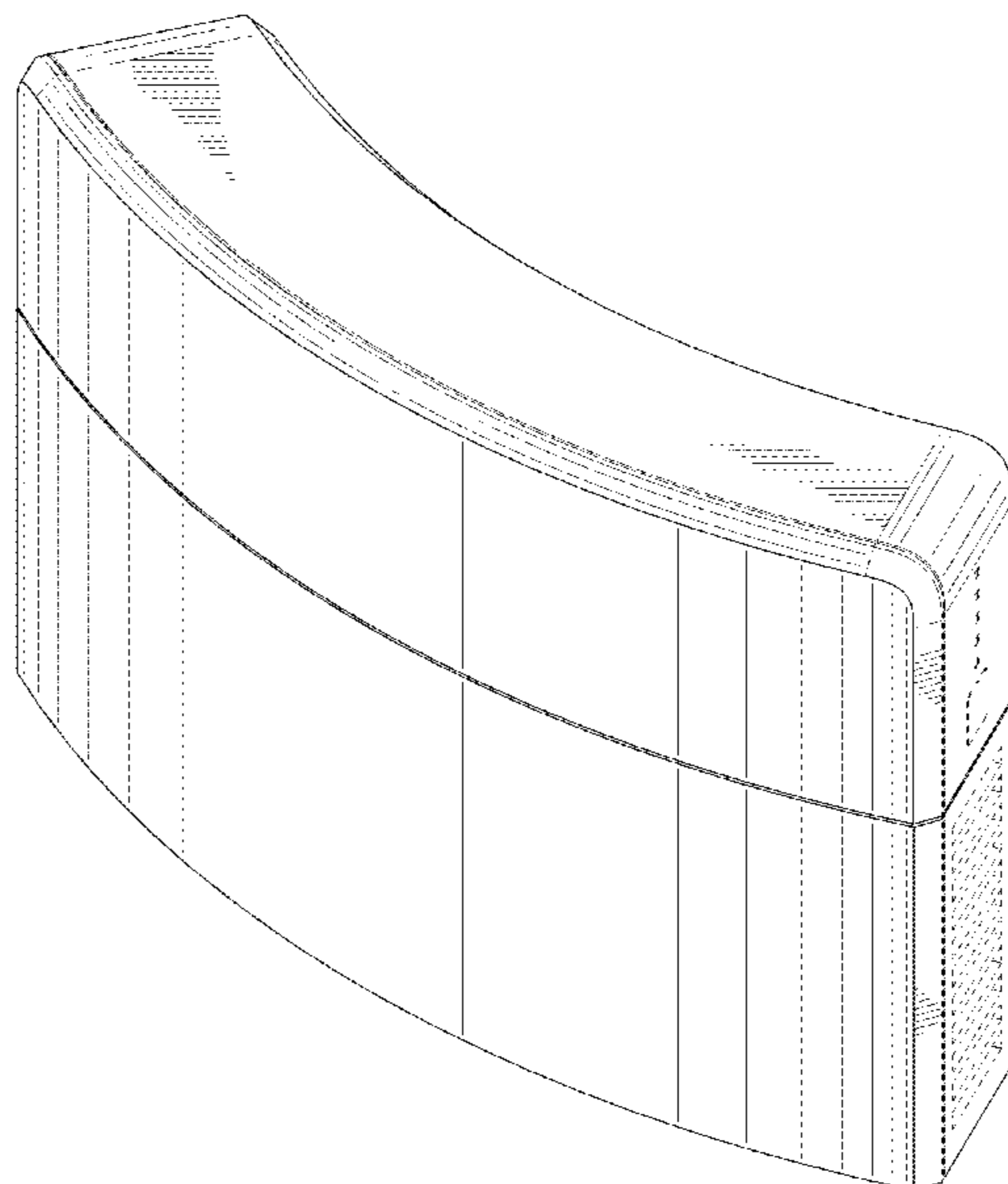
FIG. 1 is a front perspective view of an electronic device showing our new design;  
FIG. 2 is a rear perspective thereof;  
FIG. 3 is a front view thereof;  
FIG. 4 is a rear view thereof;  
FIG. 5 is a side view thereof;  
FIG. 6 is an opposite view thereof;  
FIG. 7 is a top view thereof; and,  
FIG. 8 is a bottom view thereof.  
The broken lines in the drawings illustrate portions of the electronic device that form no part of the claimed design.

**1 Claim, 8 Drawing Sheets**

(56) **References Cited**

U.S. PATENT DOCUMENTS

D366,285 S *	1/1996	Tada	.....	D21/332
D381,647 S *	7/1997	Terng	.....	D14/217
D383,800 S *	9/1997	Ishizaki	.....	D21/332
D407,762 S *	4/1999	Ota	.....	D21/332
D415,534 S *	10/1999	Ashida	.....	D21/332
D419,198 S *	1/2000	Ashida	.....	D21/332
D451,558 S *	12/2001	Sano	.....	D21/333
D452,281 S *	12/2001	Ohno	.....	D21/332
D478,127 S *	8/2003	Nishikawa	.....	D14/435
D480,764 S *	10/2003	Inoue	.....	D21/332
D499,100 S *	11/2004	Neel	.....	D14/435



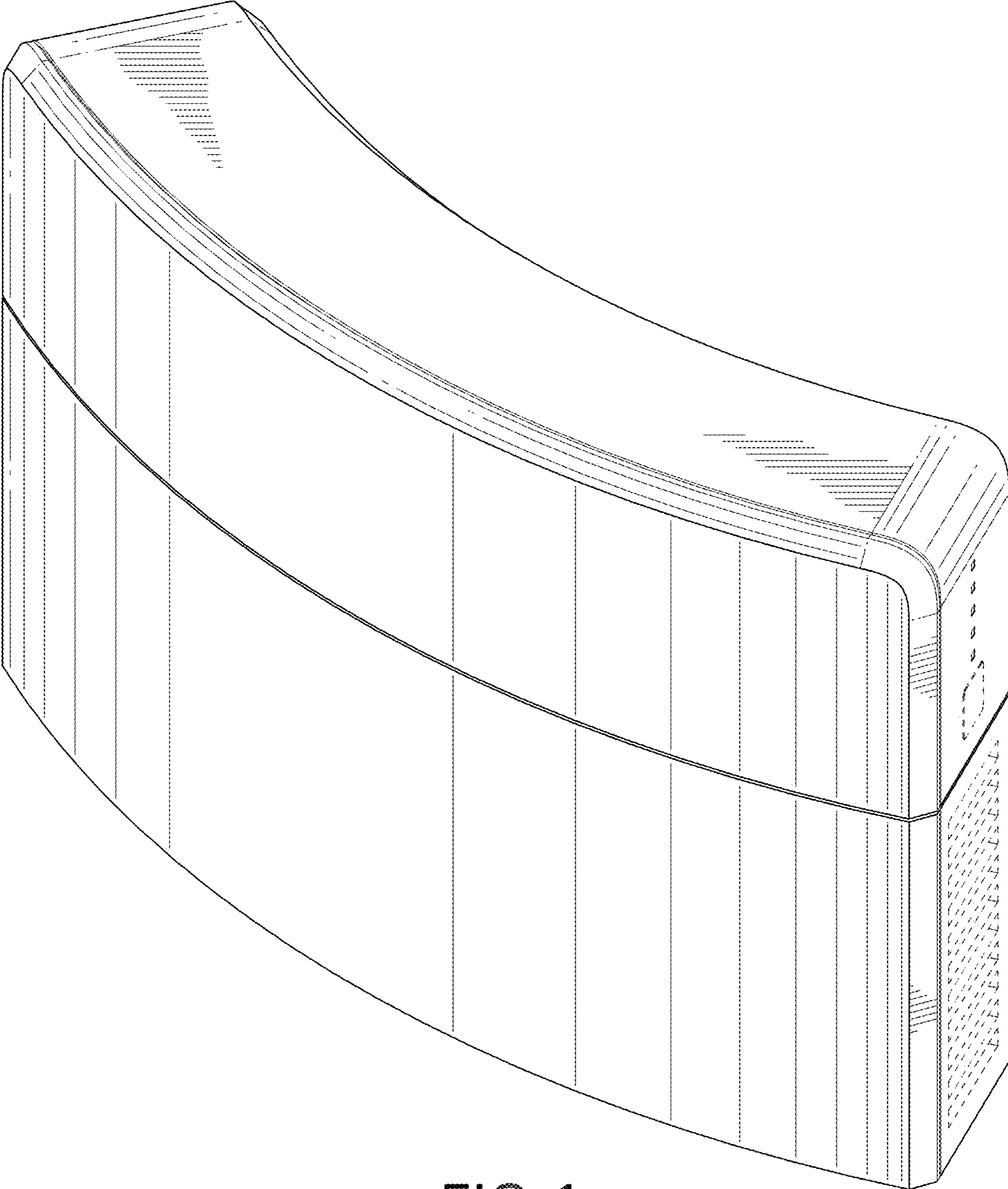


FIG. 1

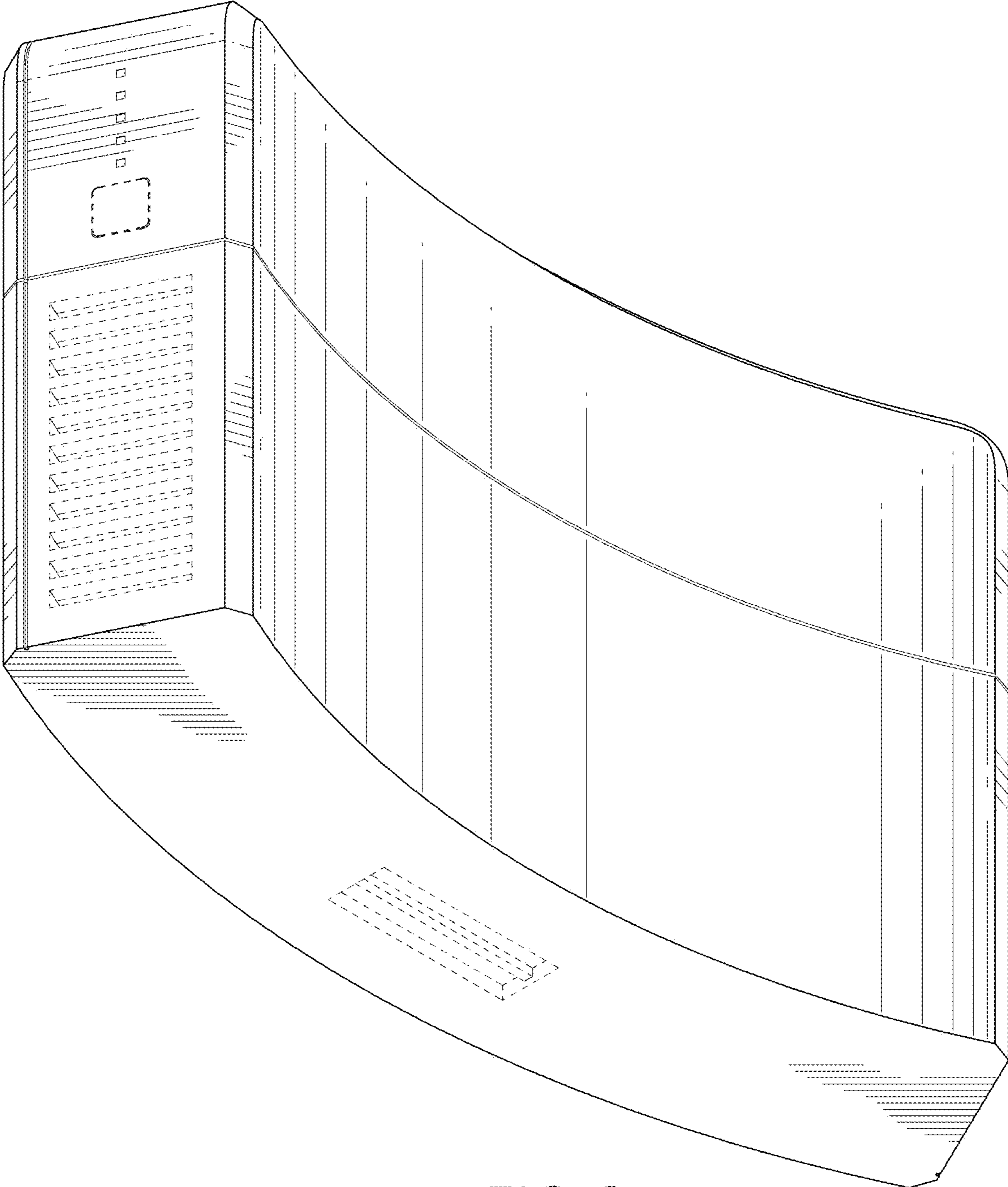


FIG.2



FIG. 3



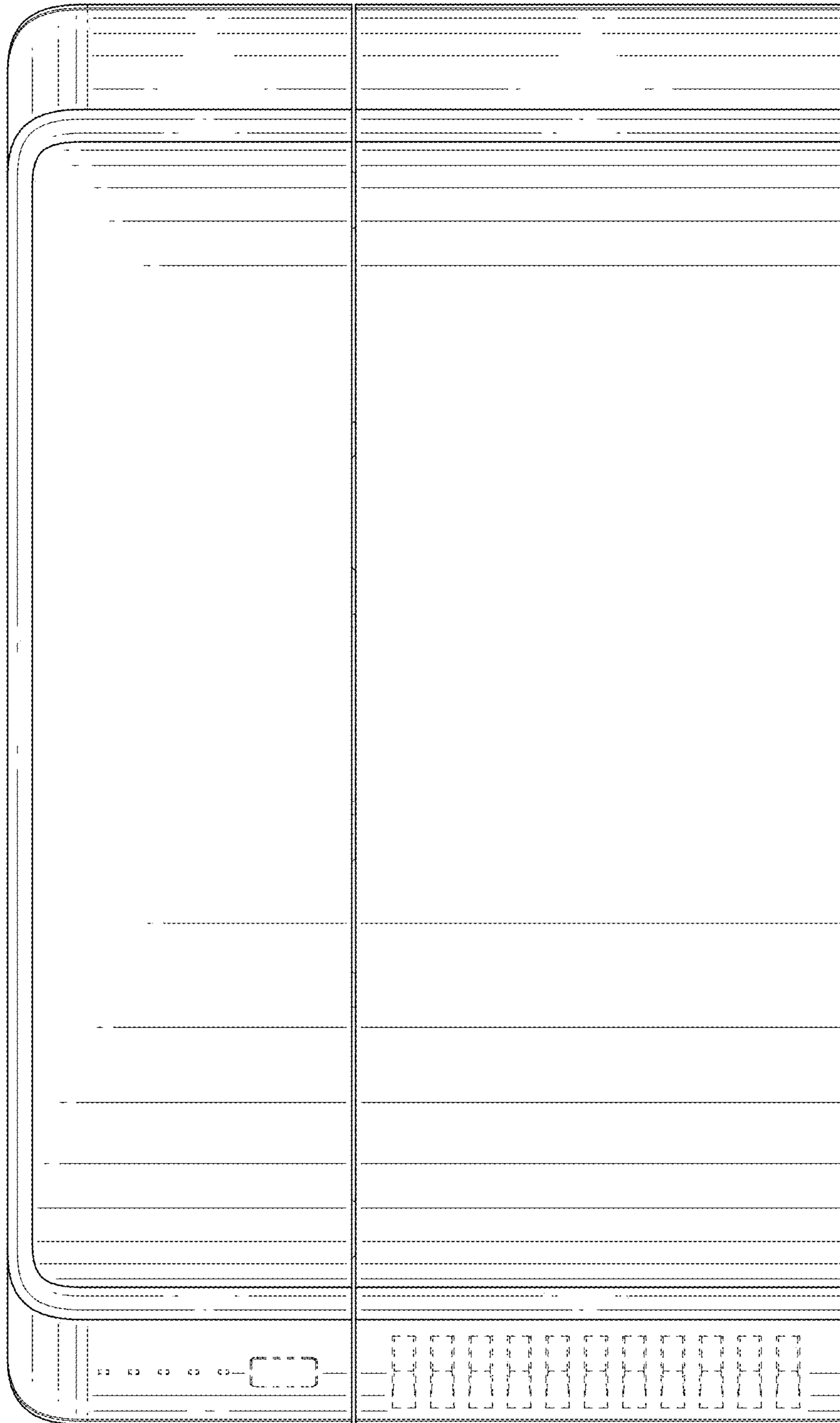


FIG. 4

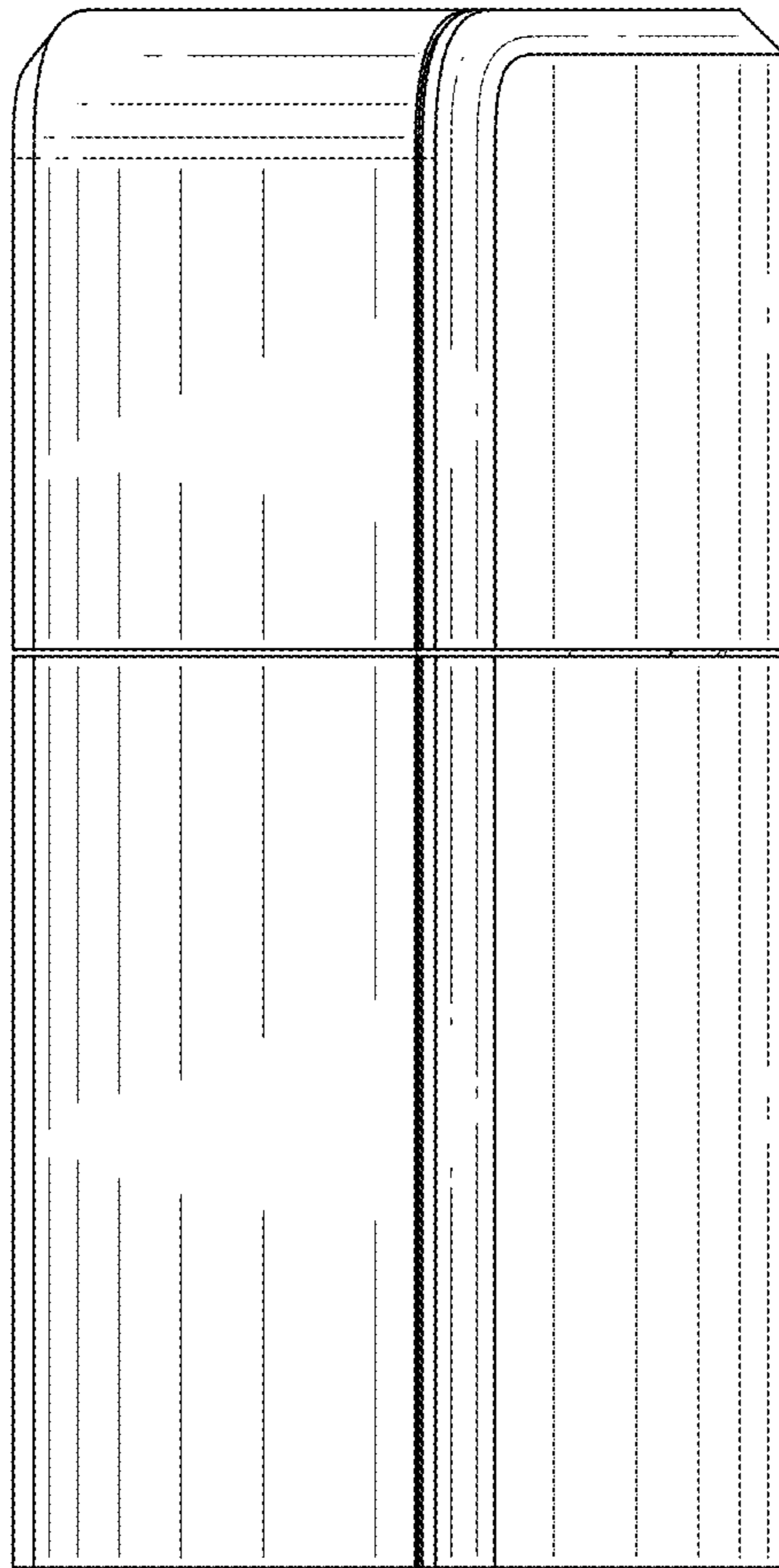


FIG. 5

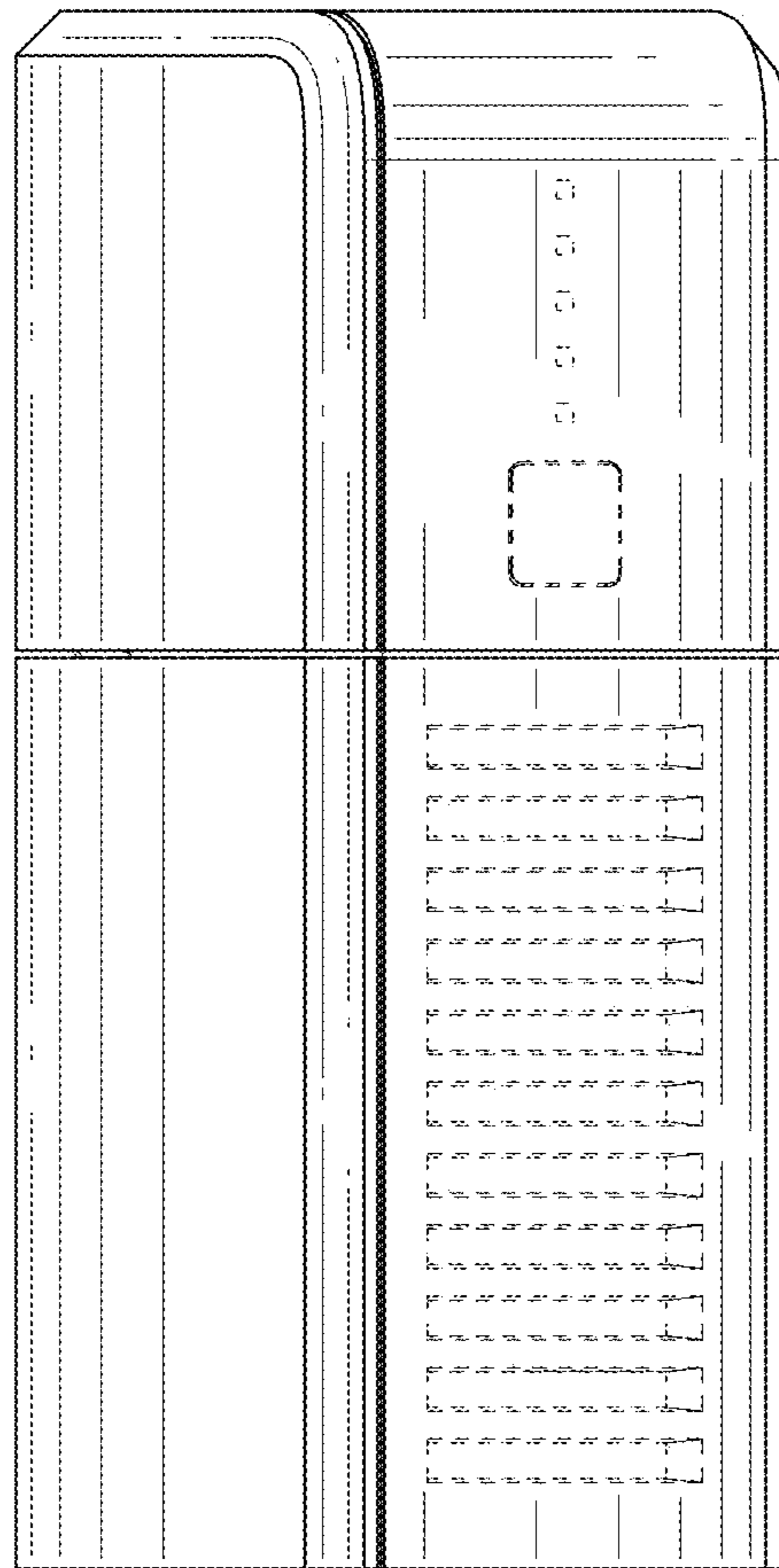


FIG.6

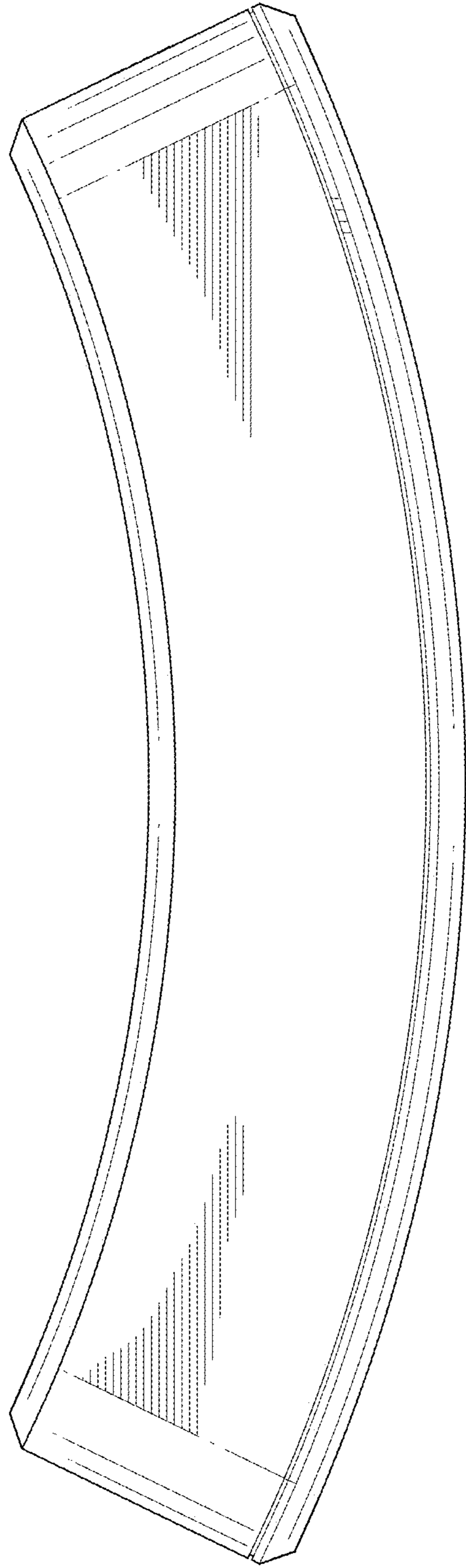


FIG. 7



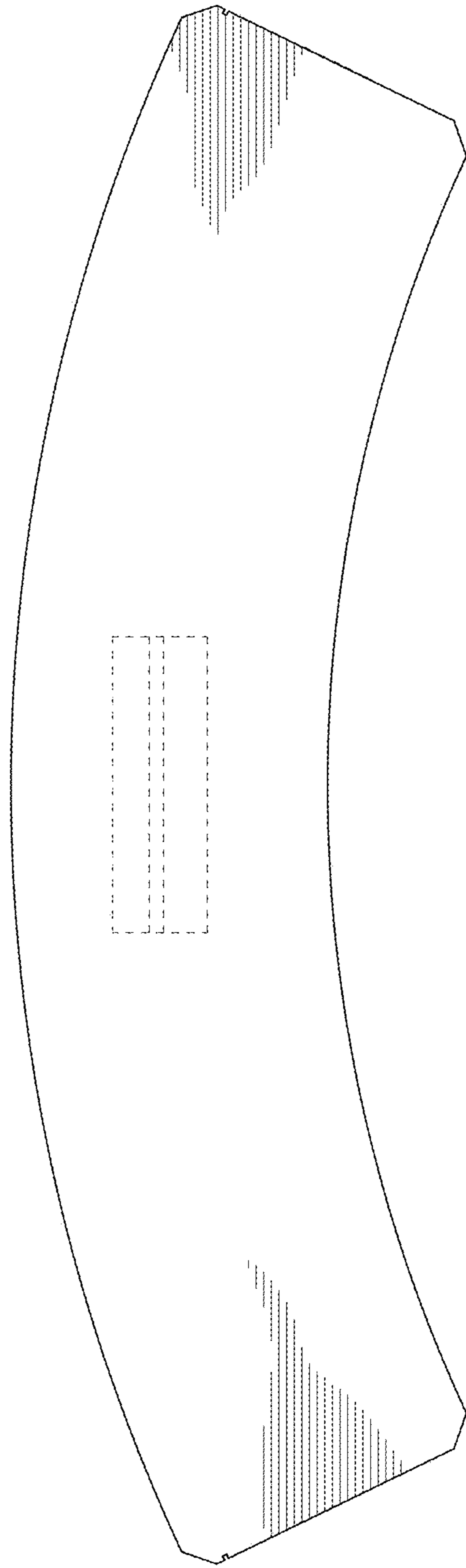


FIG. 8