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(12) **United States Design Patent** (10) **Patent No.:** **US D962,869 S**
Ohtani et al. (45) **Date of Patent:** **** Sep. 6, 2022**

(54) **RESISTOR**
(71) Applicant: **MITUTOYO CORPORATION**,
Kanagawa (JP)
(72) Inventors: **Shigeru Ohtani**, Kawasaki (JP);
Takayuki Yonezawa, Tokyo (JP); **Rie**
Arai, Kawasaki (JP); **Motohiro Osaki**,
Tokyo (JP)

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(73) Assignee: **MITUTOYO CORPORATION**,
Kawasaki (JP)

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(**) Term: **15 Years**

(Continued)

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Primary Examiner — Shawn T Gingrich

Assistant Examiner — Bryan N. Melvin

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(74) *Attorney, Agent, or Firm* — Oliff PLC

(30) **Foreign Application Priority Data**

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(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/125; D13/158**

(58) **Field of Classification Search**
USPC D8/356, 360; D10/46, 70, 74; D13/103,
D13/107-108, 123, 125, 152, 158, 160,
D13/161, 162, 162.1, 168, 171, 184, 199;
D14/140, 140.6, 299, 301, 357-358, 433,
D14/439; D15/199; D16/218; D23/411;
D34/35

CPC G01B 5/12; H01C 10/38; H01C 10/40;
H01C 10/44; H01H 13/08; H01H 13/6395
See application file for complete search history.

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(57) **CLAIM**

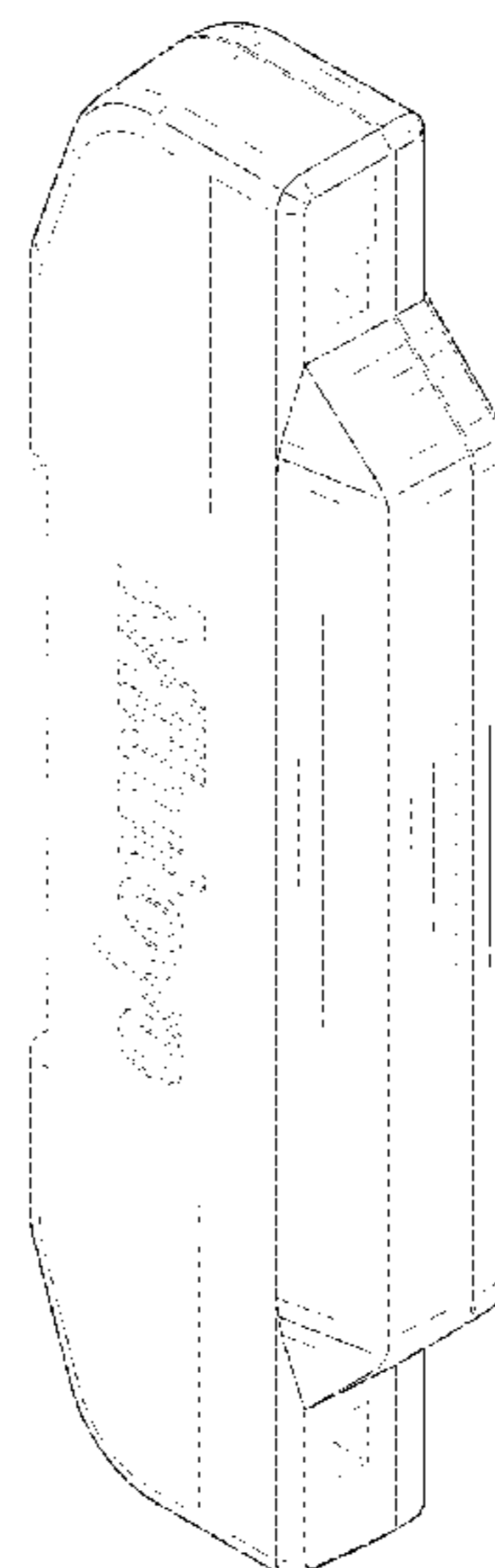
The ornamental design for a resistor, as shown and described.

DESCRIPTION

FIG. 1 is a front, left-side, top perspective view of the resistor;
FIG. 2 is a front elevational view thereof;
FIG. 3 is a rear elevational view thereof;
FIG. 4 is a left-side elevational view thereof;
FIG. 5 is a right-side elevational view thereof;
FIG. 6 is a top plan view thereof;
FIG. 7 is a bottom plan view thereof;
FIG. 8 is a rear, right-side, bottom perspective view thereof;
and,
FIG. 9 is a reference view showing the resistor in a state of use.

The broken lines in the drawings illustrate environment and portions of the resistor that form no part of the claimed design.

1 Claim, 5 Drawing Sheets



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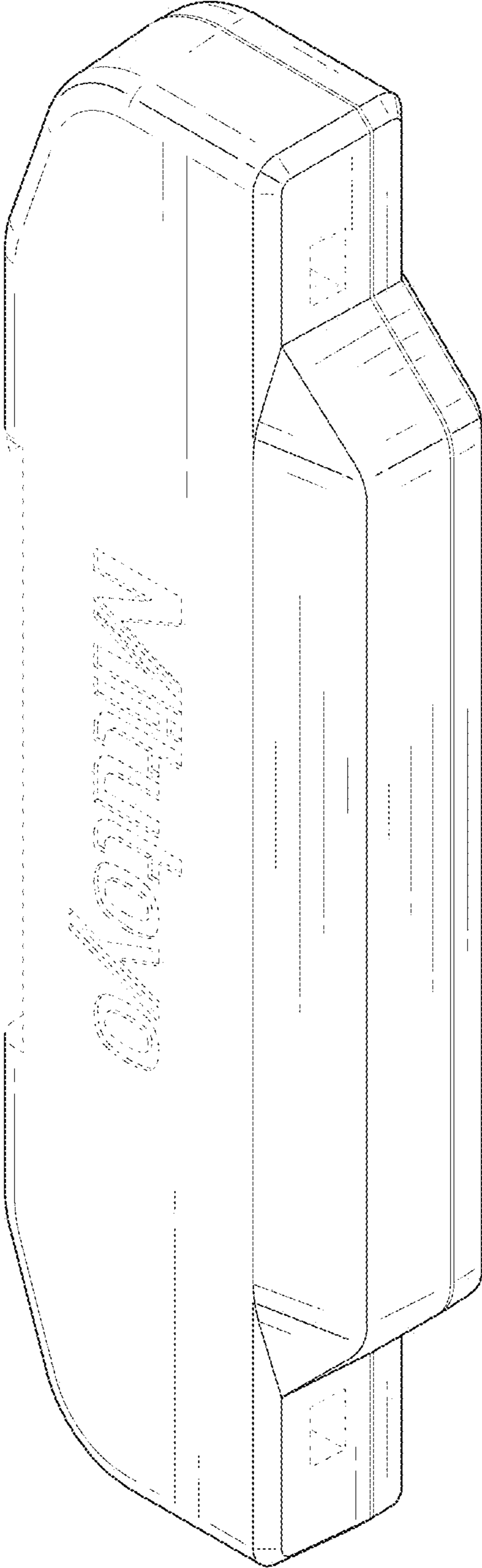


FIG. 1

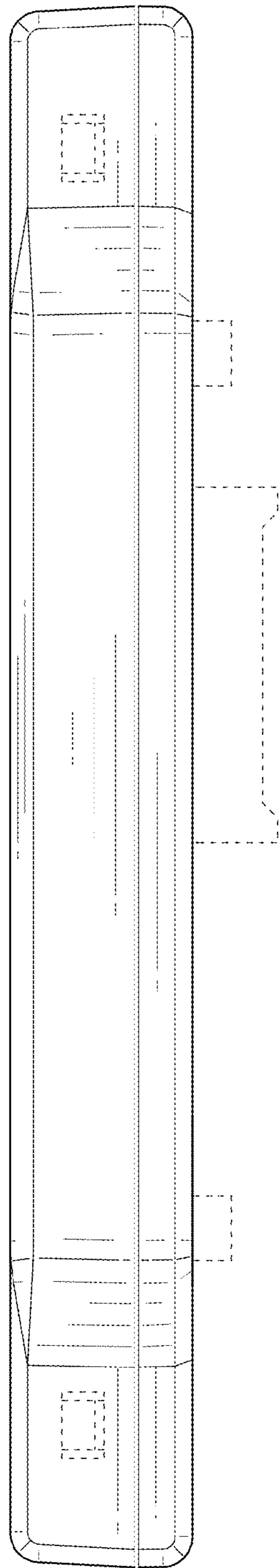


FIG. 2

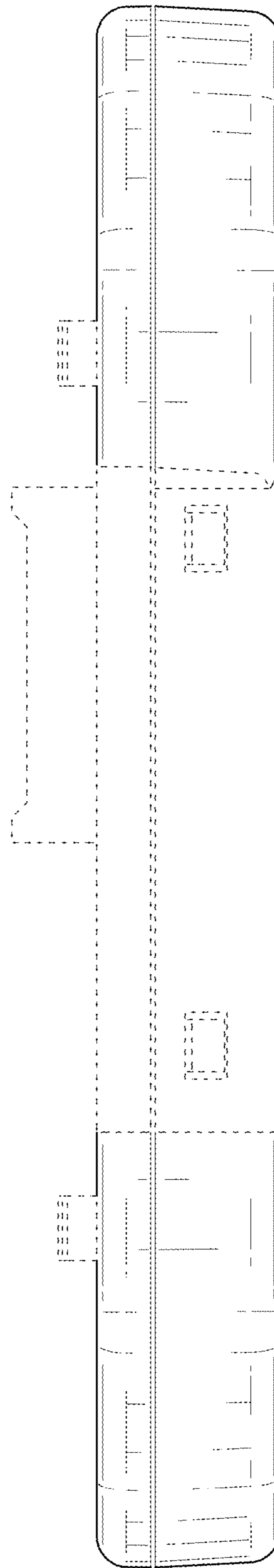


FIG. 3

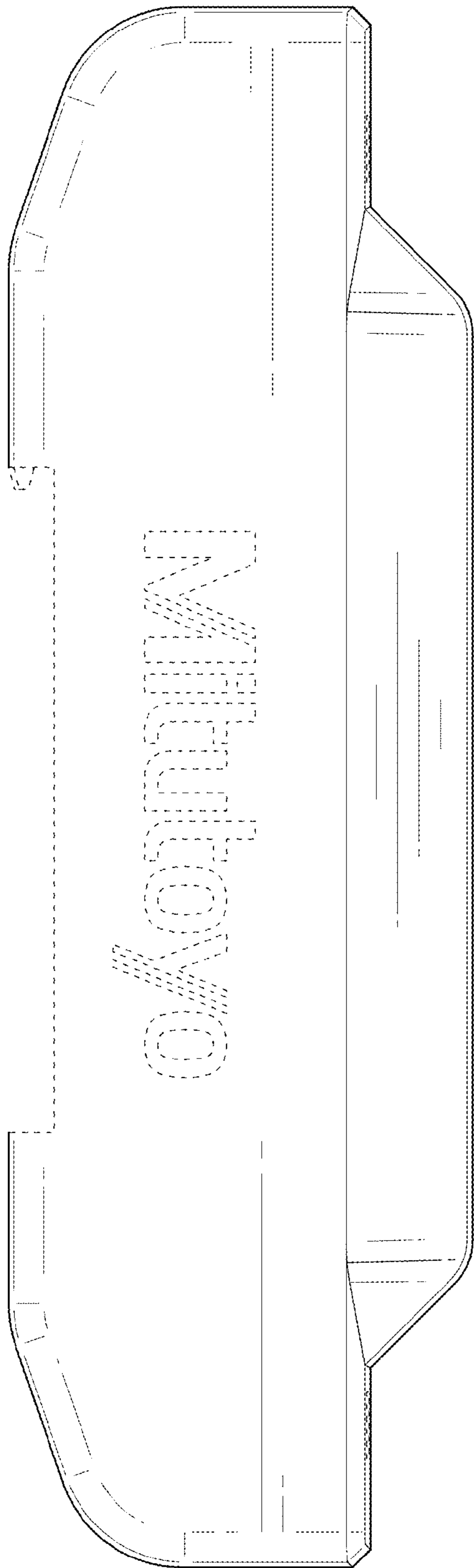


FIG. 4

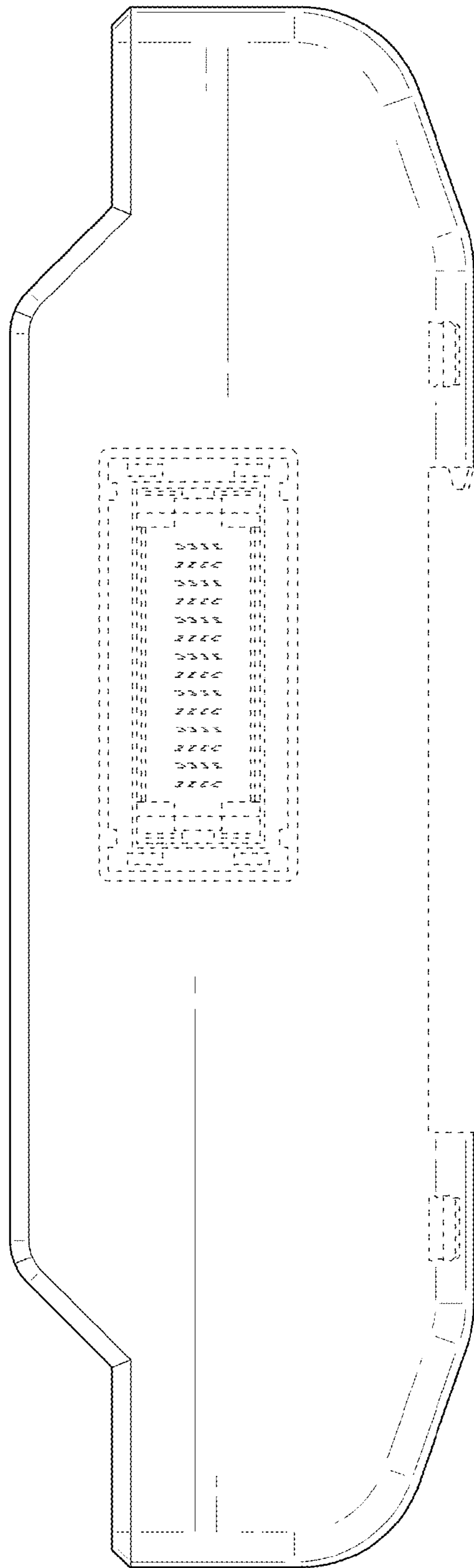


FIG. 5

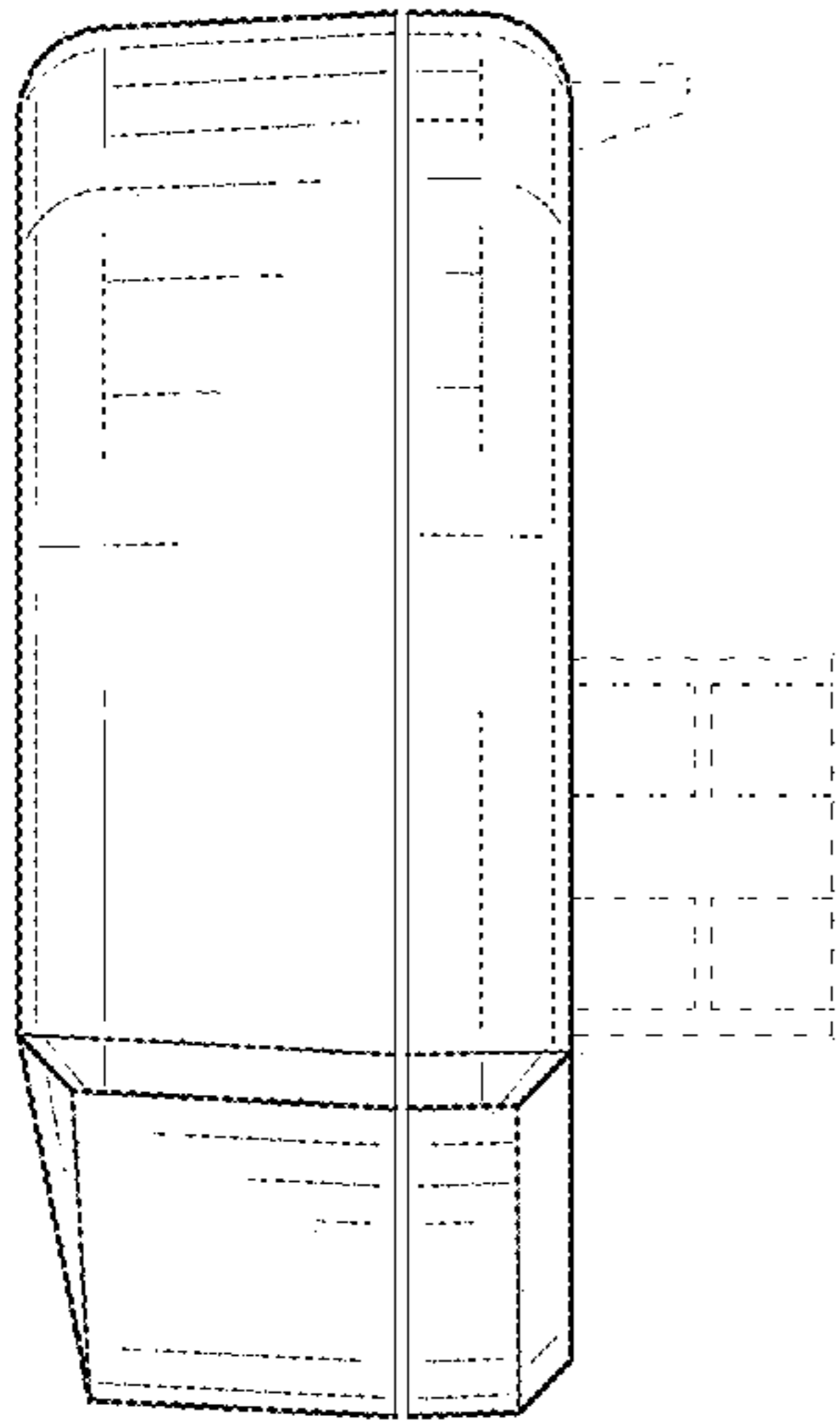


FIG. 6

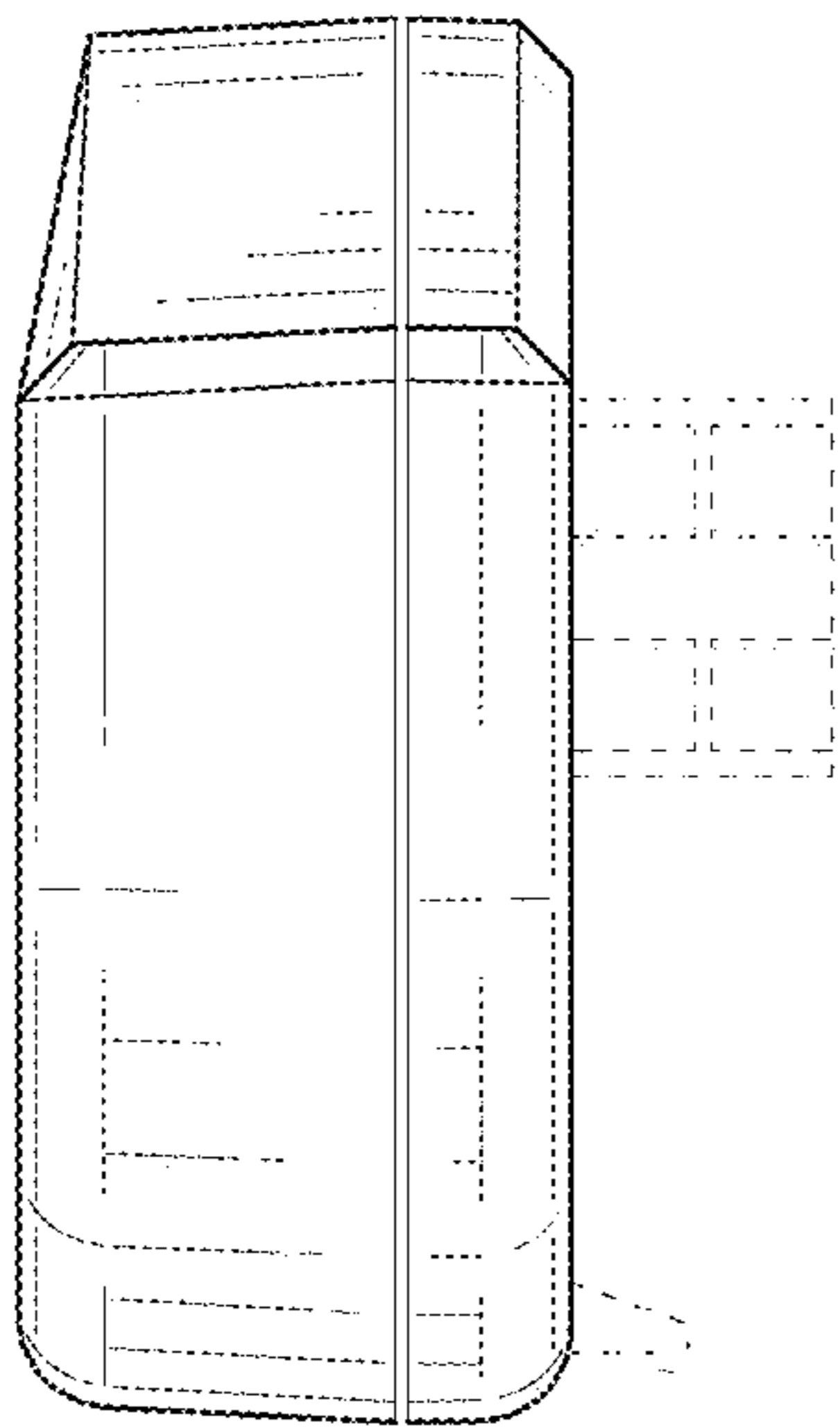


FIG. 7

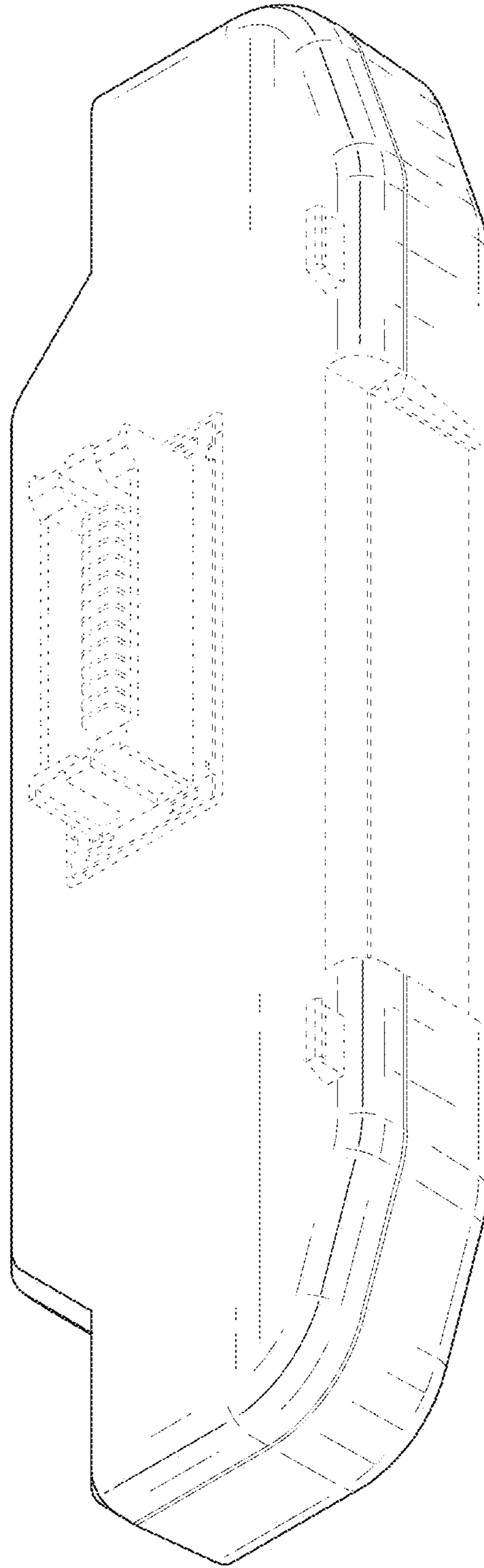


FIG. 8

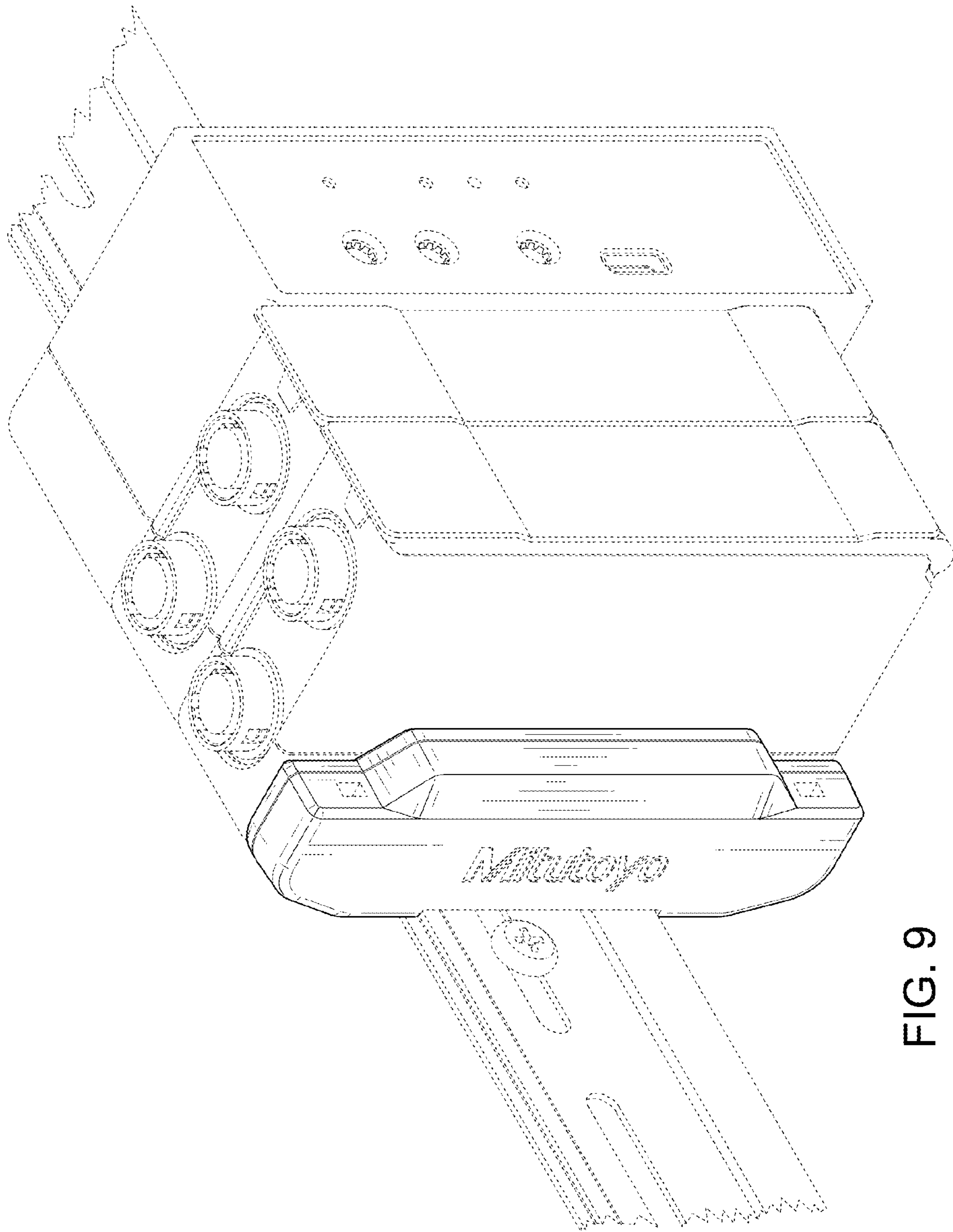


FIG. 9