



US00D958762S

(12) **United States Design Patent** (10) **Patent No.:** **US D958,762 S**
McBride et al. (45) **Date of Patent:** **** *Jul. 26, 2022**

(54) **CIRCUIT BOARD**

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(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **15 Years**

(21) Appl. No.: **29/704,997**

(22) Filed: **Sep. 9, 2019**

Related U.S. Application Data

(63) Continuation of application No. 16/564,758, filed on Sep. 9, 2019, now Pat. No. 11,251,507.

(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**

USPC D13/182; 361/748, 752, 760, 761, 807, 361/808, 736, 719, 720; 174/250, 253, 174/255, 68.1; 363/144, 147

CPC H05K 3/30; H05K 3/301; H05K 3/284; H05K 3/303; H05K 1/18; H05K 1/181; H05K 1/182; H05K 1/183; H05K 1/185; H05K 1/00; H05K 7/1417; H05K 7/02; H05K 7/12; H05K 7/06; H01L 2924/14

See application file for complete search history.

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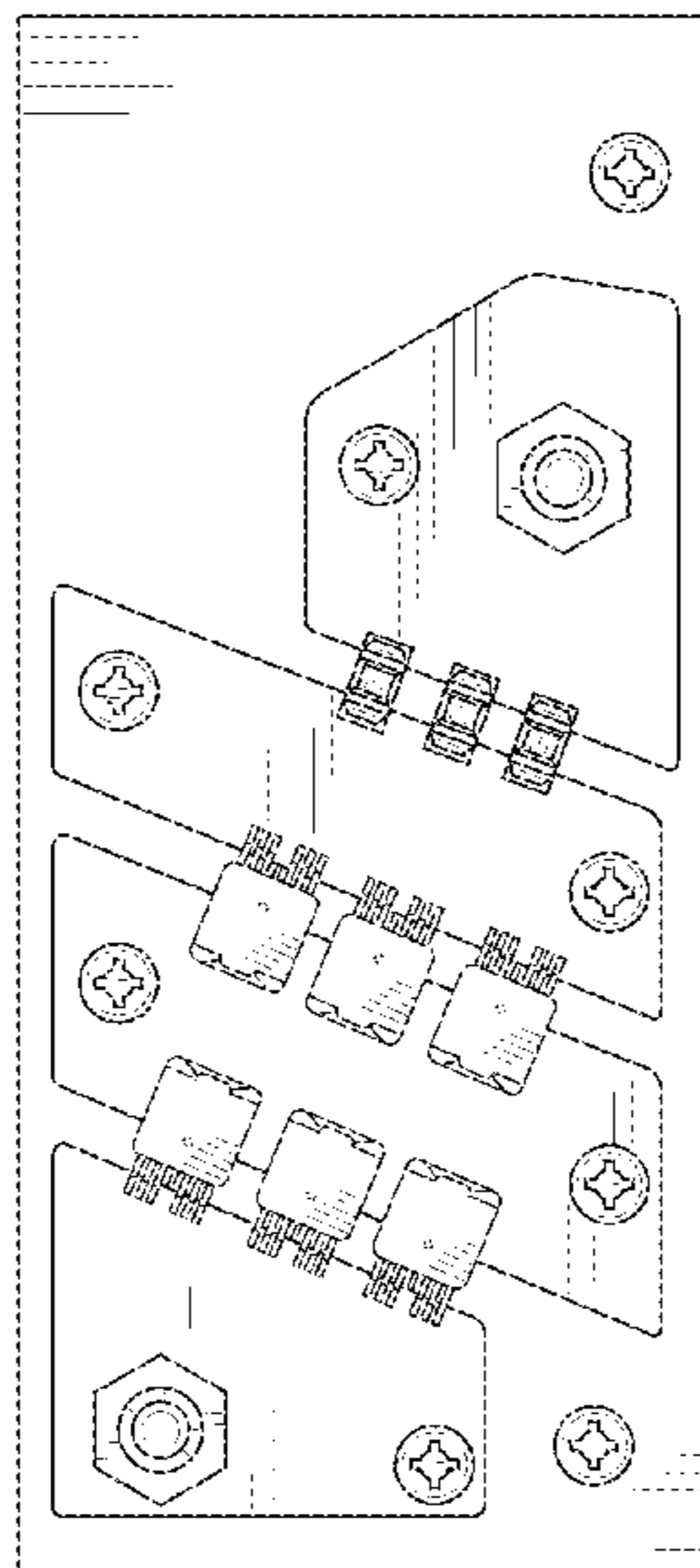
(57) **CLAIM**

The ornamental design for a circuit board, as shown and described.

DESCRIPTION

The sole FIGURE is a front elevational view of a circuit board, showing our new design. The broken lines shown in the drawings depict environmental structure or portions of the article that form no part of the claimed design.

1 Claim, 1 Drawing Sheet



(56)

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