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(12) **United States Design Patent** (10) **Patent No.:** **US D955,893 S**
Cohen et al. (45) **Date of Patent:** **** Jun. 28, 2022**

(54) **CLOCK DISPLAY** D730,869 S * 6/2015 Lee D14/214
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Abraham Genauer, Washington, DC D770,312 S * 11/2016 Puzder D10/46.1
(US); **Andrew Wingert**, Smithfield, KY D802,447 S * 11/2017 Puzder D10/46.1
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(Continued)

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(**) Term: **15 Years**

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(22) Filed: **Feb. 7, 2020**

(51) **LOC (13) Cl.** **10-01**

(52) **U.S. Cl.**
USPC **D10/2; D10/15; D10/46.1**

(58) **Field of Classification Search**
USPC D10/1-5, 14-15, 22-23, 40-41, 46.1;
D14/204, 216; D20/19, 20, 21, 42
CPC G04B 19/00; G04C 19/02; G04C 17/00;
G04C 17/02; G04G 11/00; G04G 9/0082;
G04G 9/0088; G04G 9/02; G07C 1/28
See application file for complete search history.

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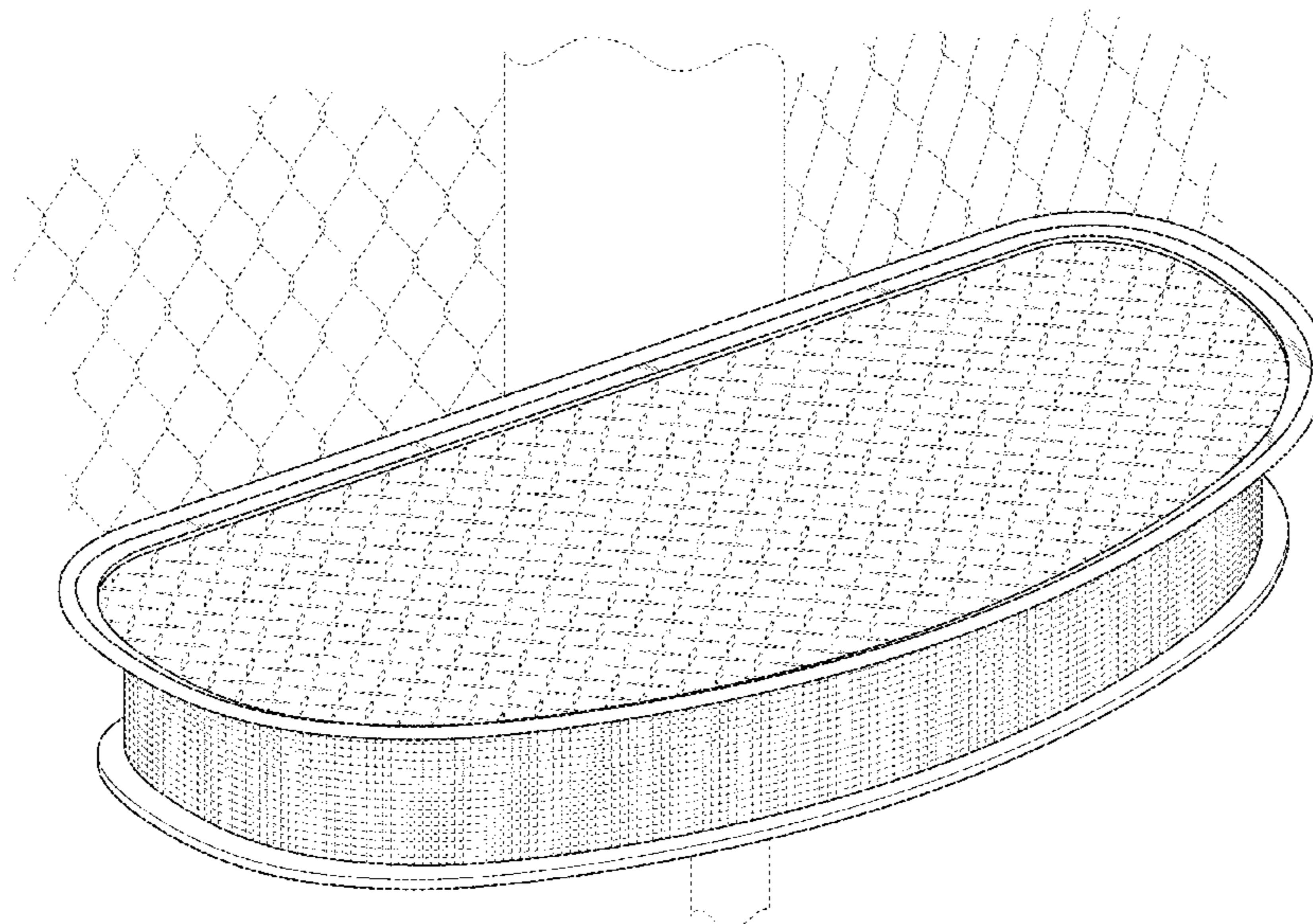
(57) **CLAIM**

We claim the ornamental design for a clock display, as shown and described.

DESCRIPTION

FIG. 1 is a top view of a clock display, shown located adjacent a competition area;
FIG. 2 is a front perspective view of the clock display, shown located adjacent the competition area of FIG. 1;
FIG. 3 is a top view of the clock display;
FIG. 4 is a bottom view of the clock display of FIG. 3;
FIG. 5 is a front view of the clock display of FIG. 3;
FIG. 6 is a rear view of the clock display of FIG. 3;
FIG. 7 is a left side view of the clock display of FIG. 3; and,
FIG. 8 is a right side view of the clock display of FIG. 3.
The broken lines in the drawings illustrate portions of the clock display that form no part of the claimed design. The broken lines showing a competition area illustrate environment and form no part of the claimed design.

1 Claim, 5 Drawing Sheets



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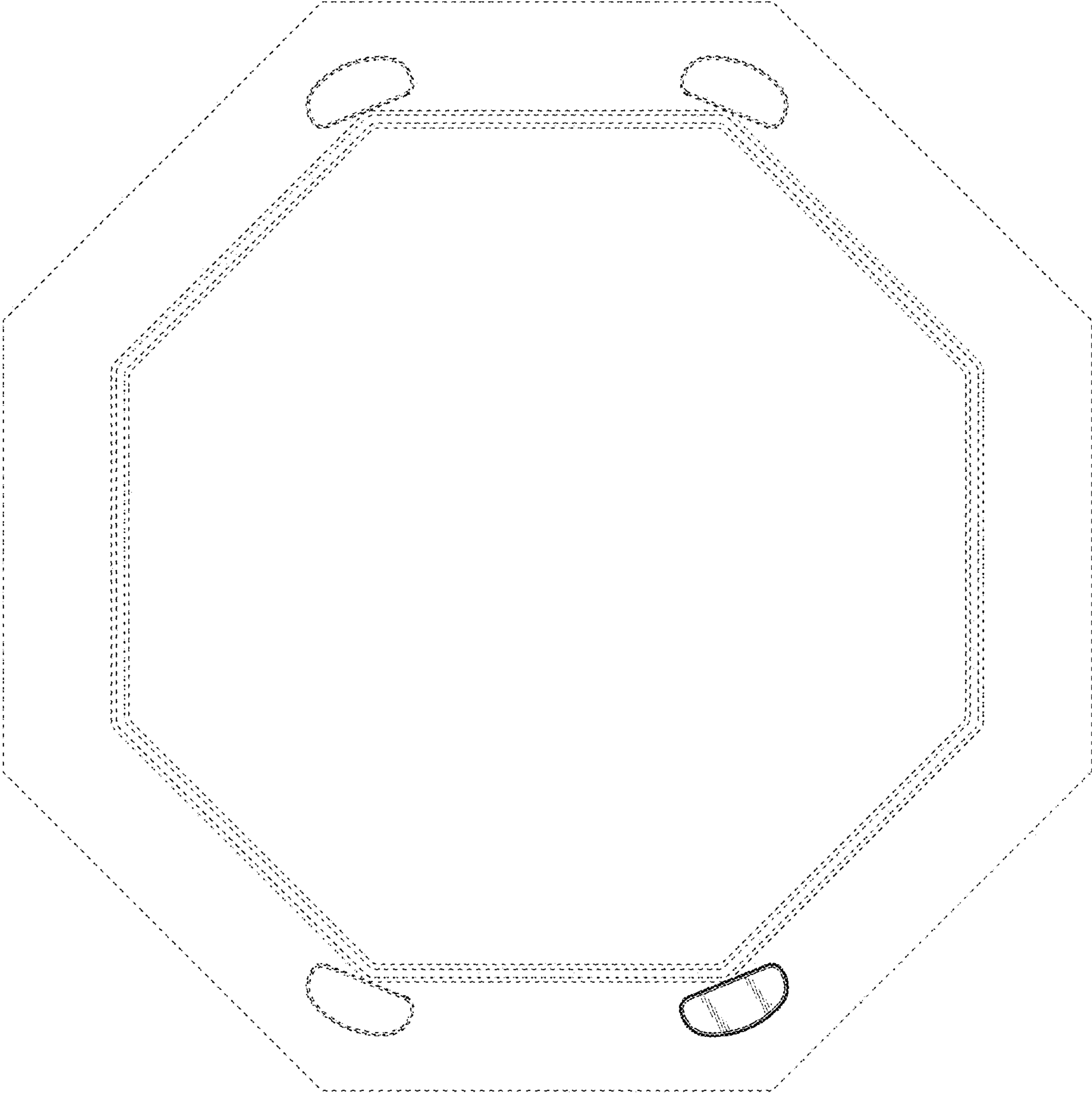


FIG. 1

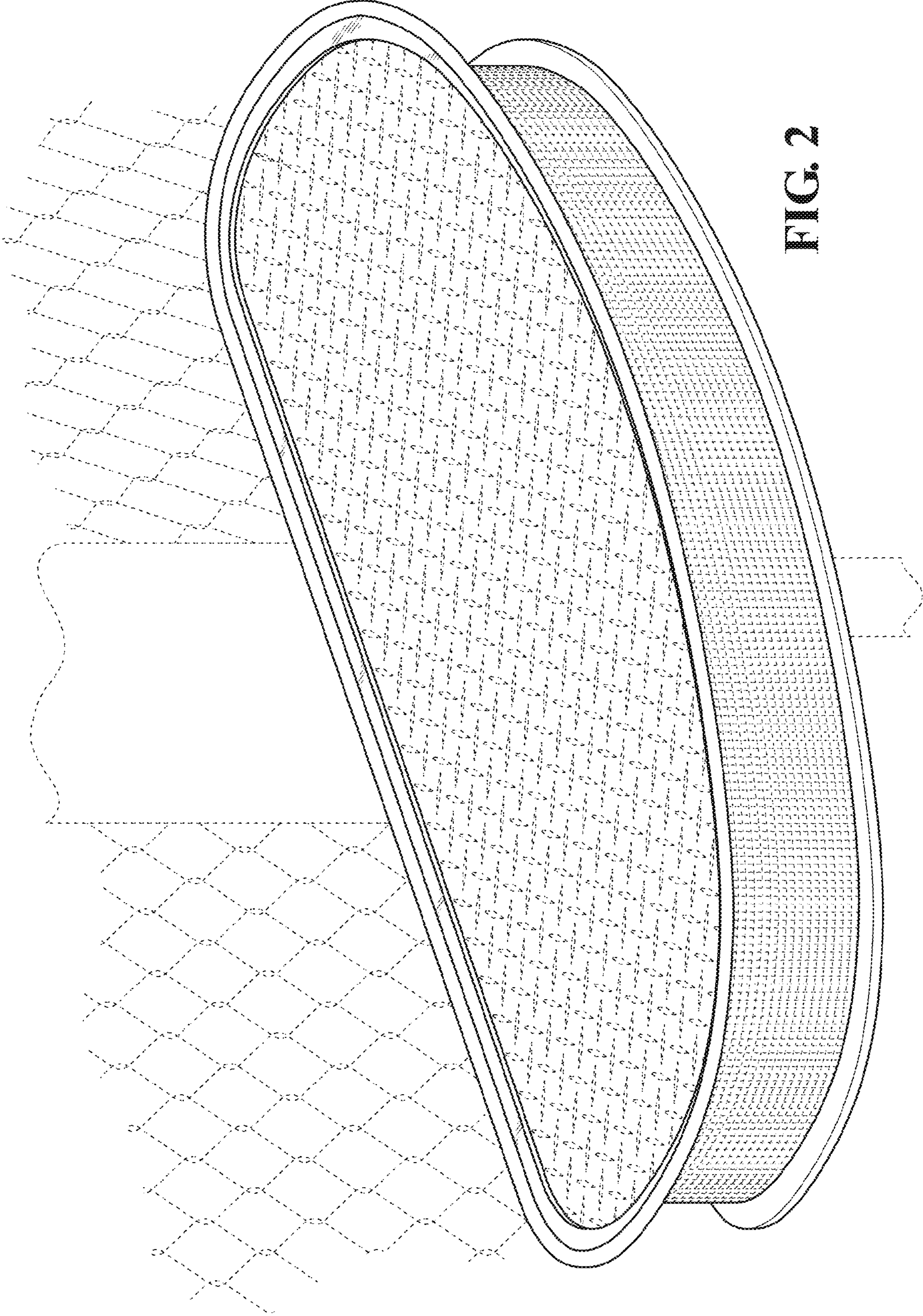


FIG. 2

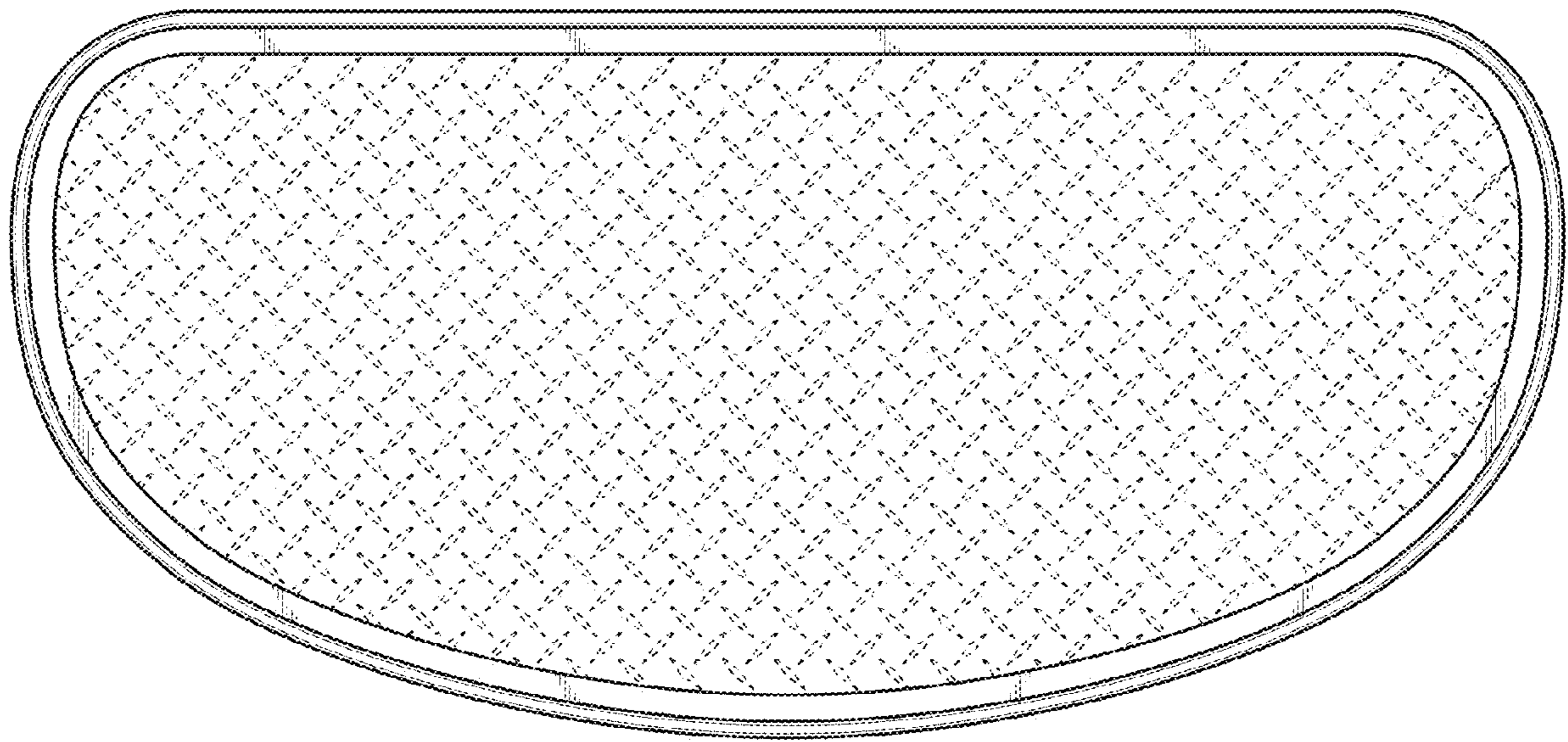


FIG. 3

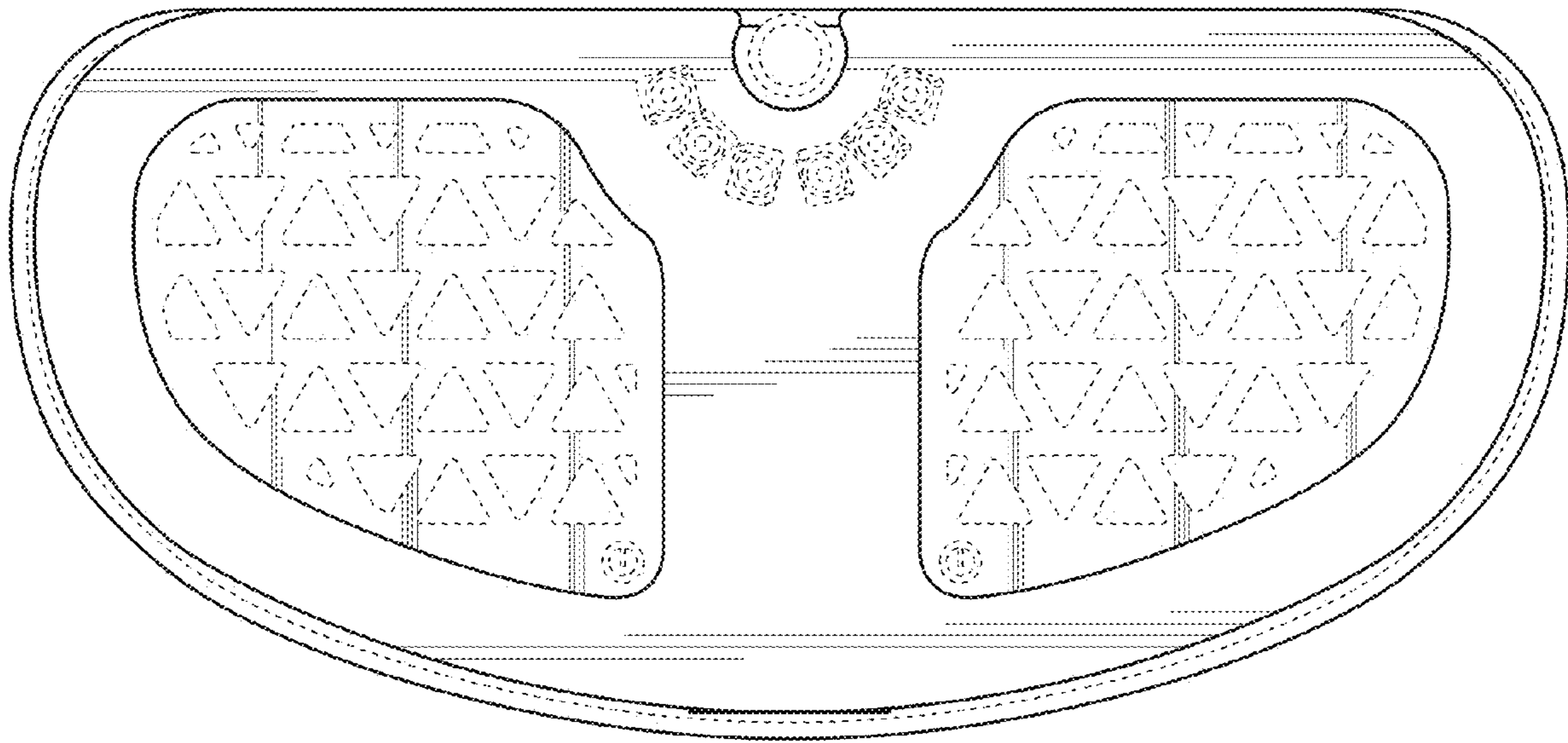


FIG. 4

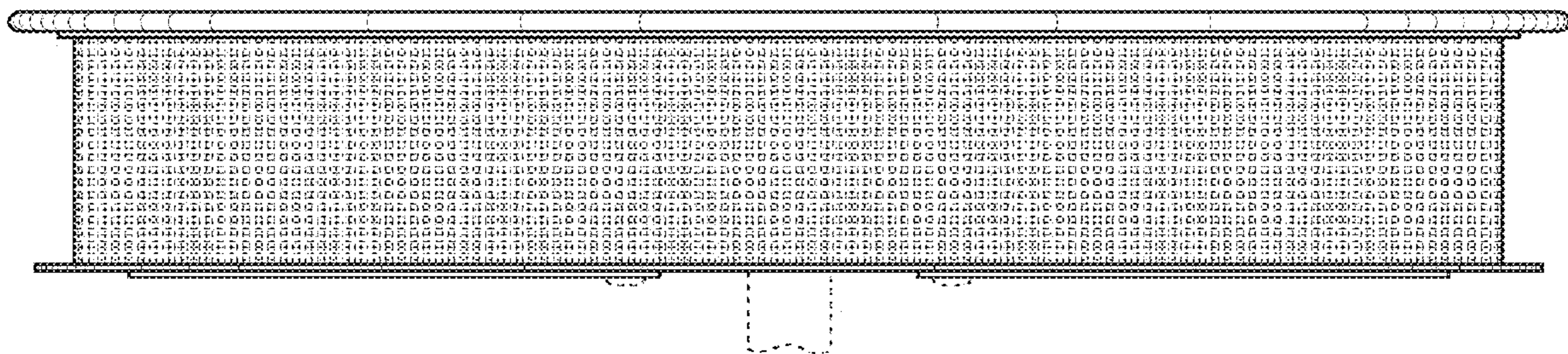


FIG. 5

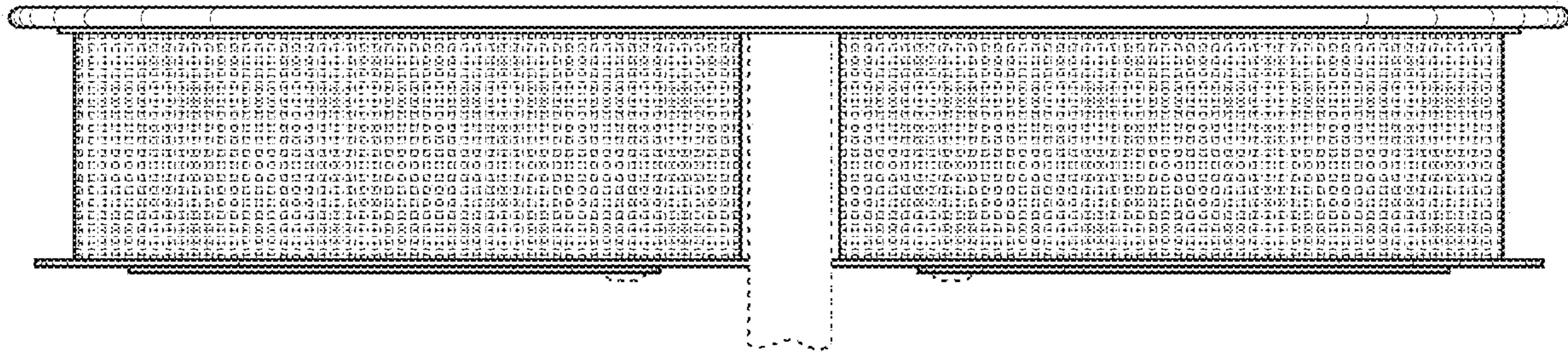


FIG. 6

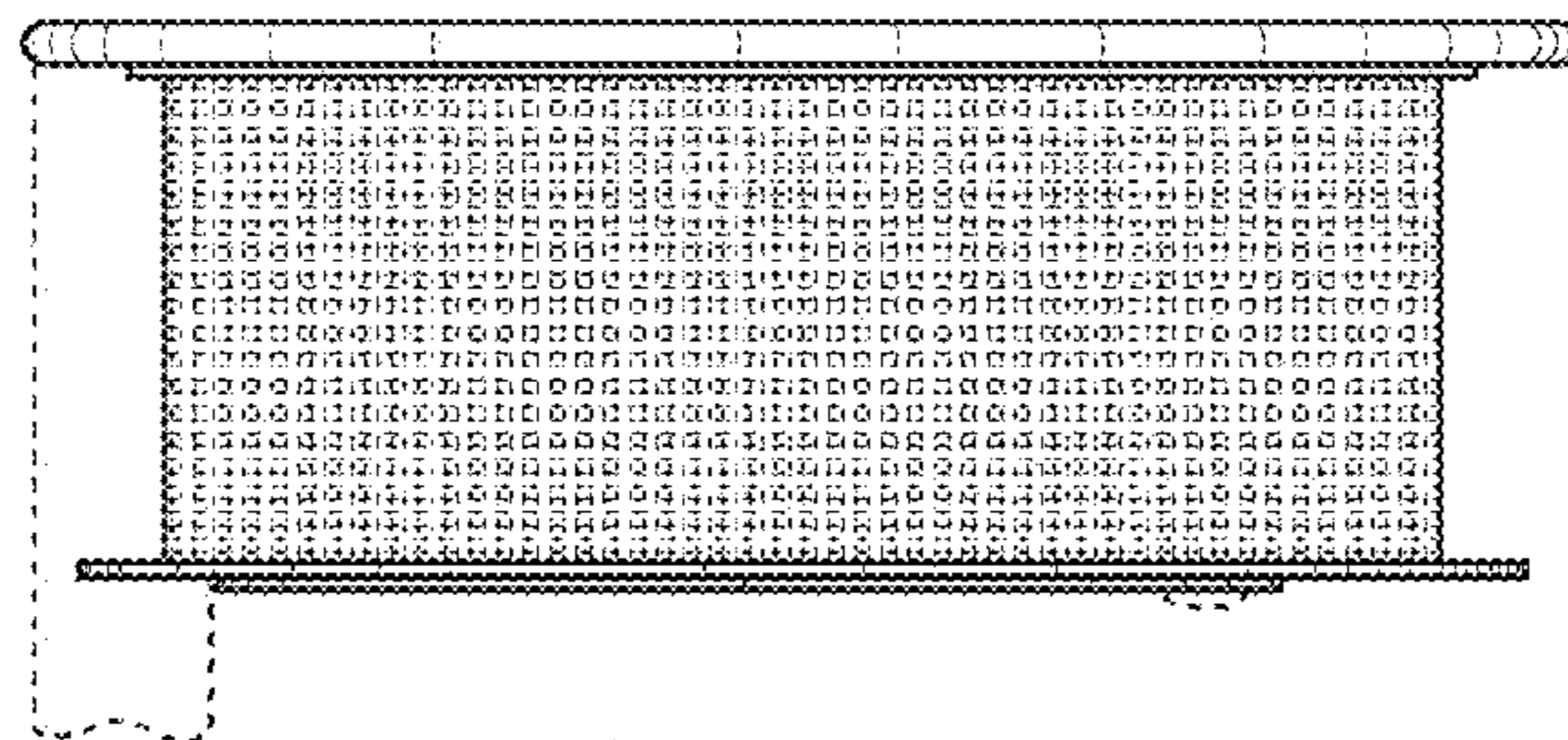


FIG. 7

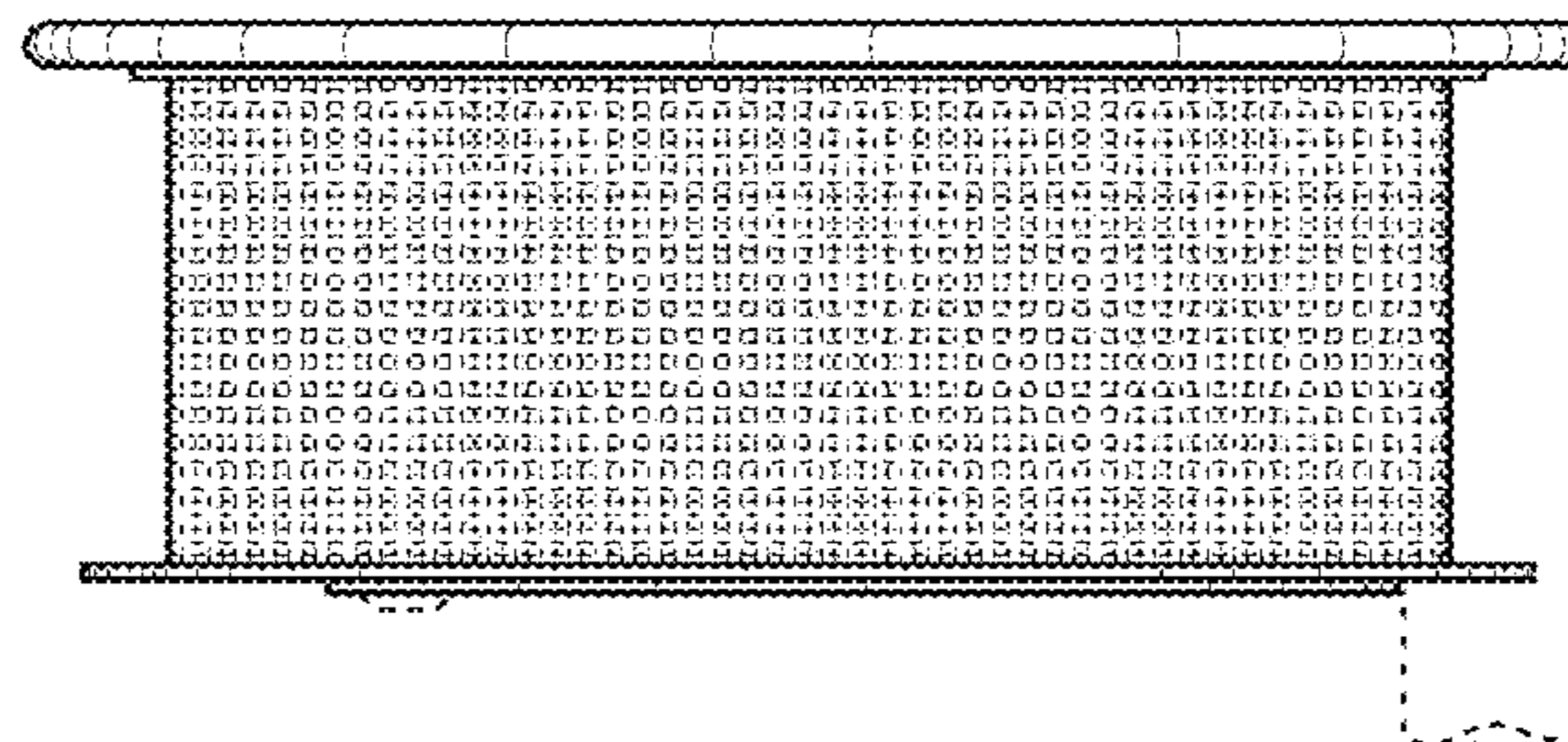


FIG. 8