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(12) **United States Design Patent**
Andersson et al.

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(54) **PROGRAMMABLE LOGIC CONTROLLER**

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- (**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/162.1**

(58) **Field of Classification Search**
USPC D14/300–302, 308, 310–311, 313,
D14/348–349, 351, 354, 439; D13/162,
D13/162.1, 184
CPC G06F 1/183; G05B 9/02; G05B 19/05;
G05B 19/04; G05B 19/056; H05K
7/1462; H05K 7/1467; H05K 7/1474
See application file for complete search history.

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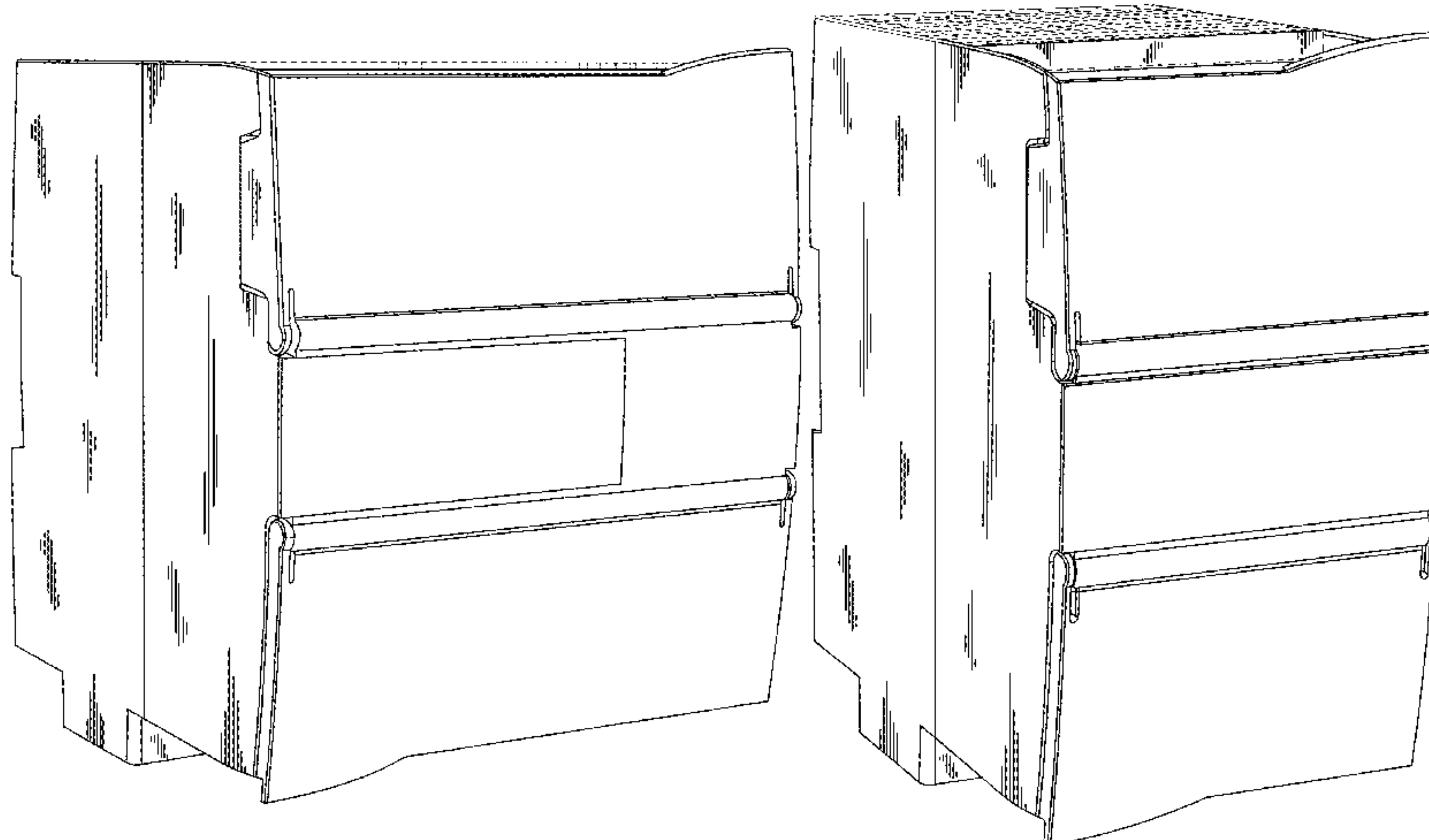
(57) **CLAIM**

The ornamental design for a programmable logic controller, as shown and described.

DESCRIPTION

FIG. 1 is a front view of a first embodiment of a programmable logic controller, showing our new design; FIG. 2 is a rear view thereof; FIG. 3 is a top view thereof; FIG. 4 is a bottom view thereof; FIG. 5 is a right side view thereof; FIG. 6 is a left side view thereof; FIG. 7 is a front and left side perspective view thereof; FIG. 8 is a front view of a second embodiment of a programmable logic controller, showing our new design; FIG. 9 is a rear view thereof; FIG. 10 is a top view thereof; FIG. 11 is a bottom view thereof; FIG. 12 is a right side view thereof; FIG. 13 is a left side view thereof; and, FIG. 14 is a front and left side perspective view thereof. Portions of the article shown in broken lines form no part of the claimed design.

1 Claim, 14 Drawing Sheets



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FIG. 1

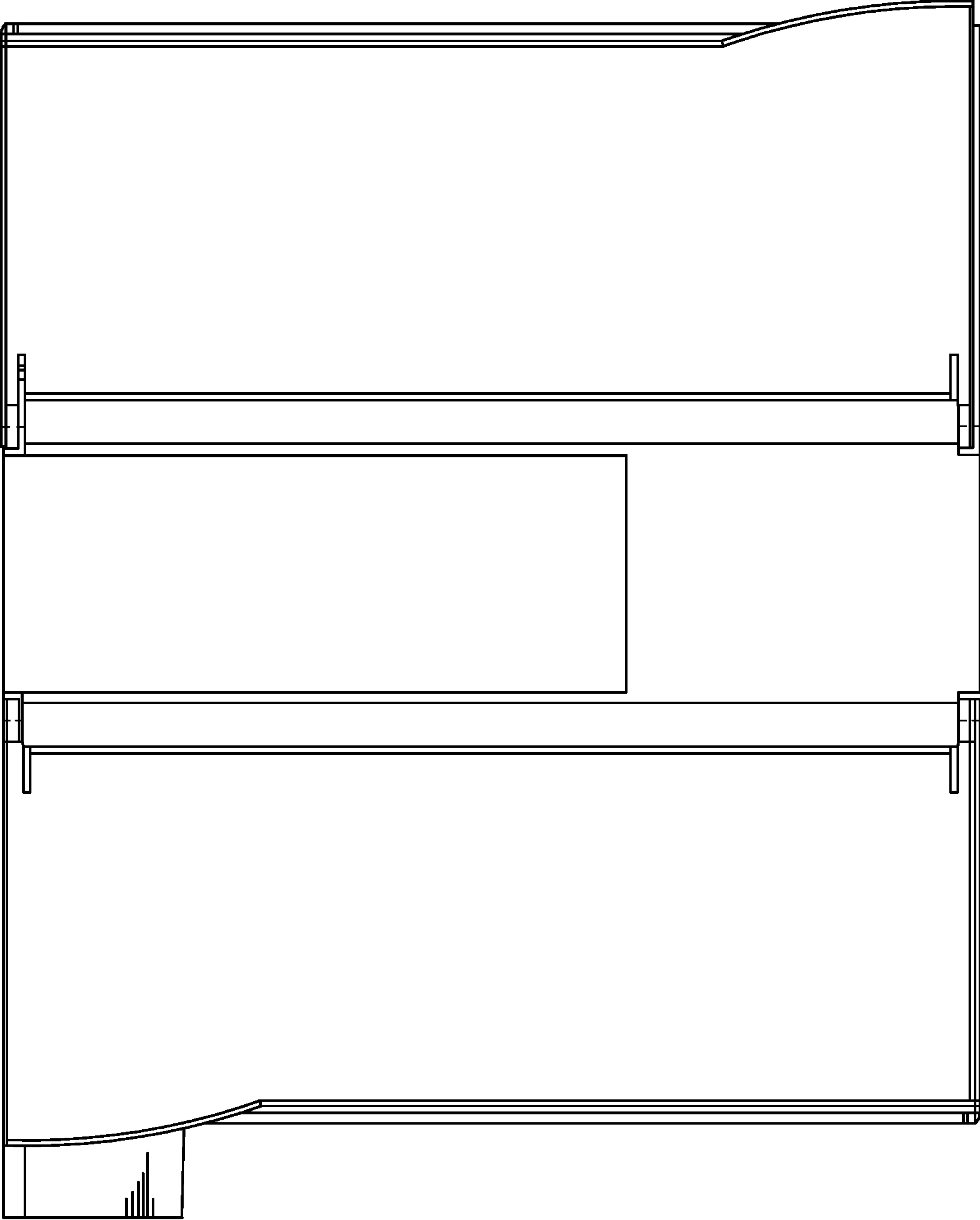


FIG. 2

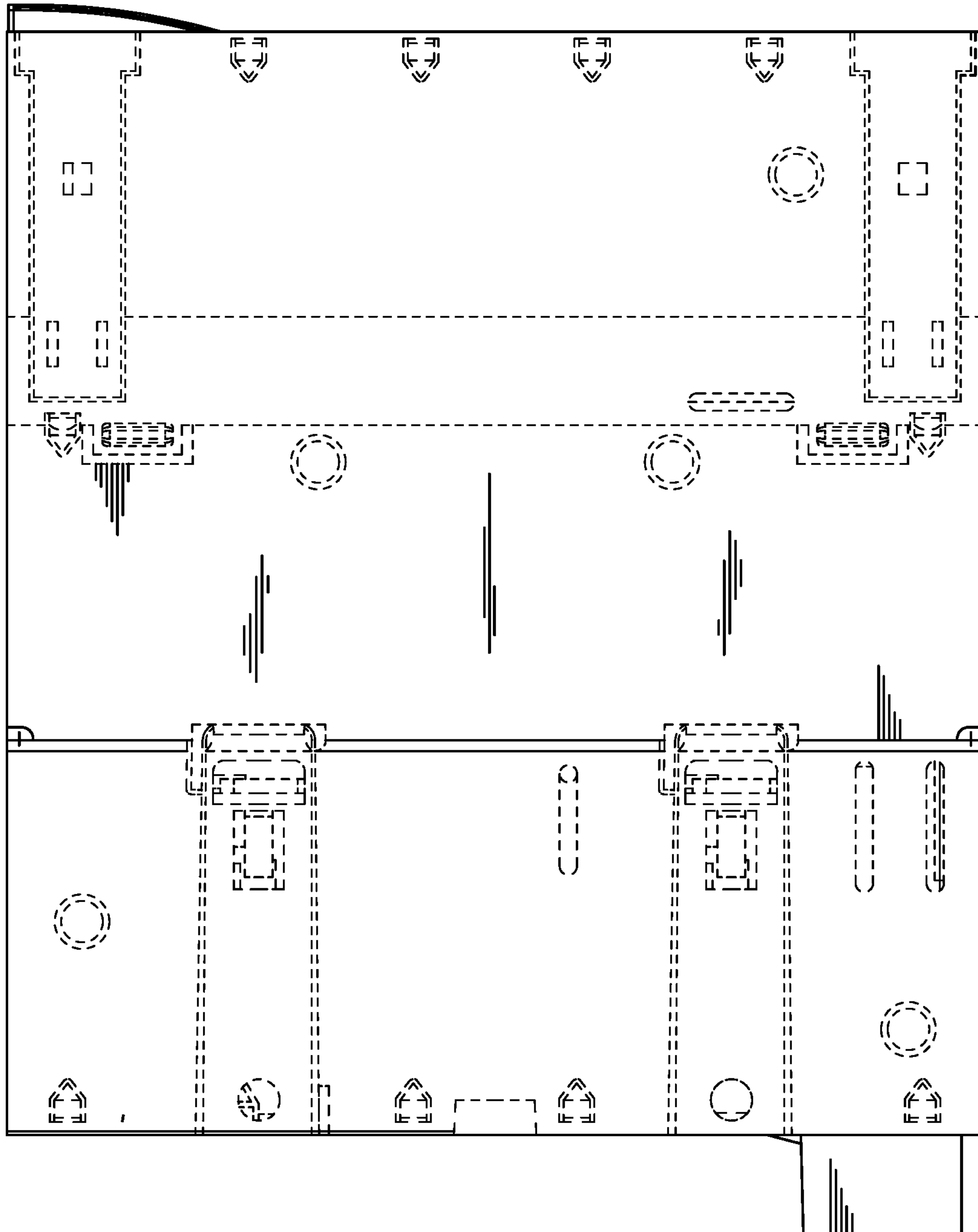


FIG. 3

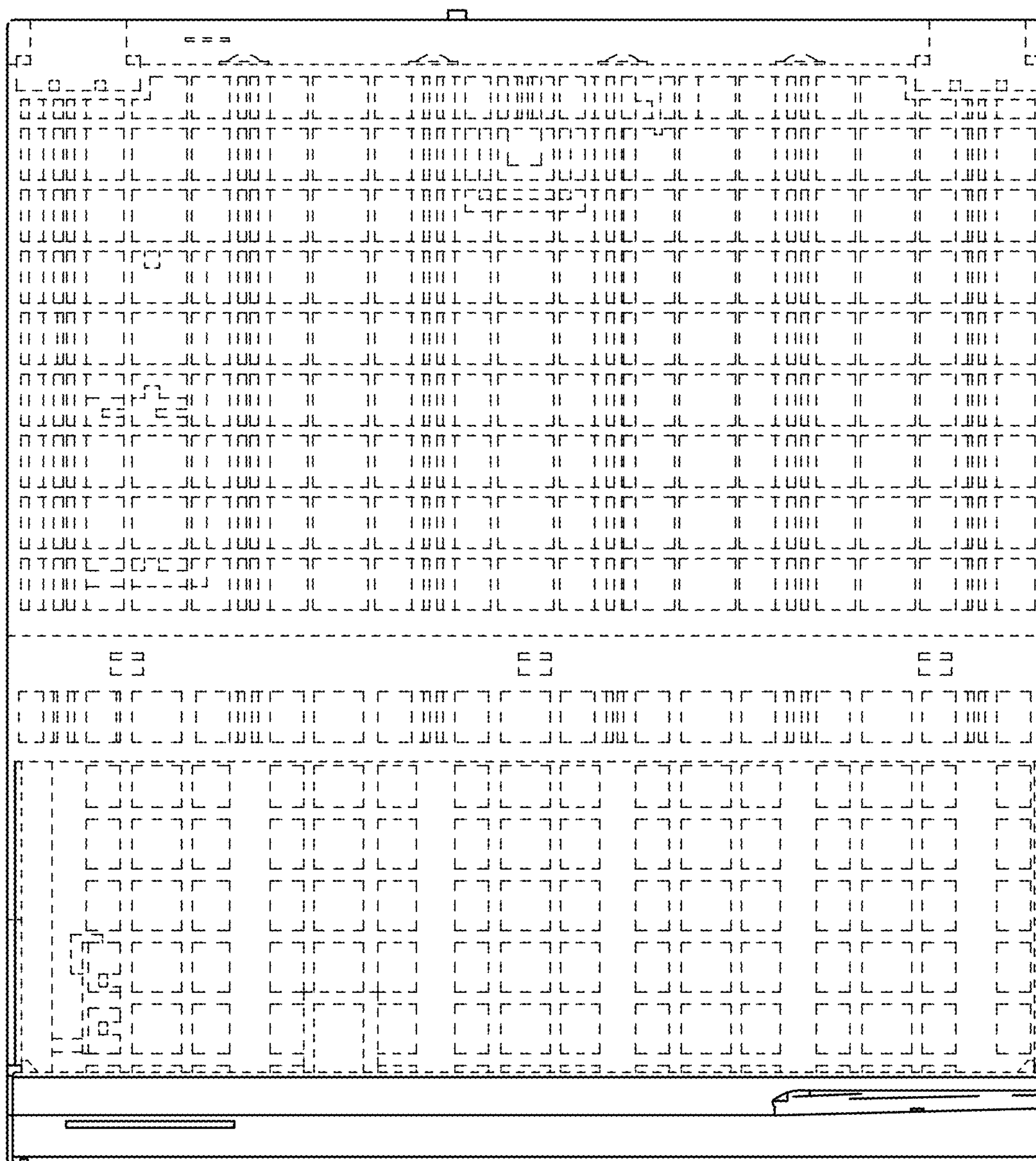


FIG. 5

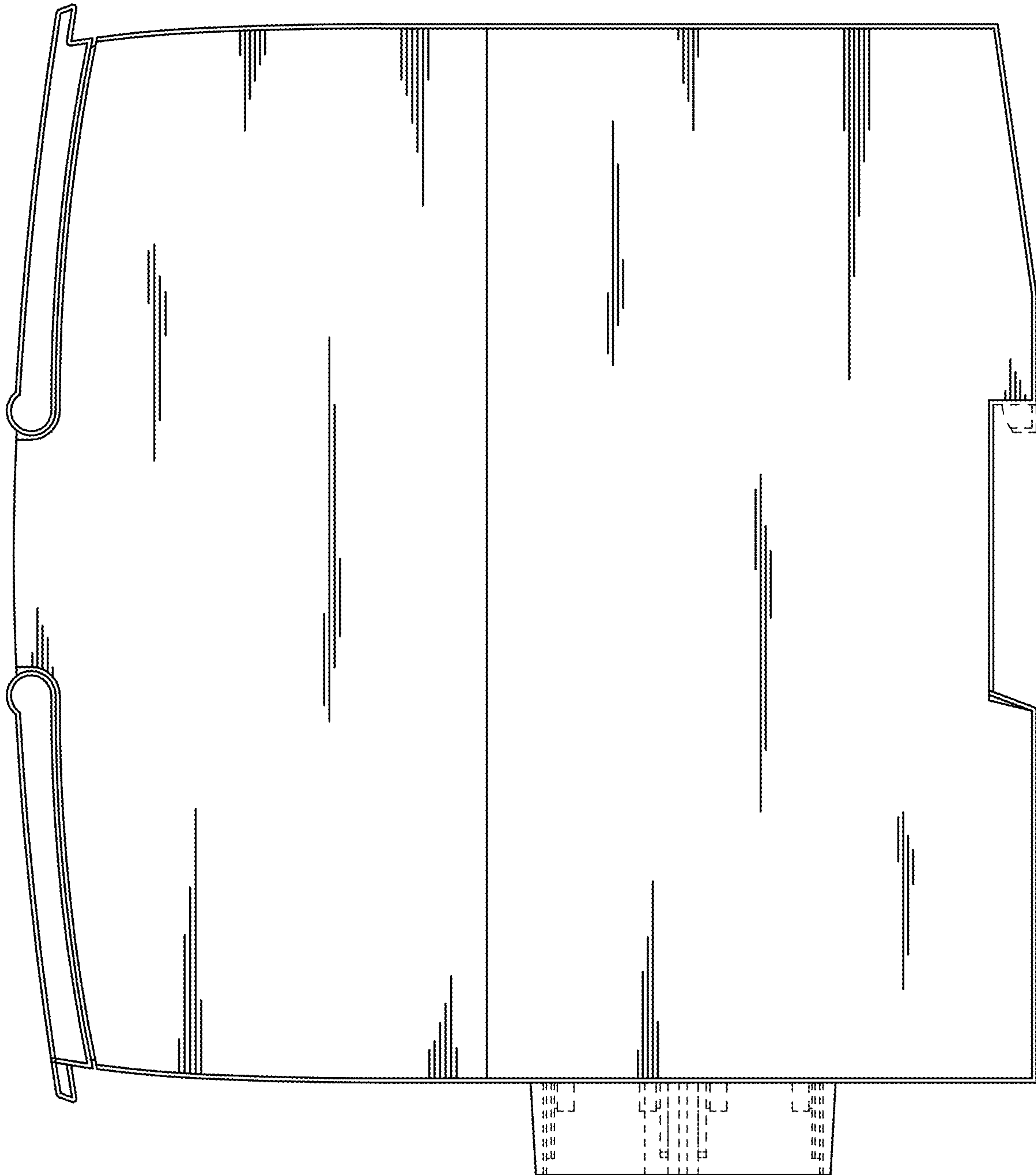


FIG. 6

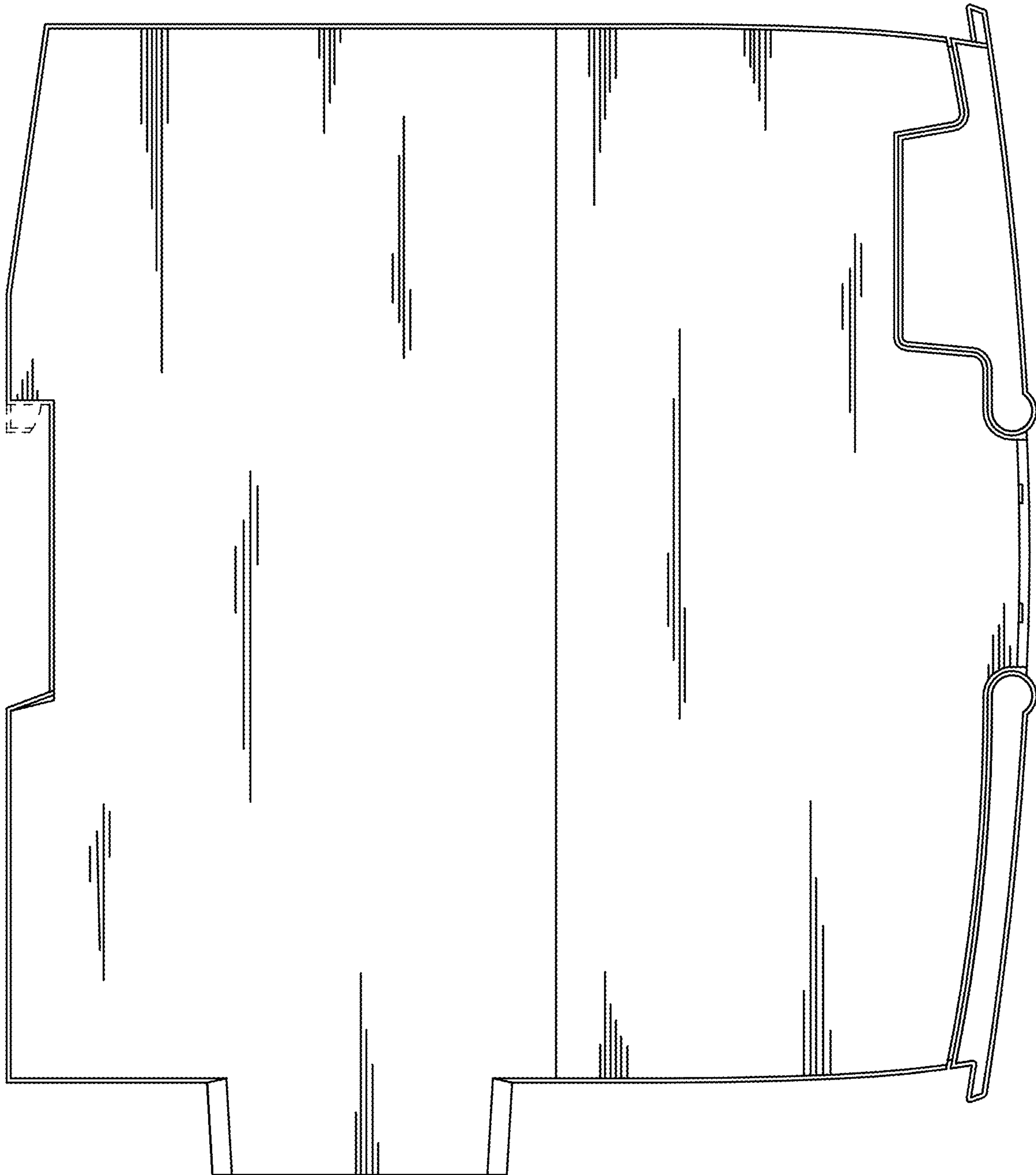


FIG. 7

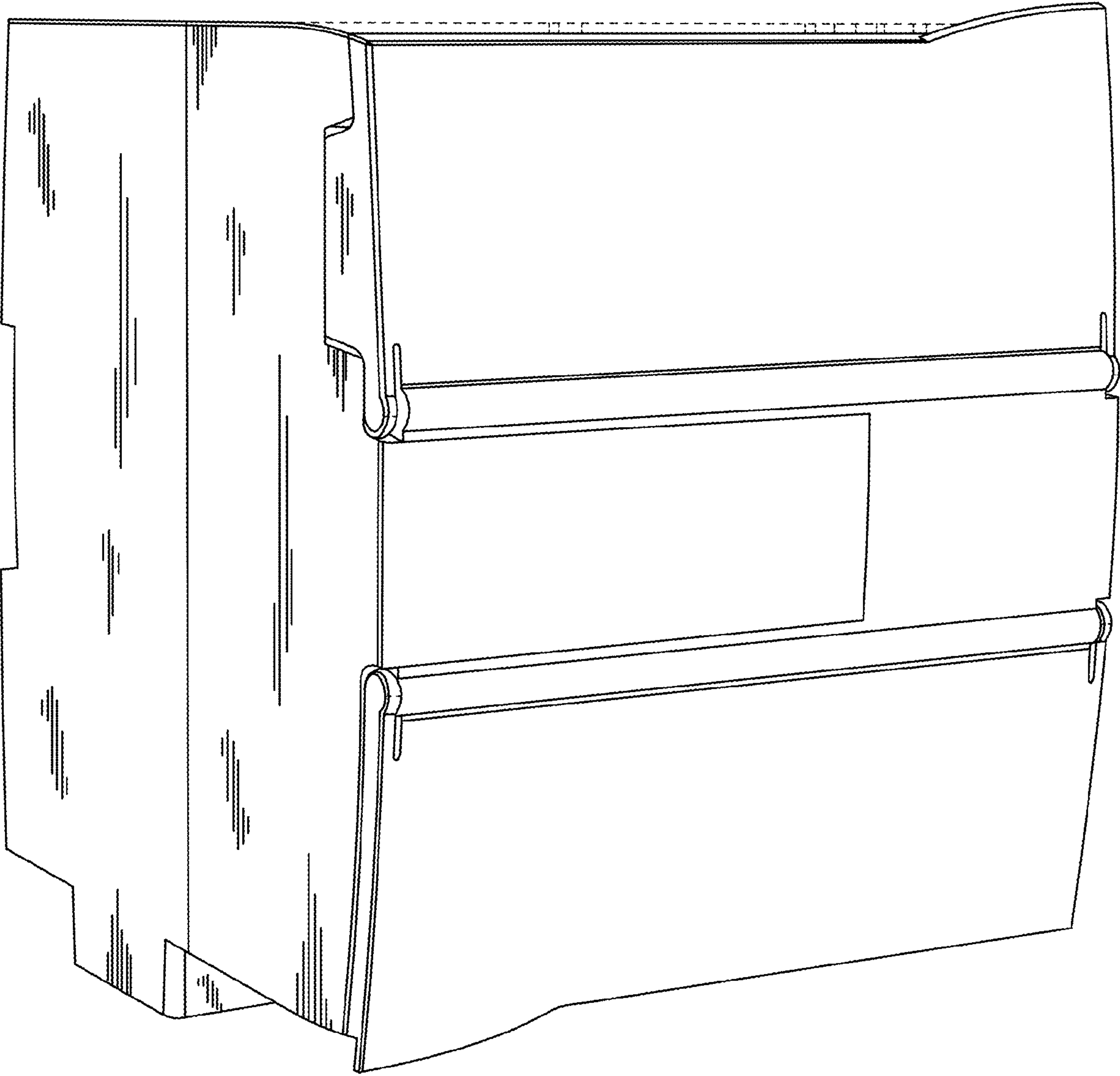


FIG. 8

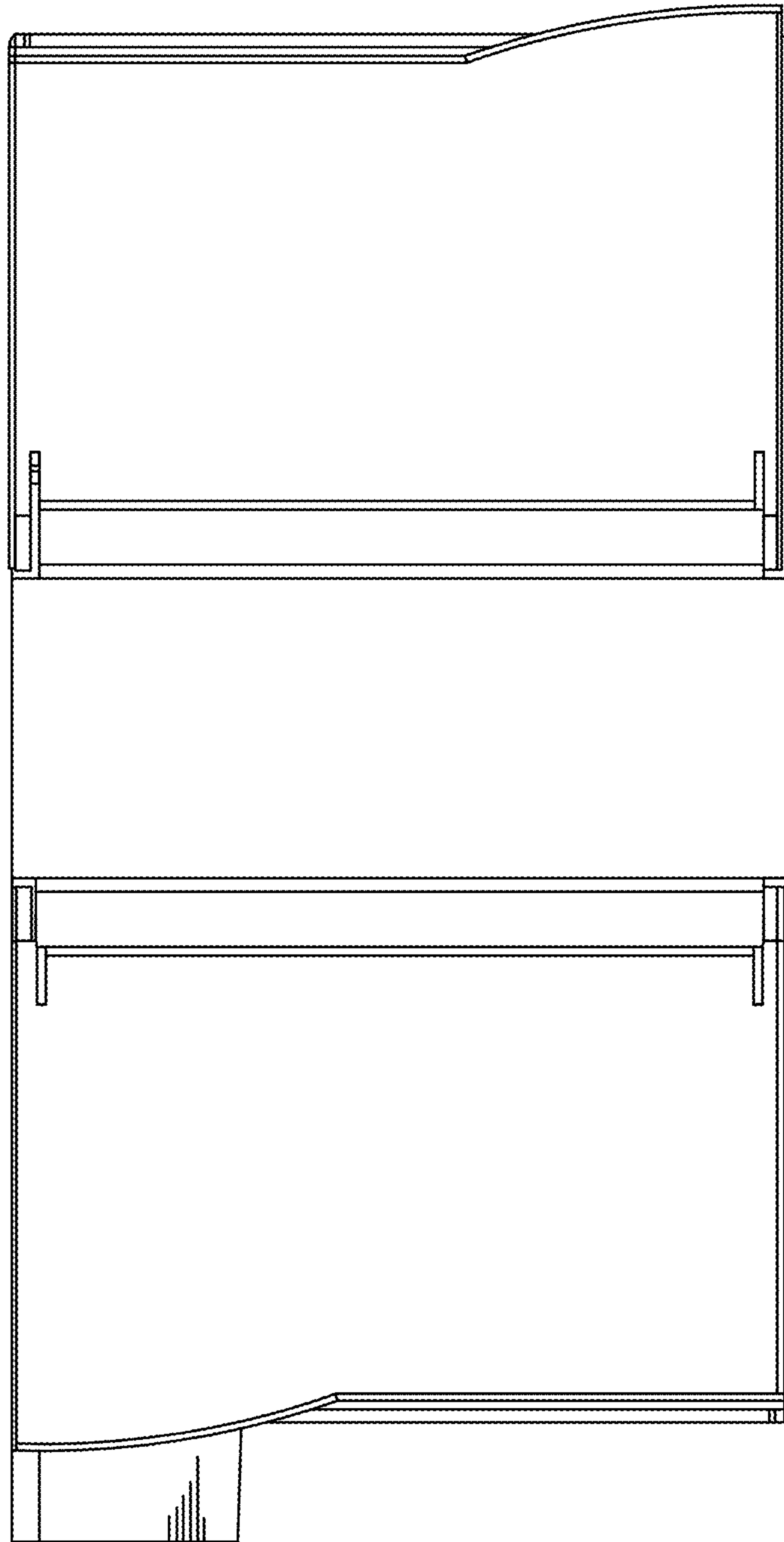


FIG. 9

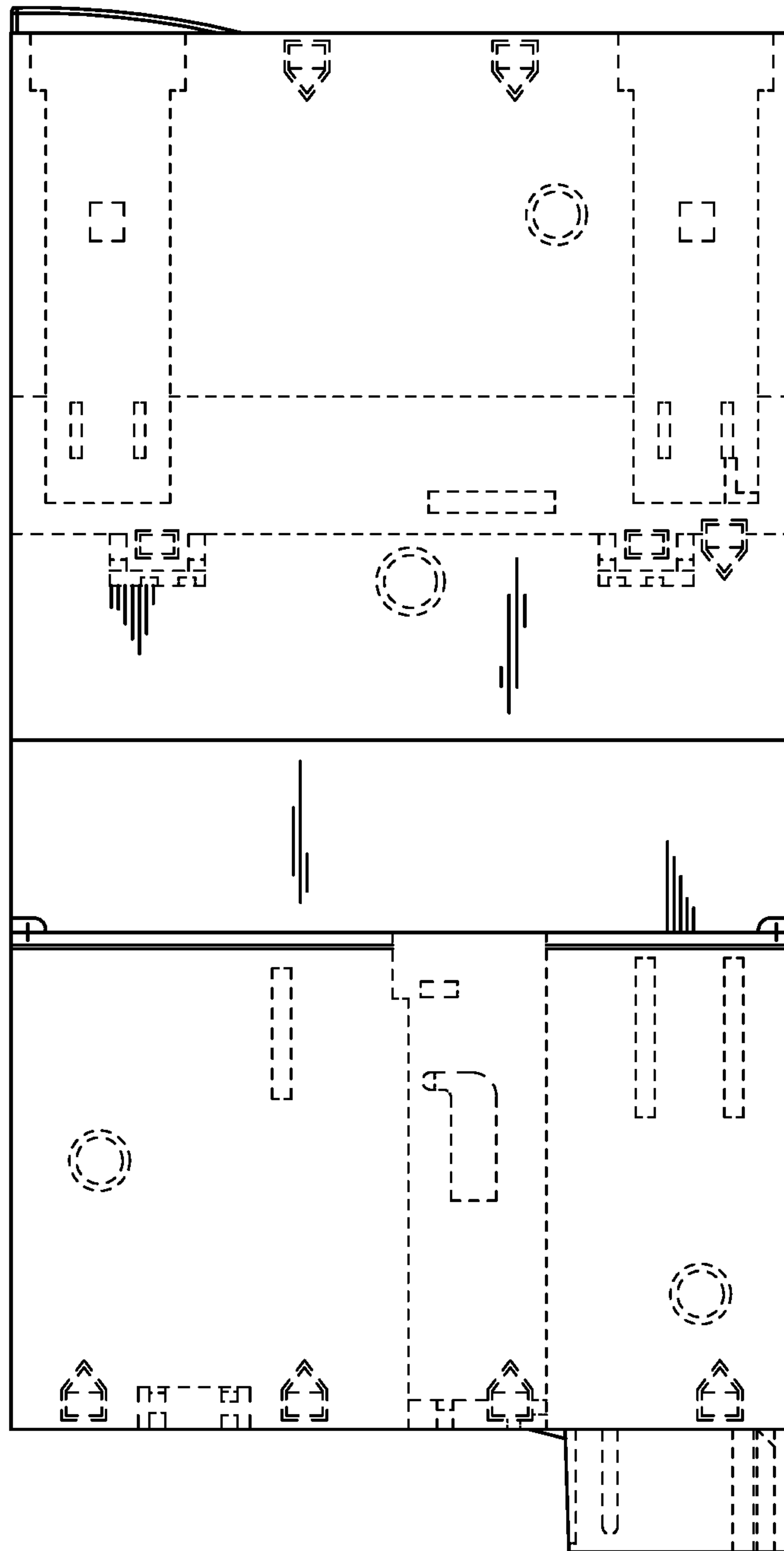


FIG. 10

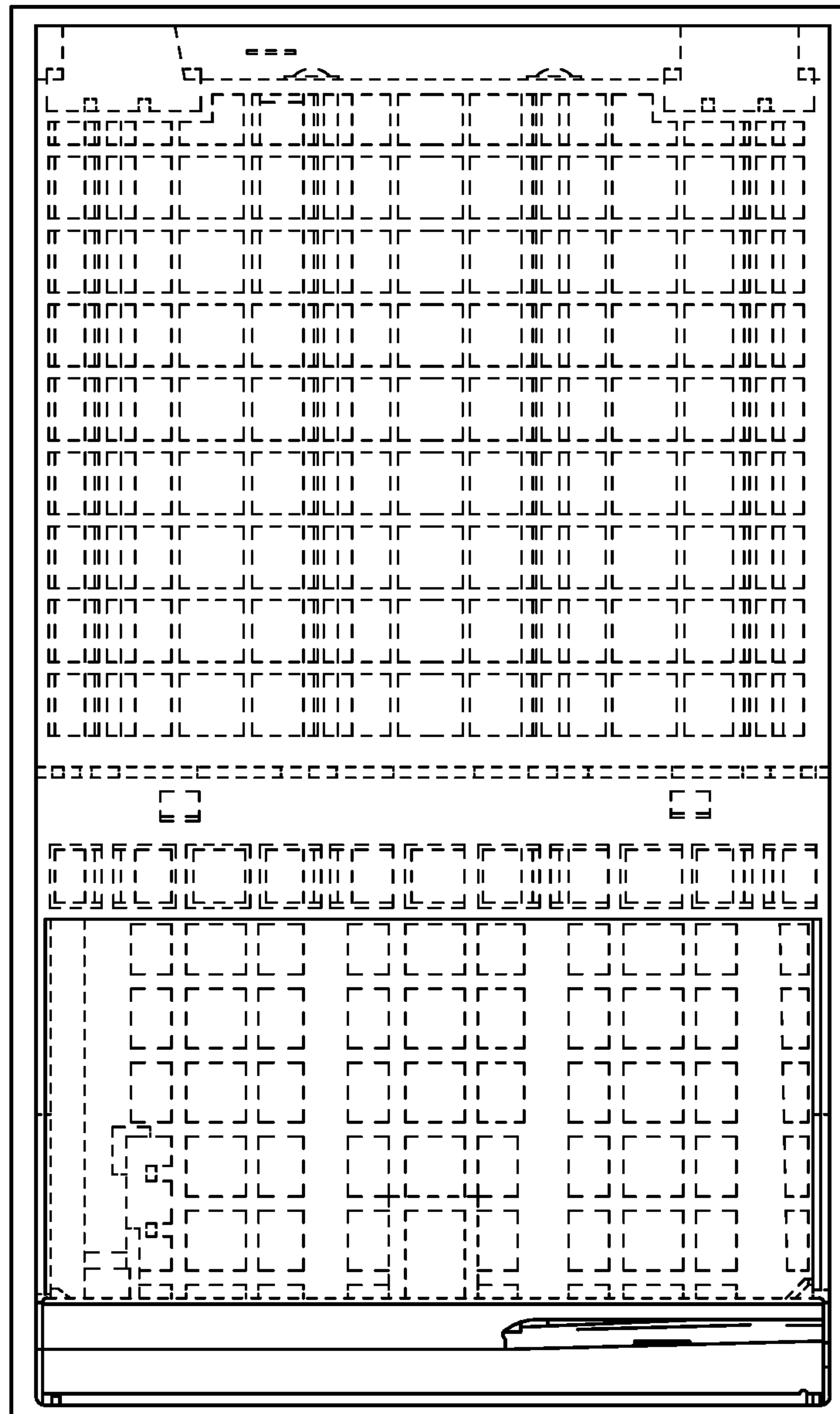


FIG. 11

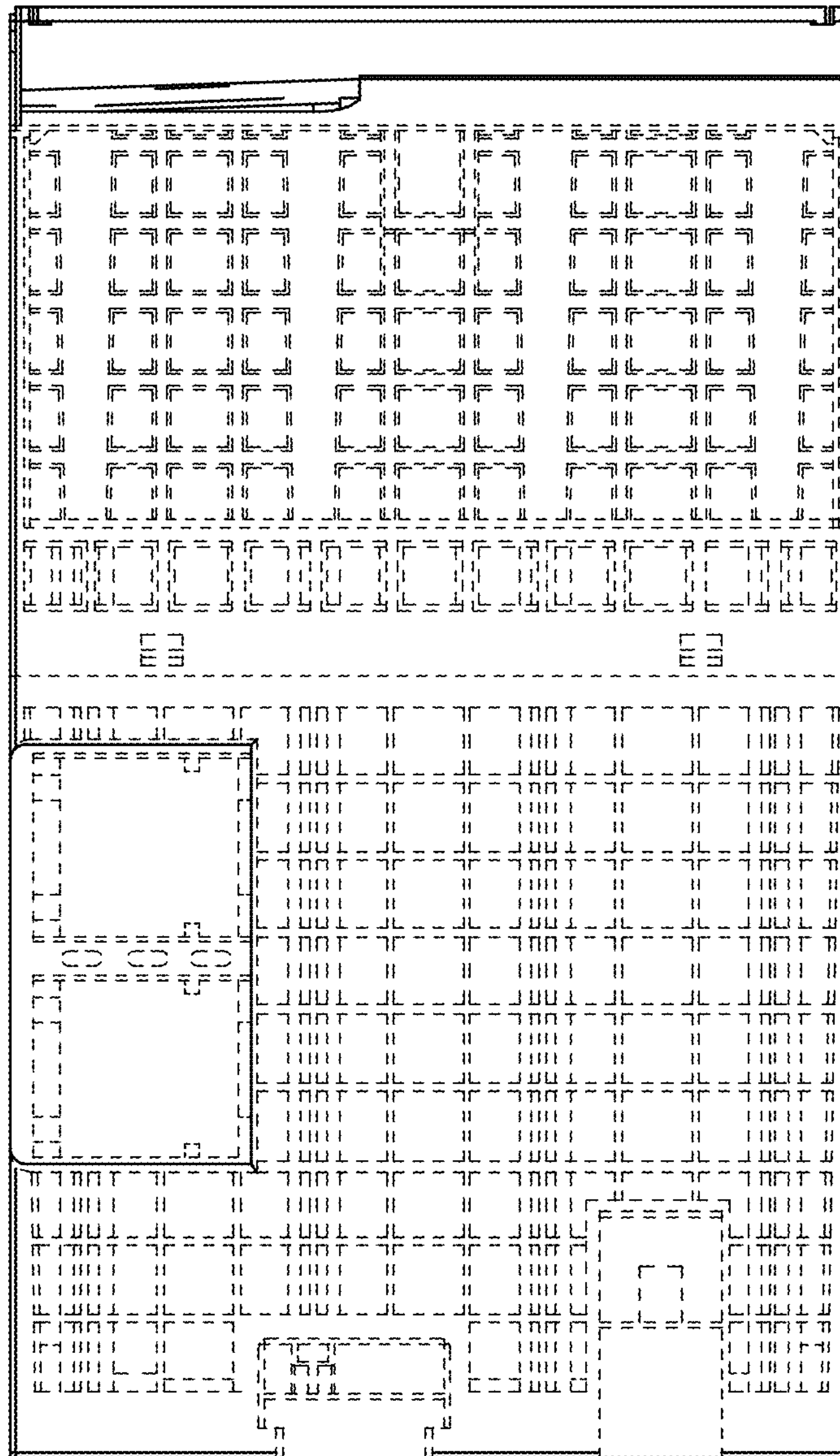


FIG. 12

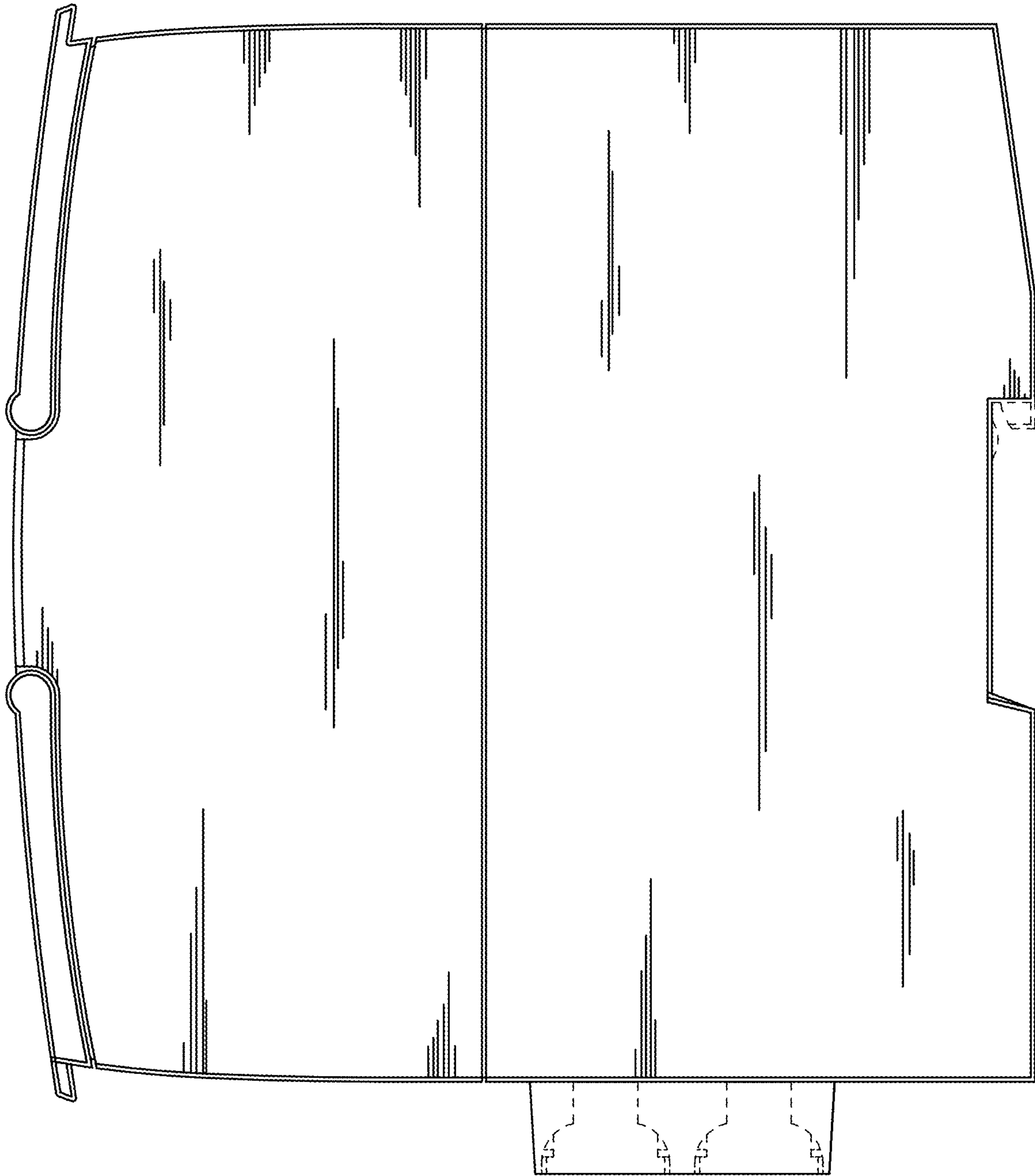


FIG. 13

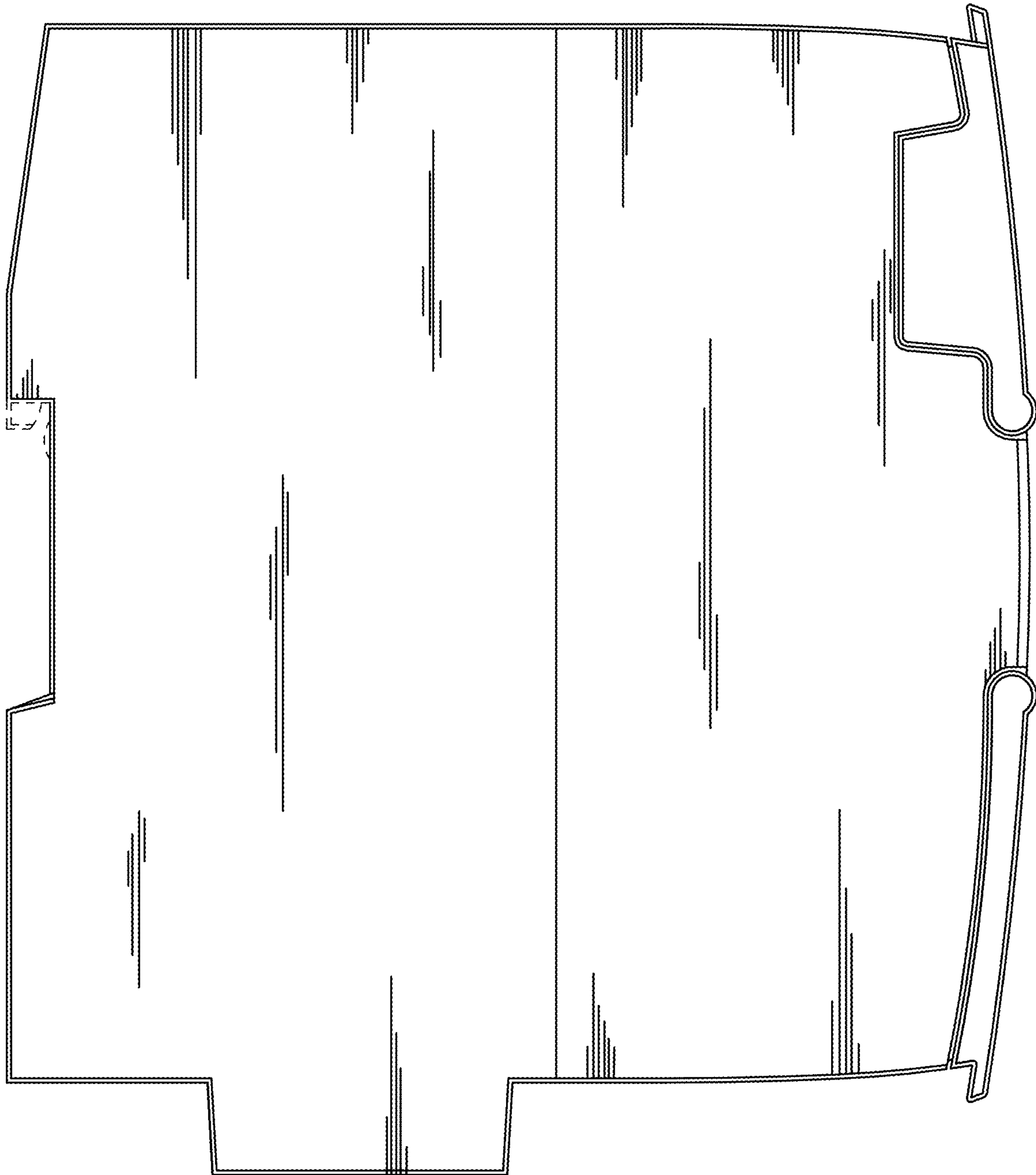


FIG. 14

