US00D951954S

(12) United States Design Patent

Licitra et al.

(10) Patent No.: US D951,954 S
(45) Date of Patent: ** May 17, 2022

(54) **DOUBLE STACK MULTIPORT FOR MAKING OPTICAL CONNECTIONS**

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(**) Term: **15 Years**

(21) Appl. No.: **29/664,750**

(22) Filed: **Sep. 27, 2018**

Related U.S. Application Data

(63) Continuation of application No. 29/642,334, filed on Mar. 29, 2018, now Pat. No. Des. 941,295, and a continuation of application No. 29/642,339, filed on Mar. 29, 2018, now Pat. No. Des. 941,821, and a continuation of application No. 29/642,340, filed on Mar. 29, 2018, now Pat. No. Des. 941,296.

(51) **LOC (13) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/433**; D13/147

(58) **Field of Classification Search**
USPC D13/123, 133, 146, 147, 152, 154, 156, D13/158, 173, 177, 184, 199; D14/242, D14/433, 434, 435.1, 438; D9/432, 703
CPC G02B 6/38; G02B 6/3853; G02B 6/3861; G02B 6/3885; G02B 6/3893; G02B 6/4471; G02B 6/44; G02B 6/4455; G02B 6/4452; G06F 3/00; G06F 5/00; G06F 13/14; H04L 12/2832; H04L 12/2838
See application file for complete search history.

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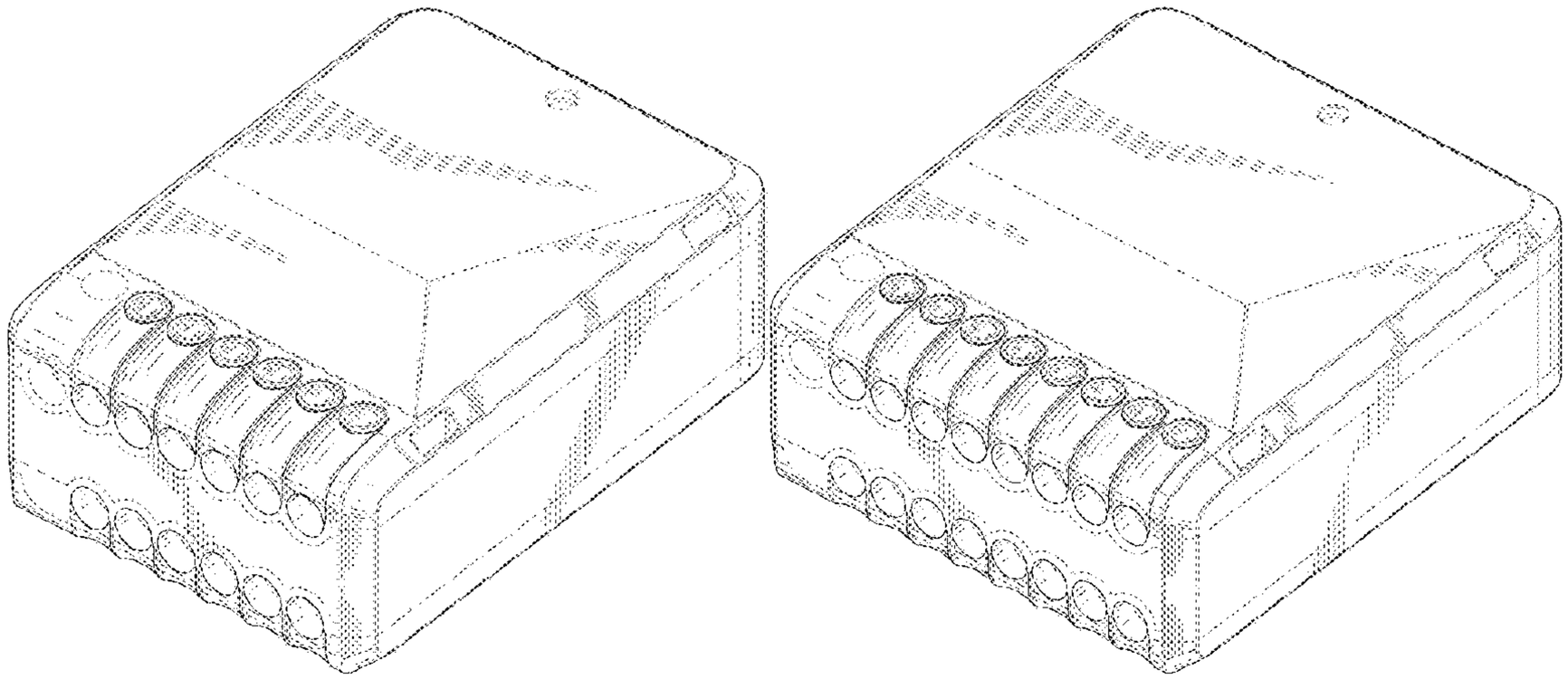

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(57) **CLAIM**

The ornamental design for a double stack multiport for making optical connections, as shown and described.

DESCRIPTION

FIG. **1** is a front perspective view of a first embodiment of a double stack multiport for making optical connections showing our new design;
FIG. **2** is a top view thereof of FIG. **1**;
FIG. **3** is a bottom view thereof of FIG. **1**;
FIG. **4** is a right side view thereof of FIG. **1**;
FIG. **5** is a left side view thereof of FIG. **1**;
FIG. **6** is a front view thereof of FIG. **1**; and
FIG. **7** is a rear view thereof of FIG. **1**.
FIG. **8** is a front perspective view of a second embodiment of a double stack multiport for making optical connections showing our new design;
FIG. **9** is a top view thereof of FIG. **8**;
FIG. **10** is a bottom view thereof of FIG. **8**;
FIG. **11** is a right side view thereof of FIG. **8**;
FIG. **12** is a left side view thereof of FIG. **8**;
FIG. **13** is a front view thereof of FIG. **8**; and
FIG. **14** is a rear view thereof of FIG. **8**.
FIG. **15** is a front perspective view of a third embodiment of a double stack multiport for making optical connections showing our new design;
FIG. **16** is a top view thereof of FIG. **15**;
FIG. **17** is a bottom view thereof of FIG. **15**;

FIG. **18** is a right side view thereof of FIG. **15**;
FIG. **19** is a left side view thereof of FIG. **15**;
FIG. **20** is a front view thereof of FIG. **15**; and
FIG. **21** is a rear view thereof of FIG. **15**.
FIG. **22** is a front perspective view of a fourth embodiment of a double stack multiport for making optical connections showing our new design;
FIG. **23** is a top view thereof of FIG. **22**;
FIG. **24** is a bottom view thereof of FIG. **22**;
FIG. **25** is a right side view thereof of FIG. **22**;
FIG. **26** is a left side view thereof of FIG. **22**;
FIG. **27** is a front view thereof of FIG. **22**; and,
FIG. **28** is a rear view thereof of FIG. **22**.
In FIGS. **1-28**, the evenly-spaced broken lines are included for the purpose of illustrating environmental structure and form no part of the claimed design.

1 Claim, 20 Drawing Sheets

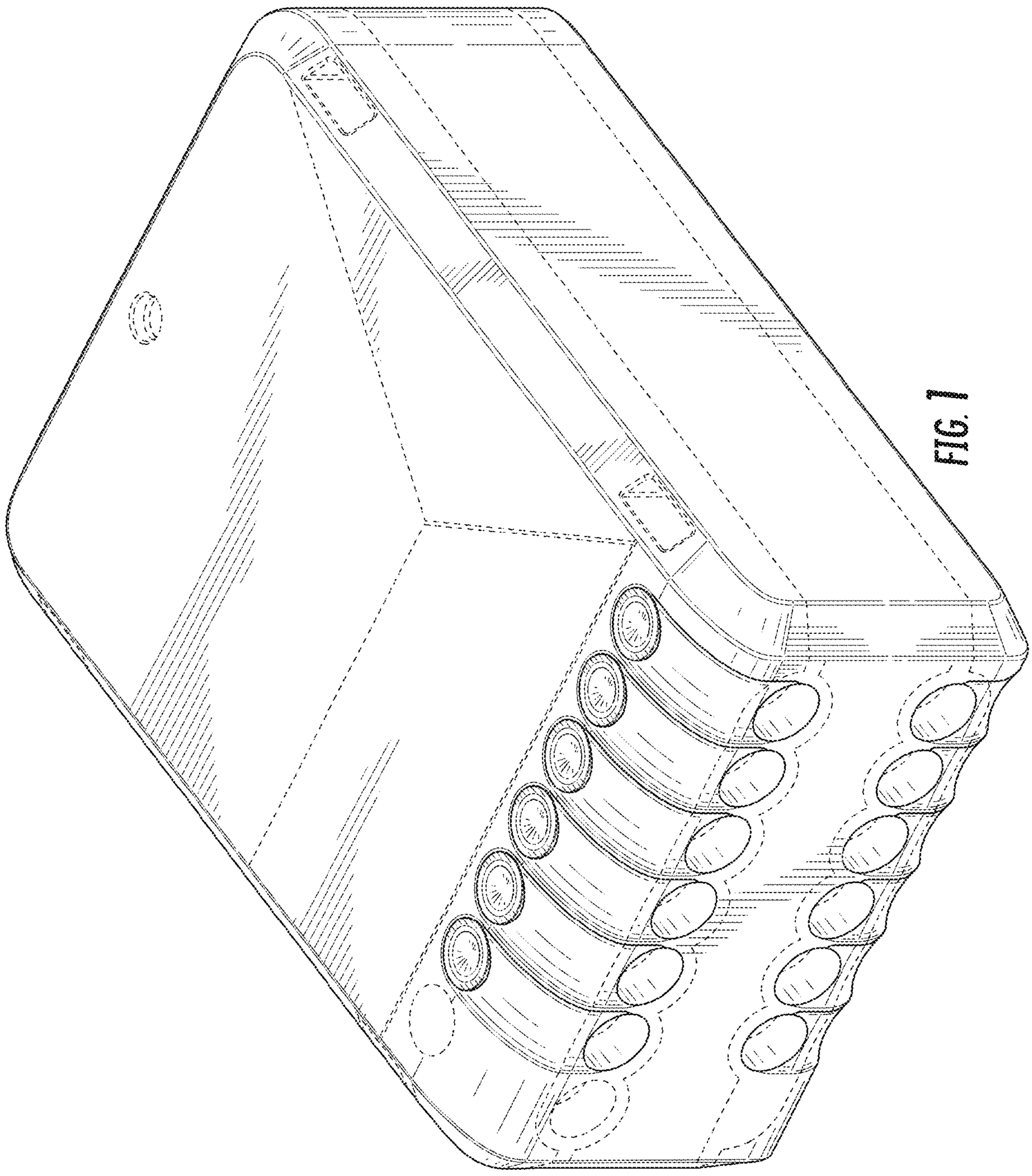


FIG. 1

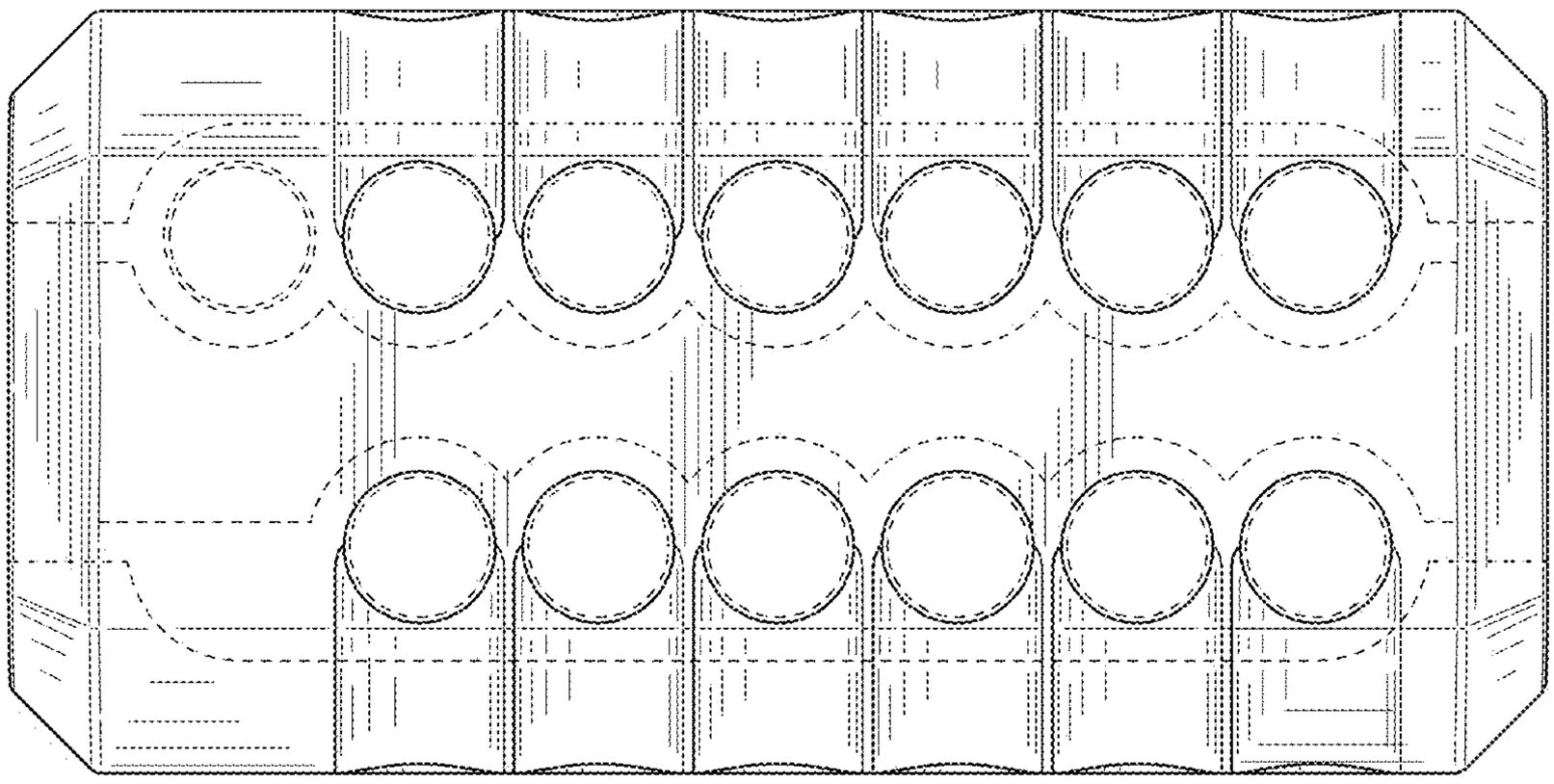


FIG. 2

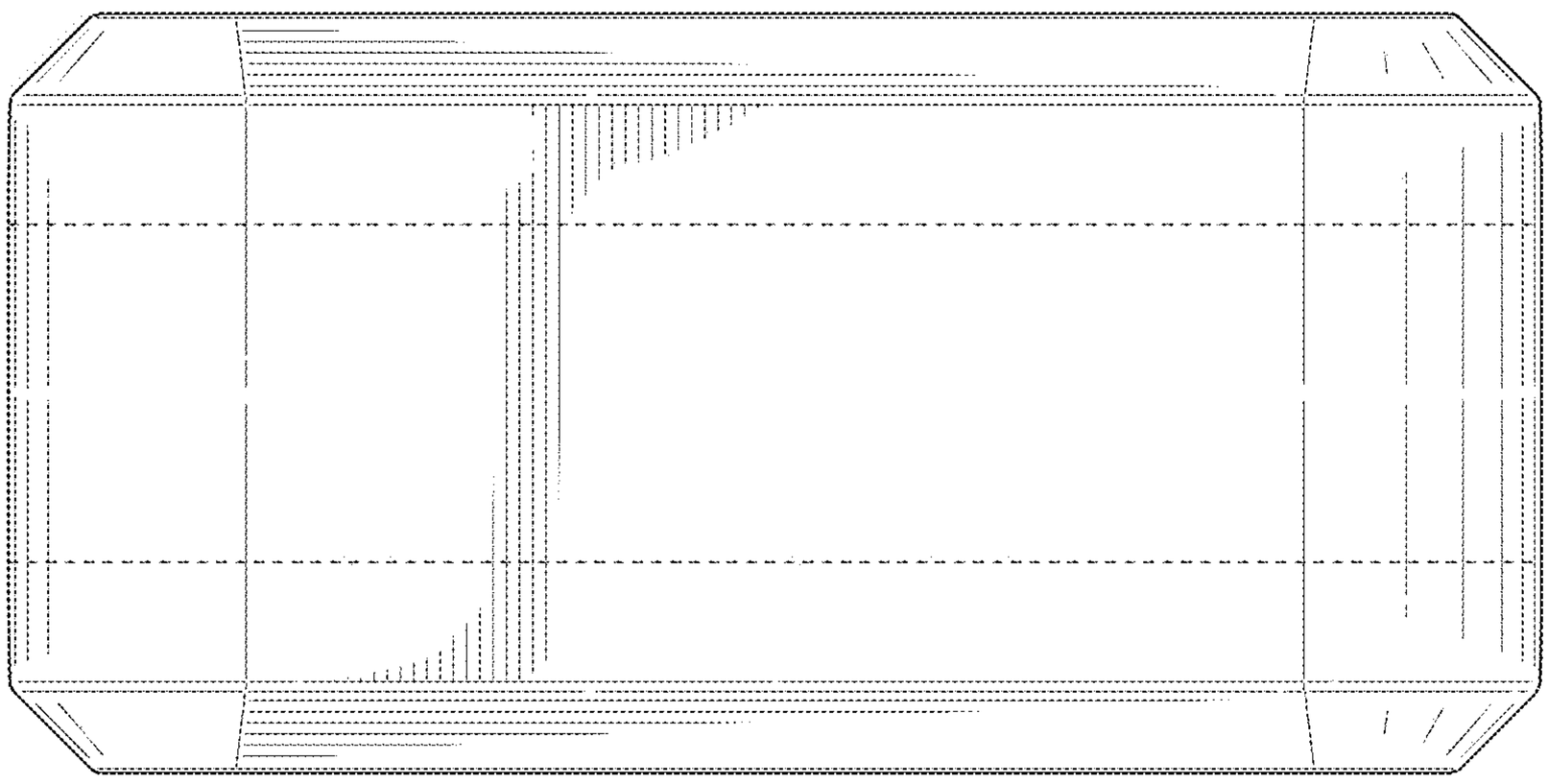


FIG. 3

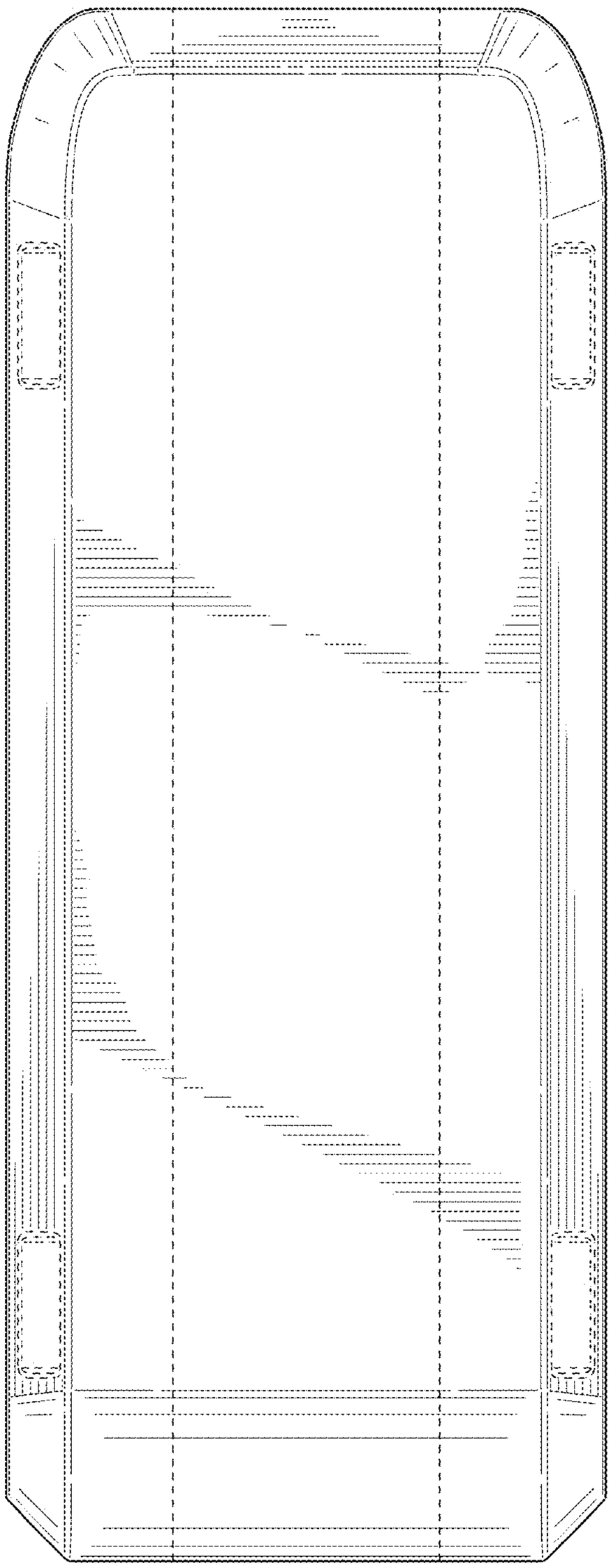


FIG. 4

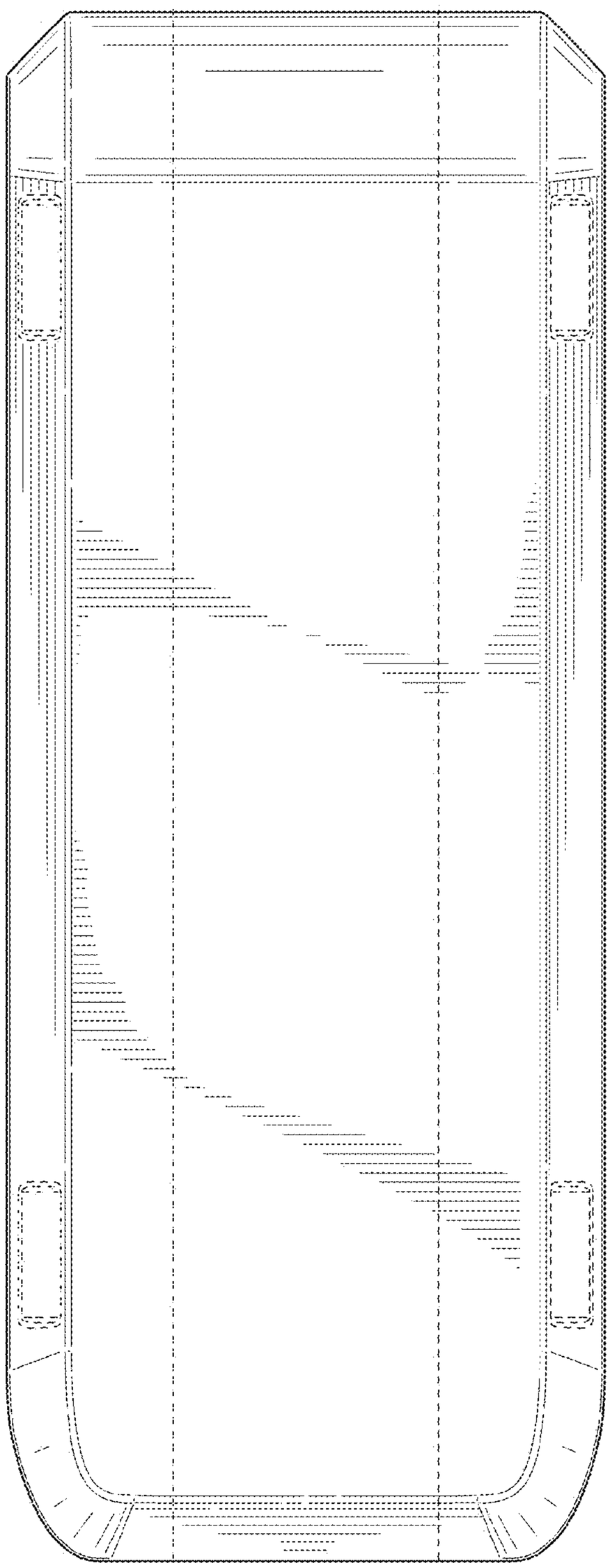


FIG. 5

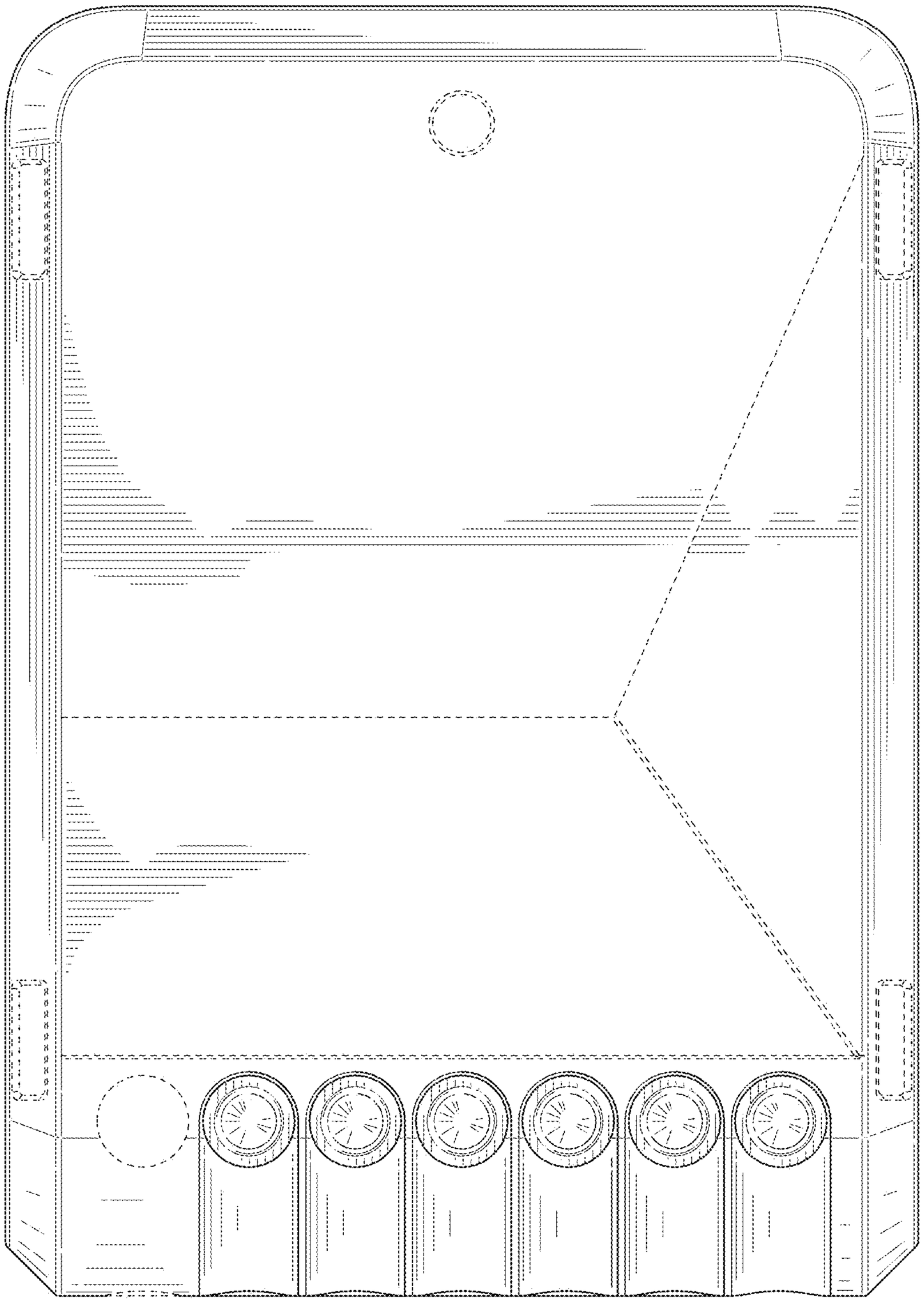


FIG. 6

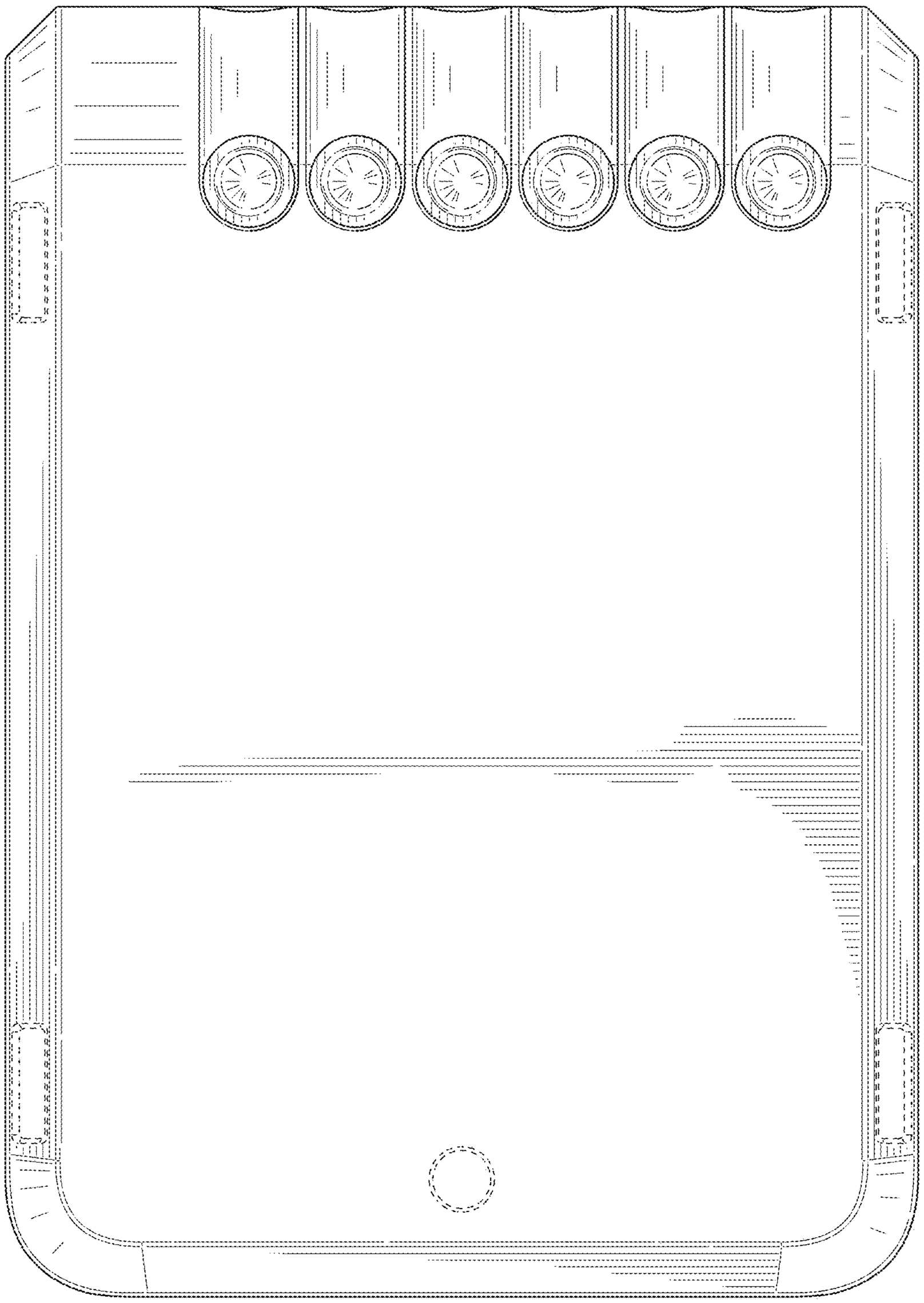


FIG. 7

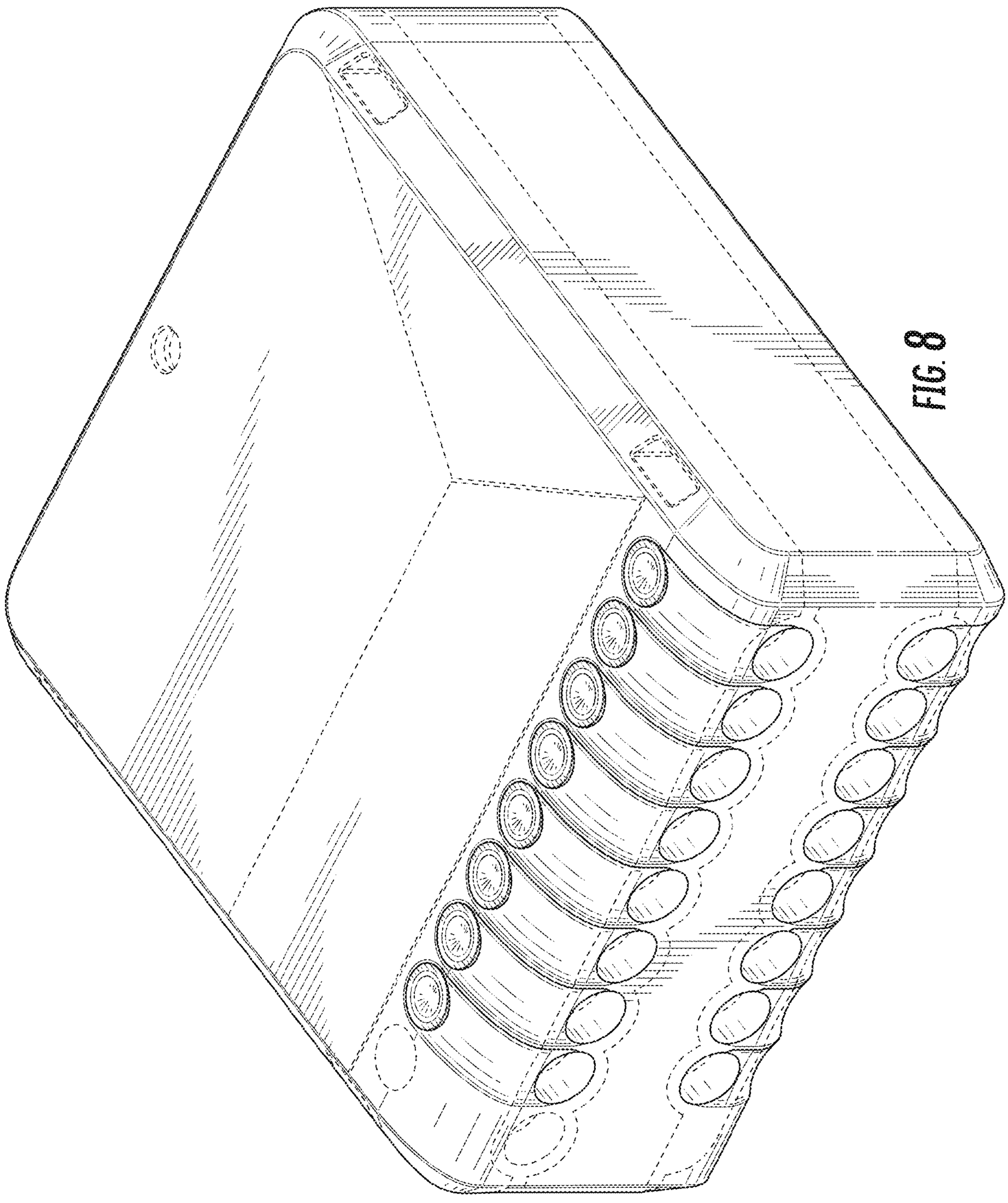


FIG. 8

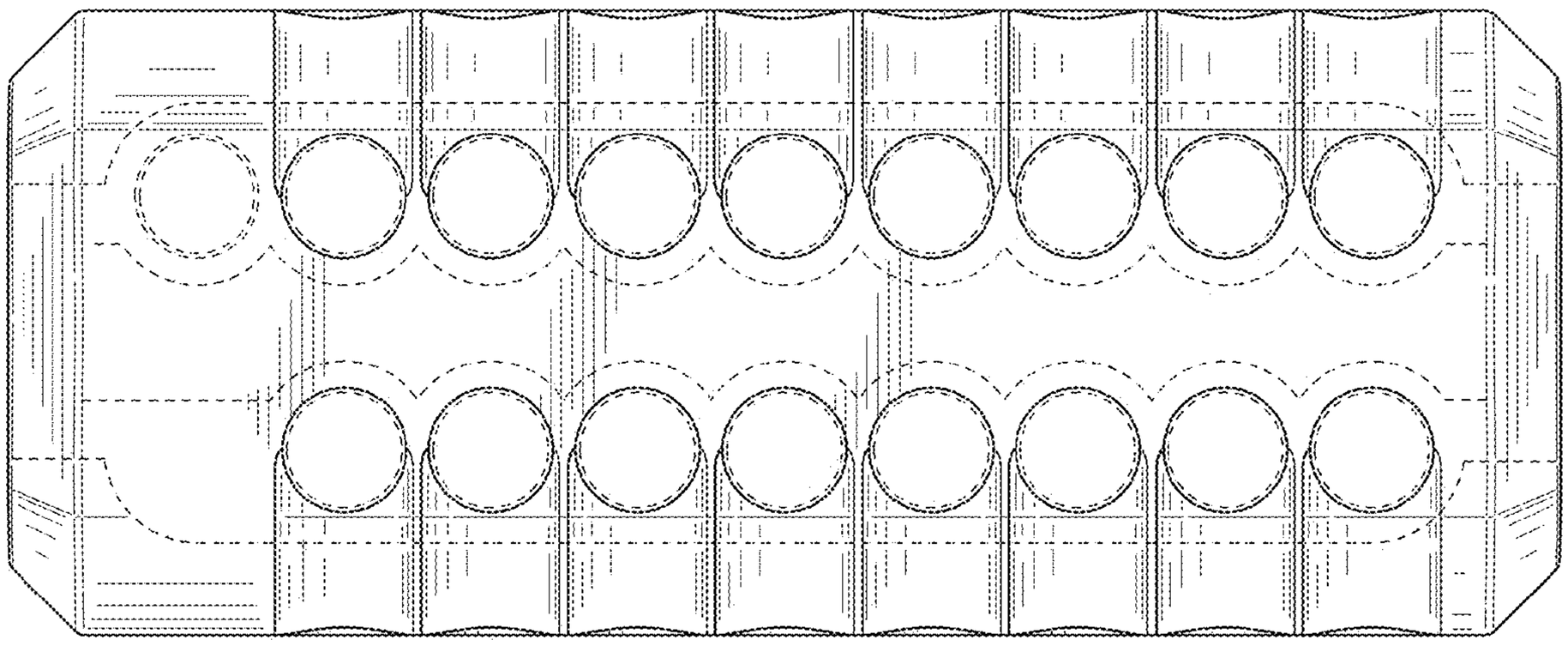


FIG. 9

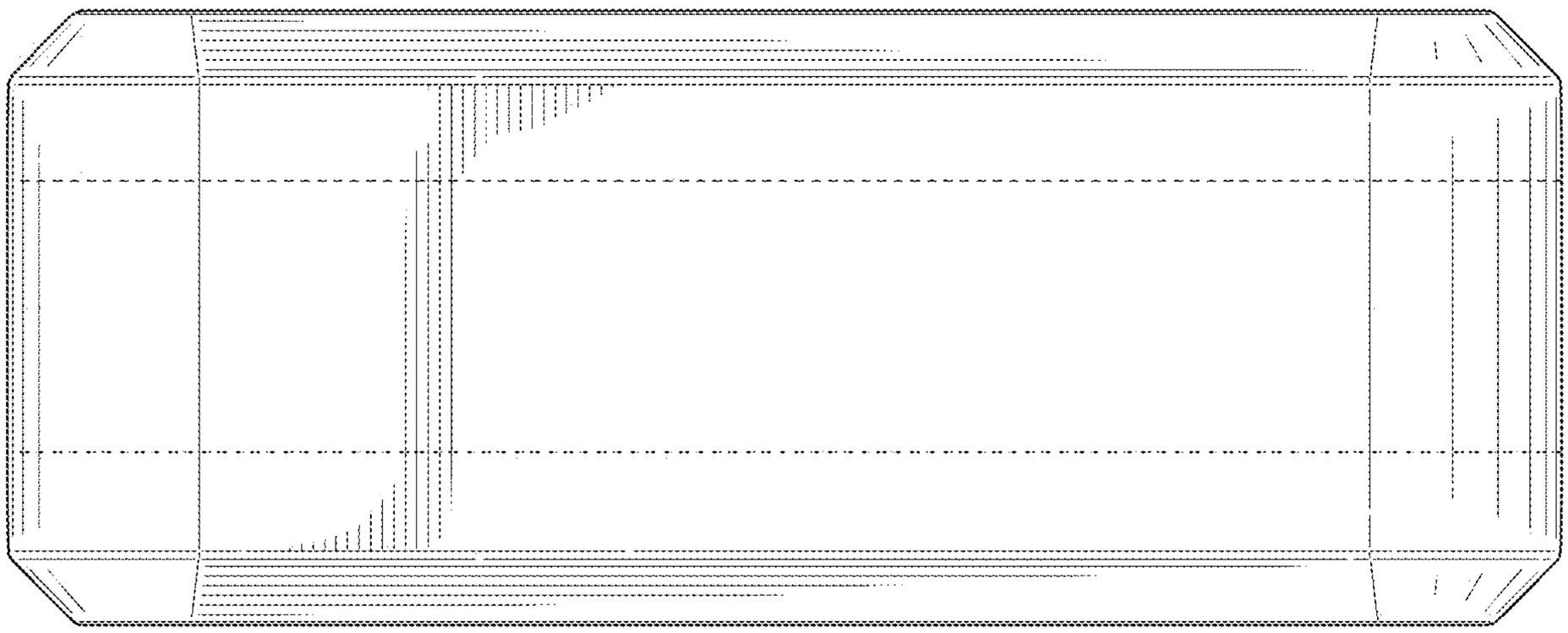
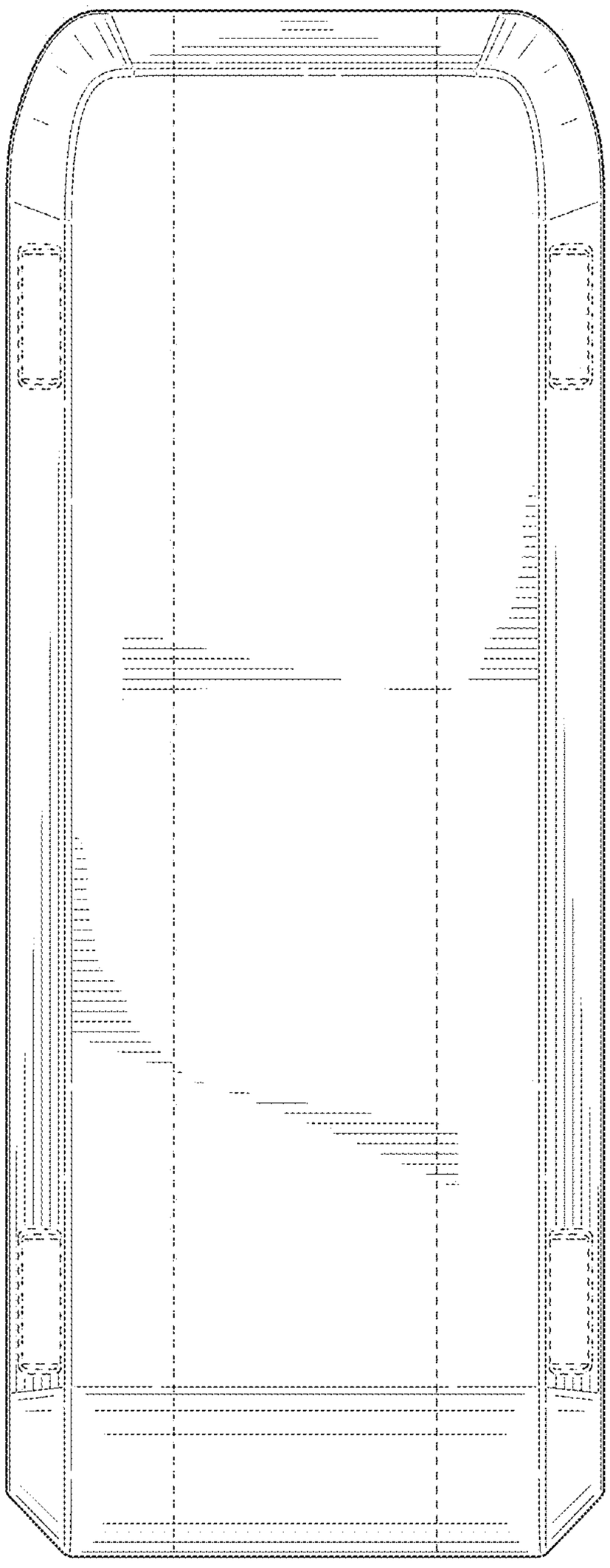


FIG. 10



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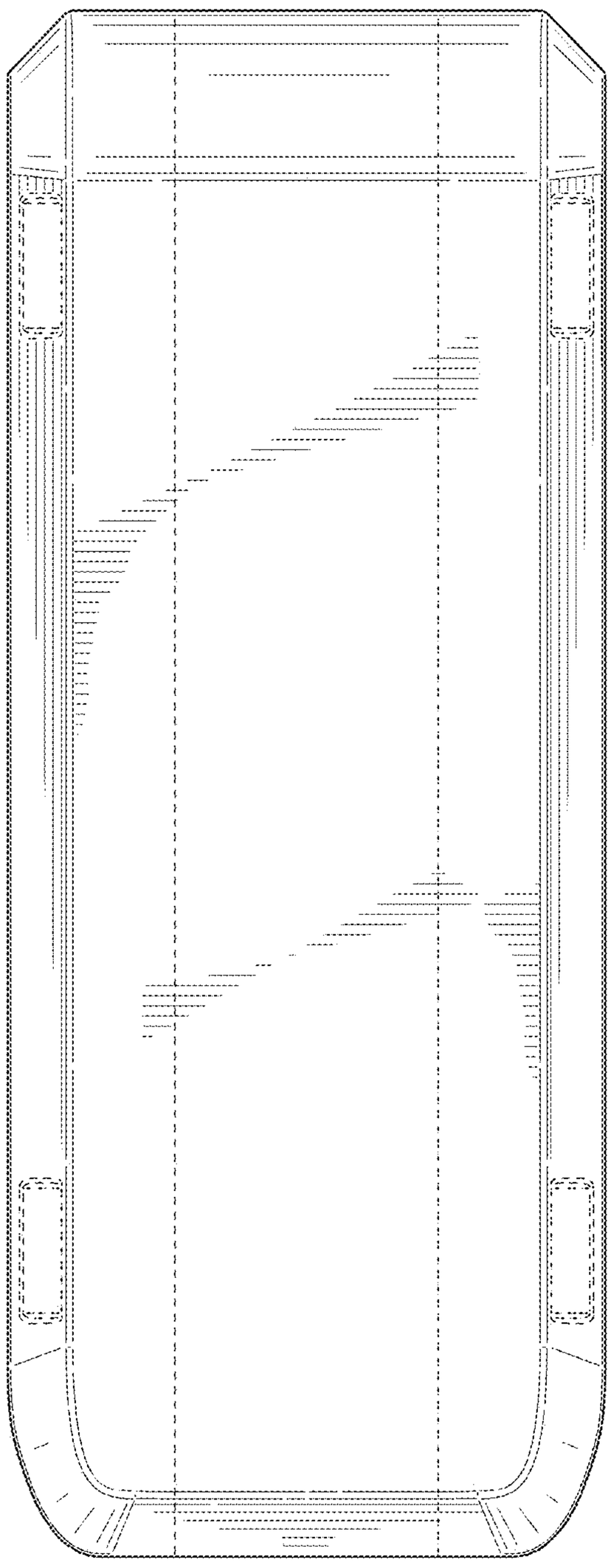

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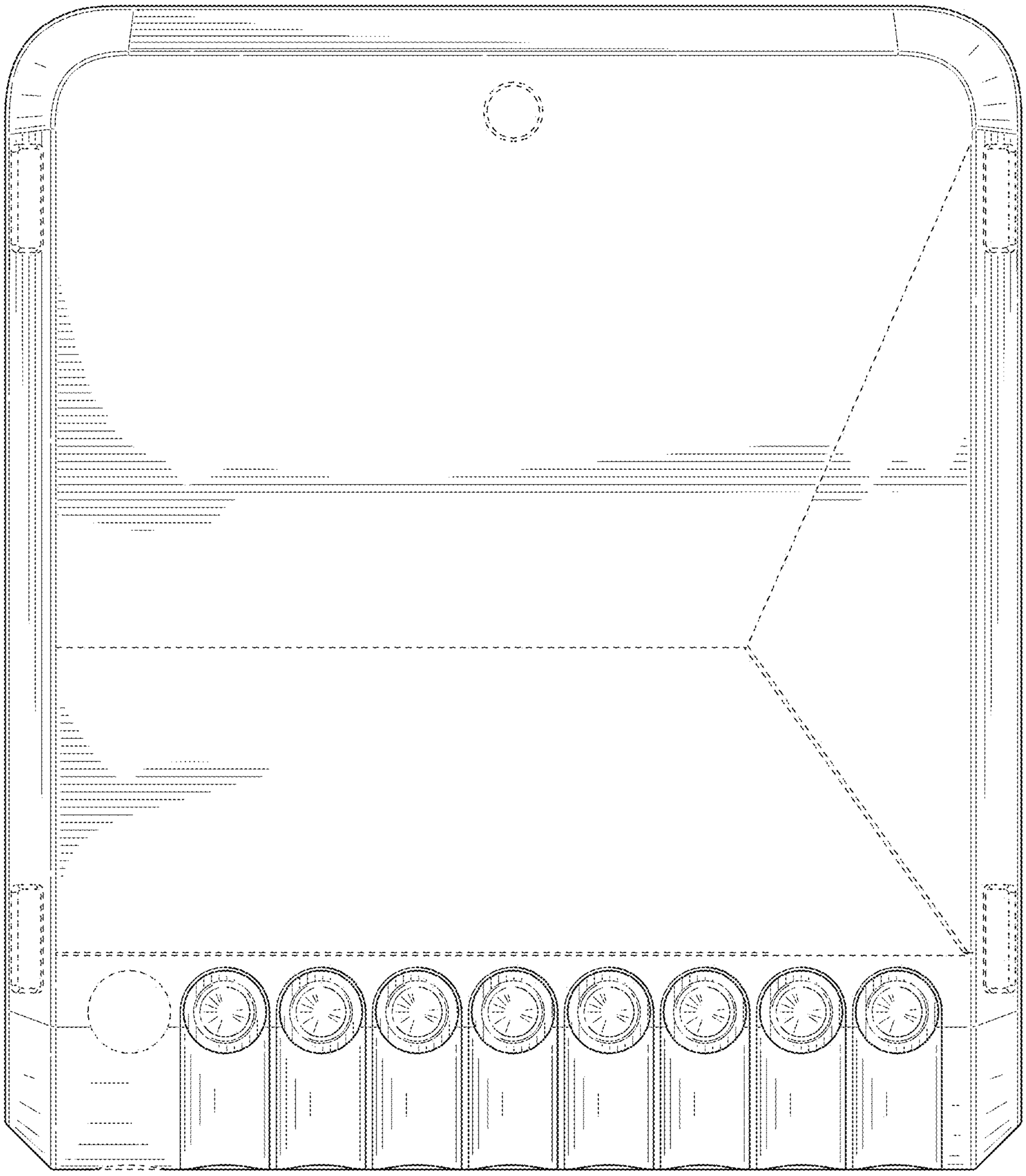

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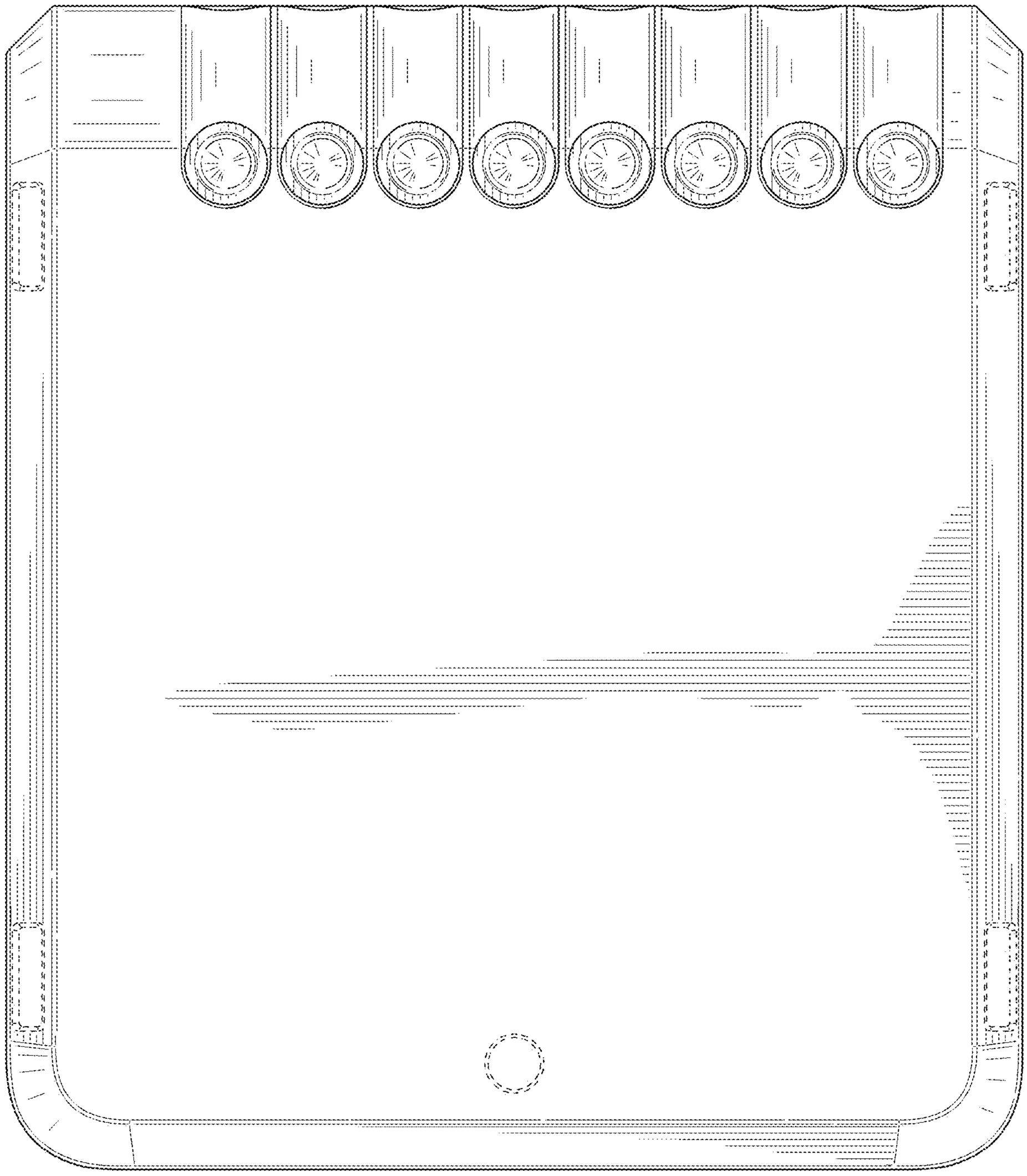

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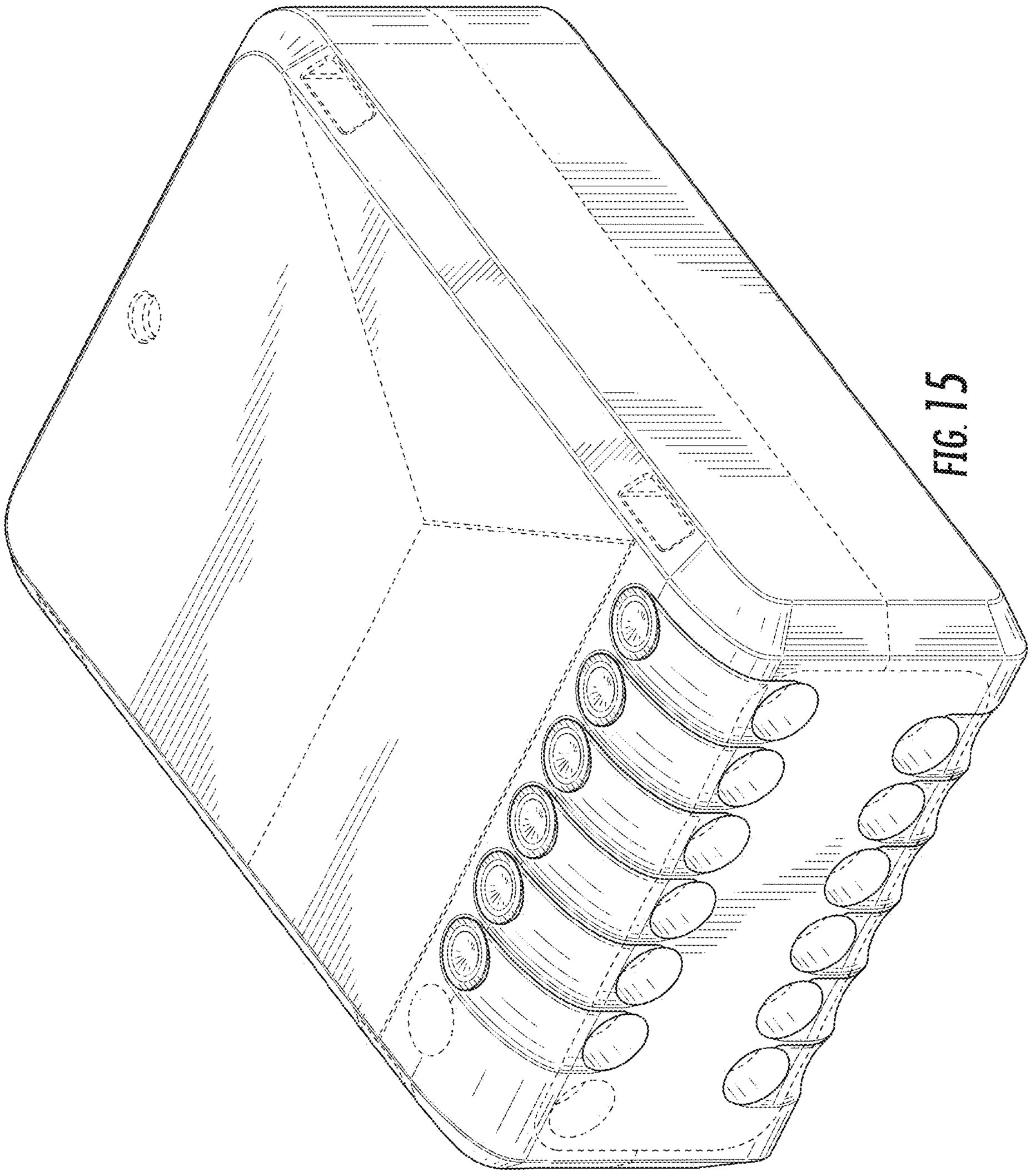

FIG. 15

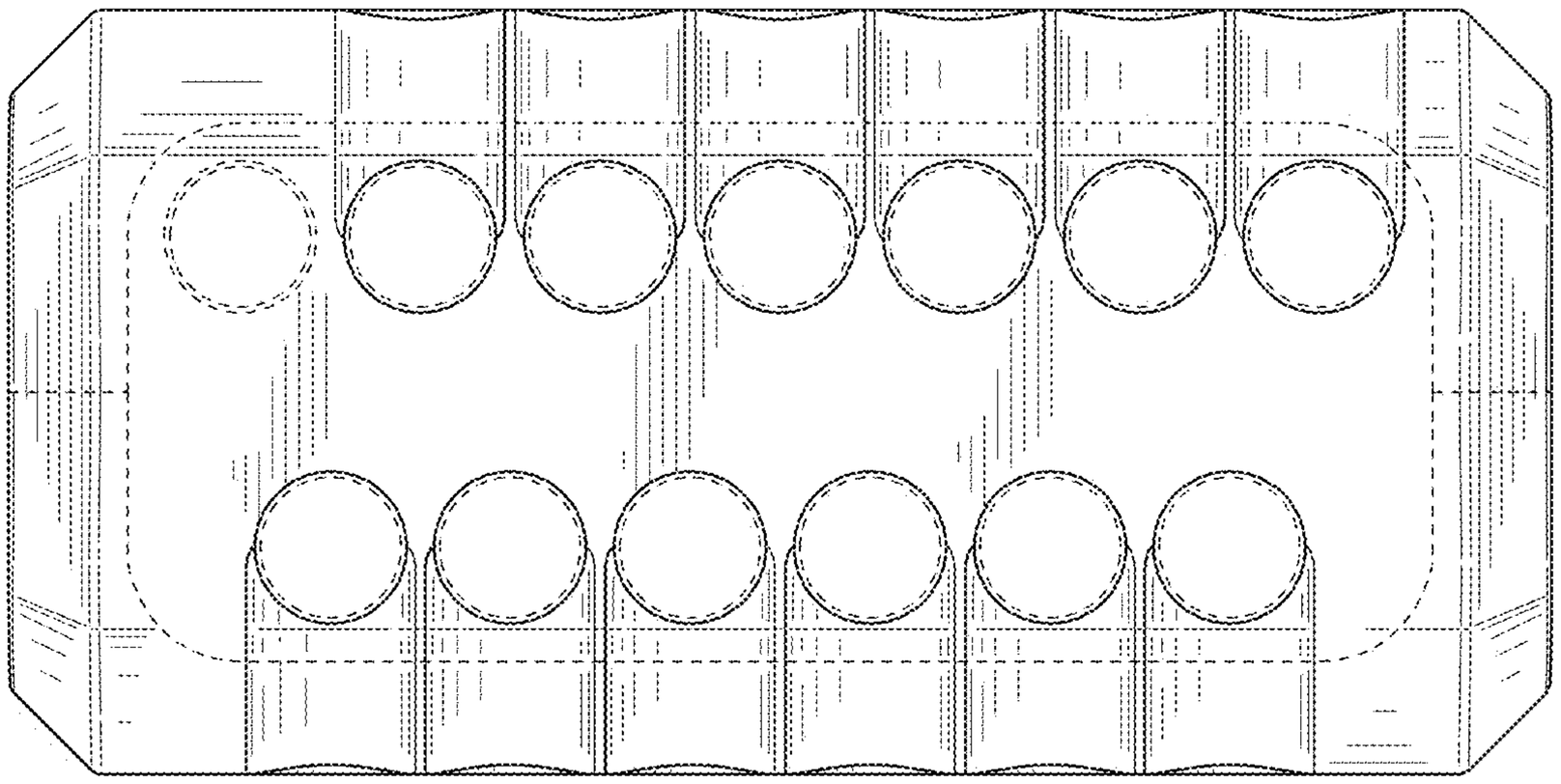

FIG. 16

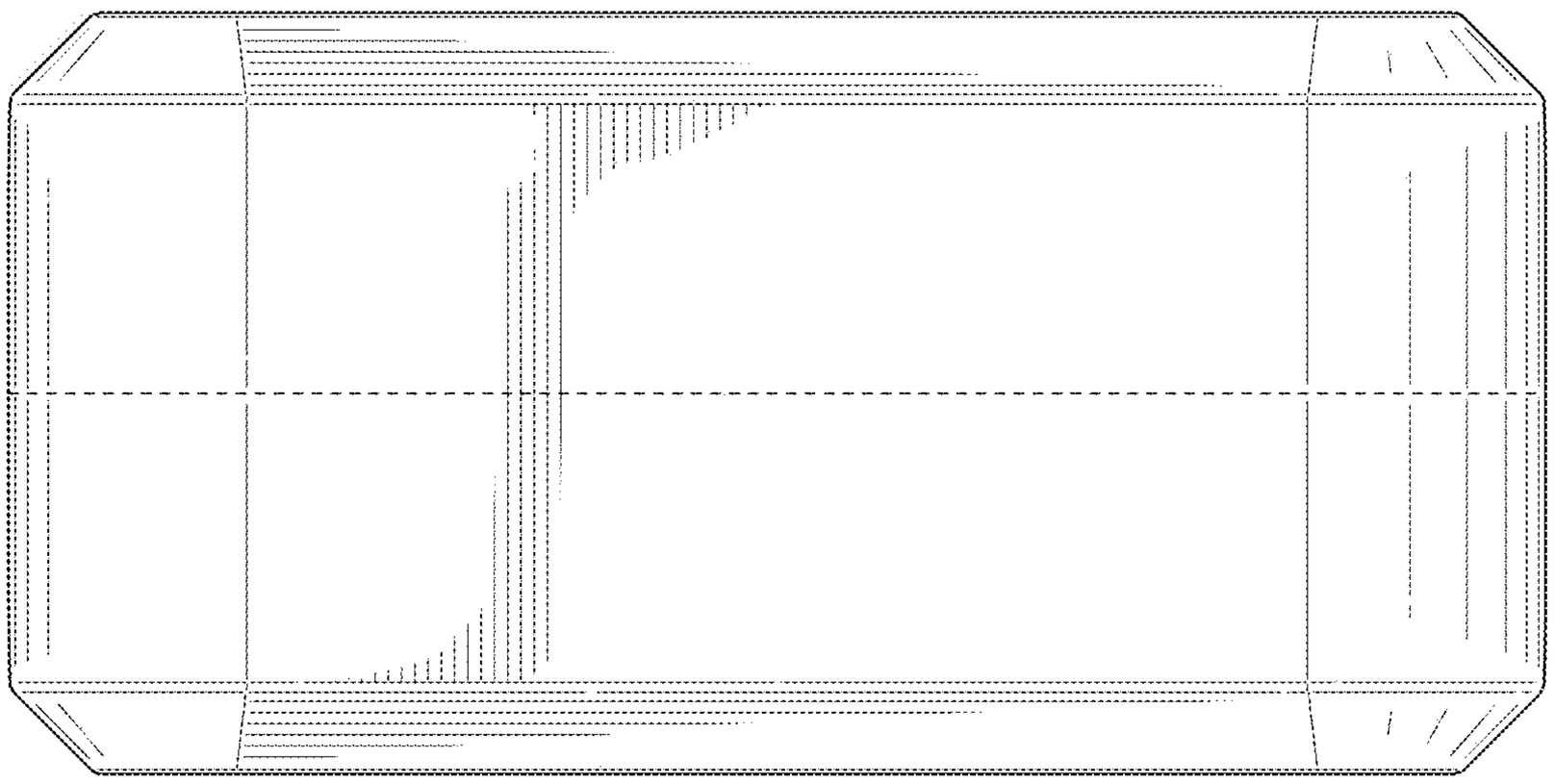

FIG. 17

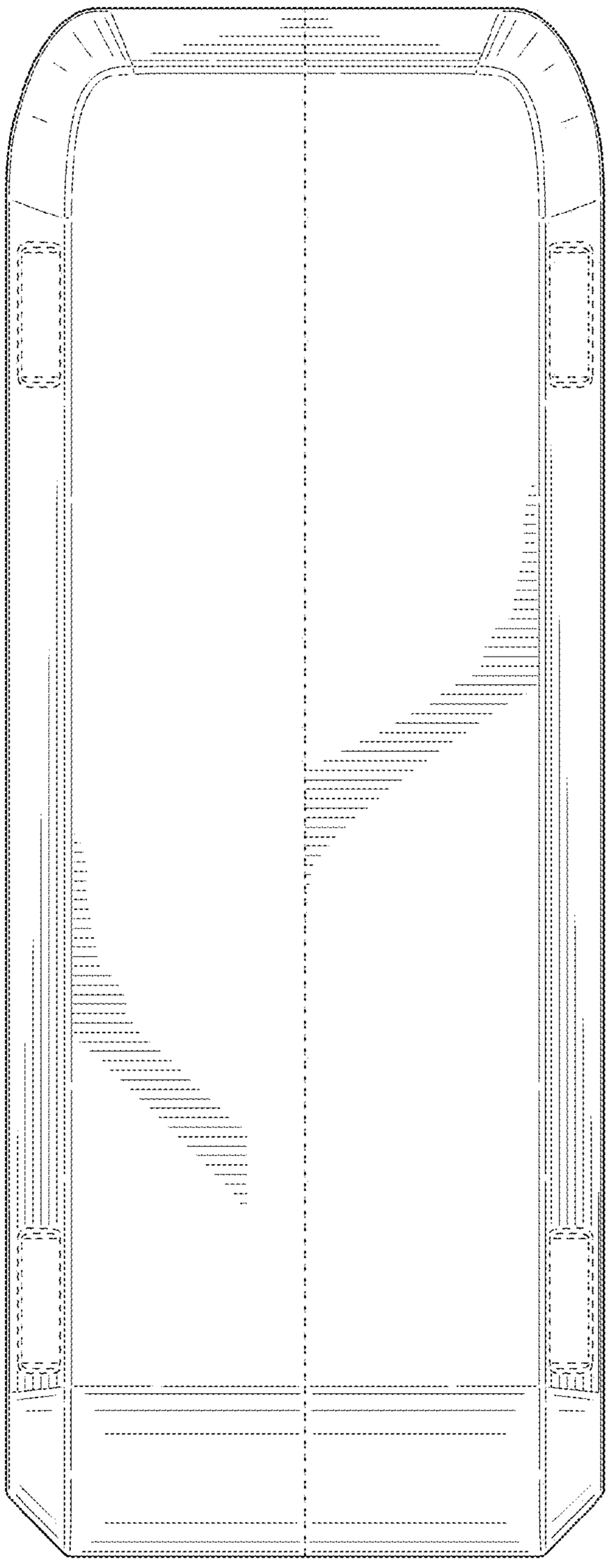

FIG. 18

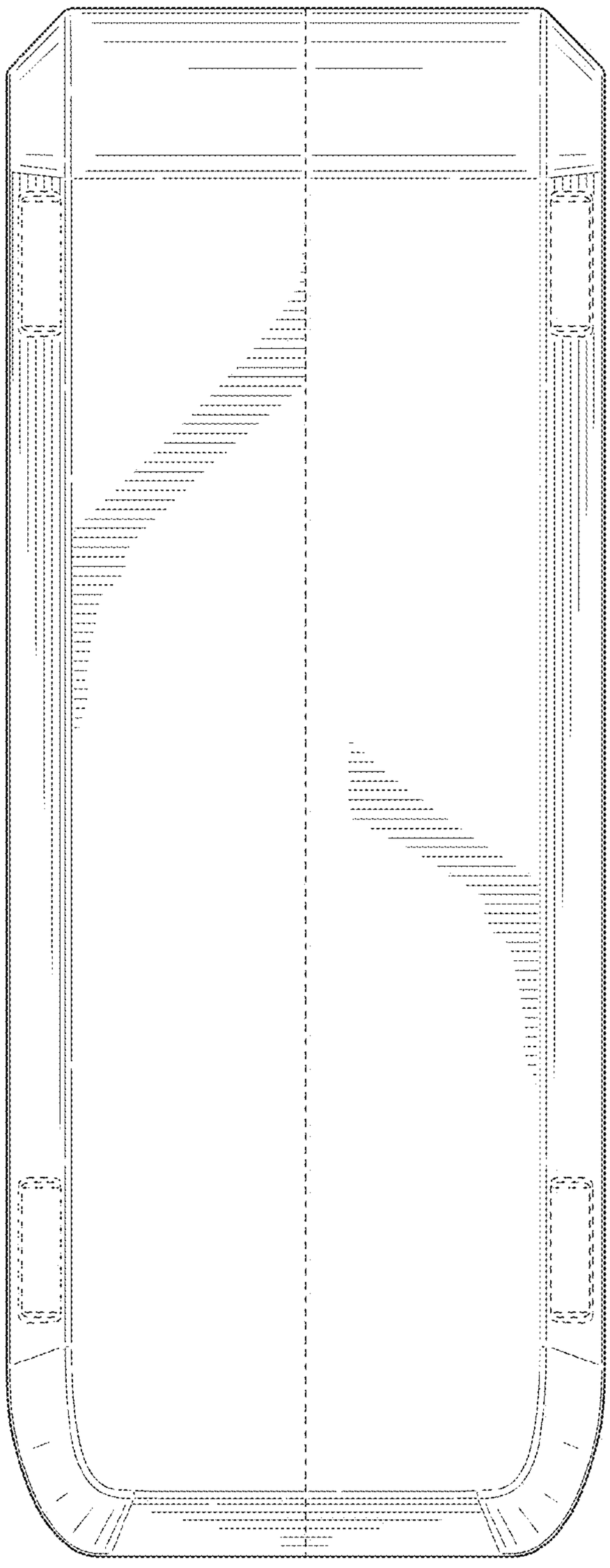

FIG. 19

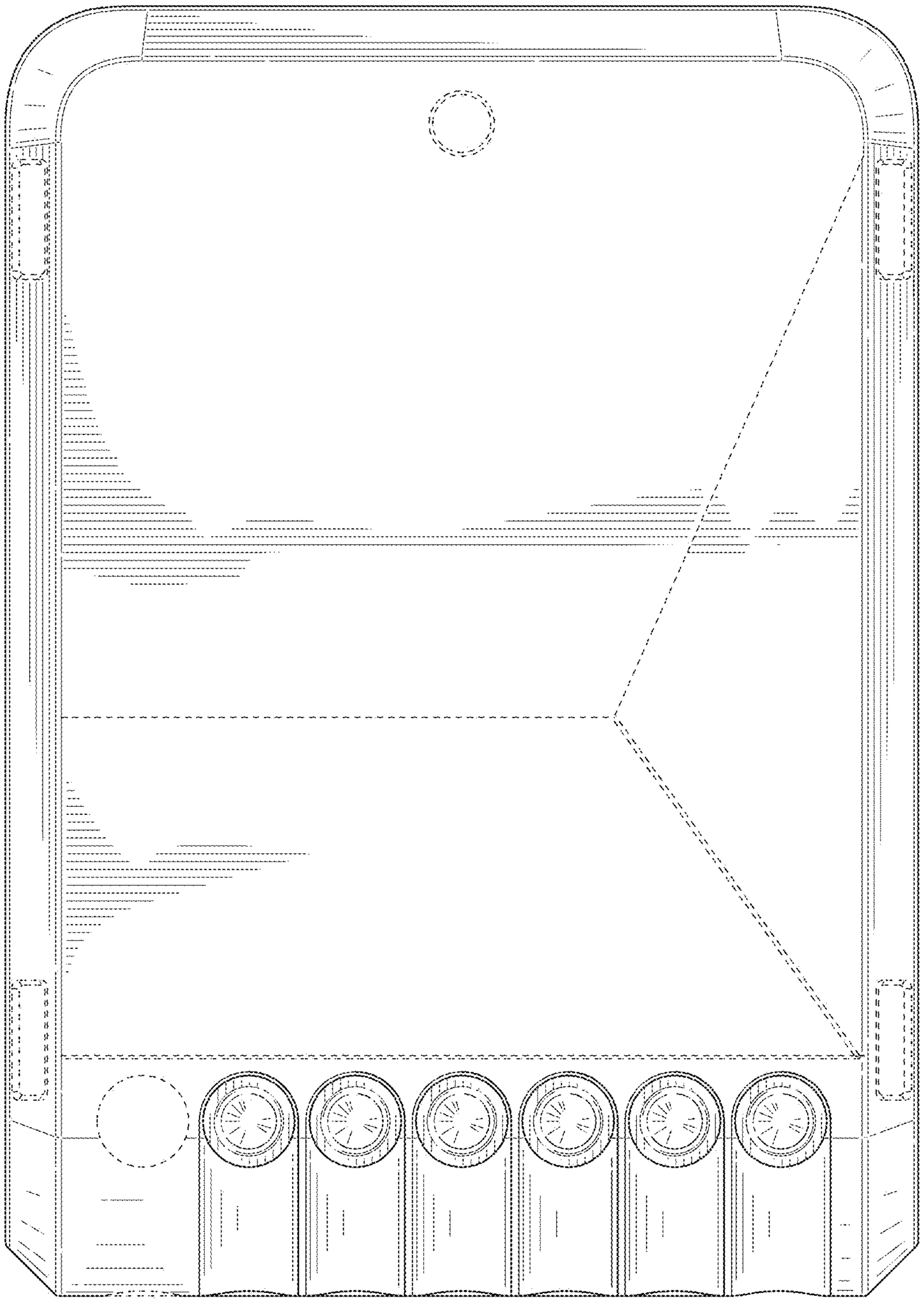

FIG. 20

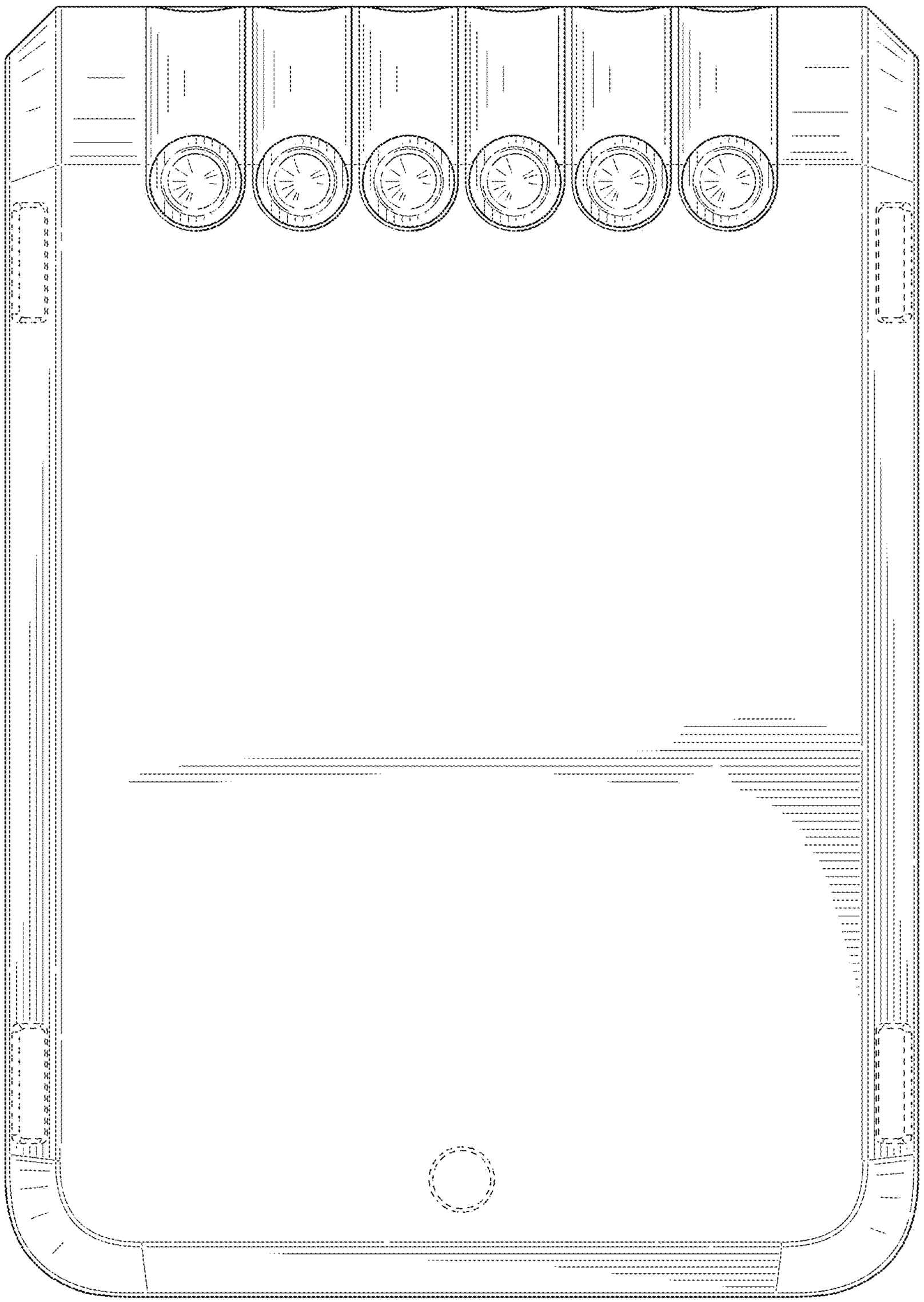

FIG. 21

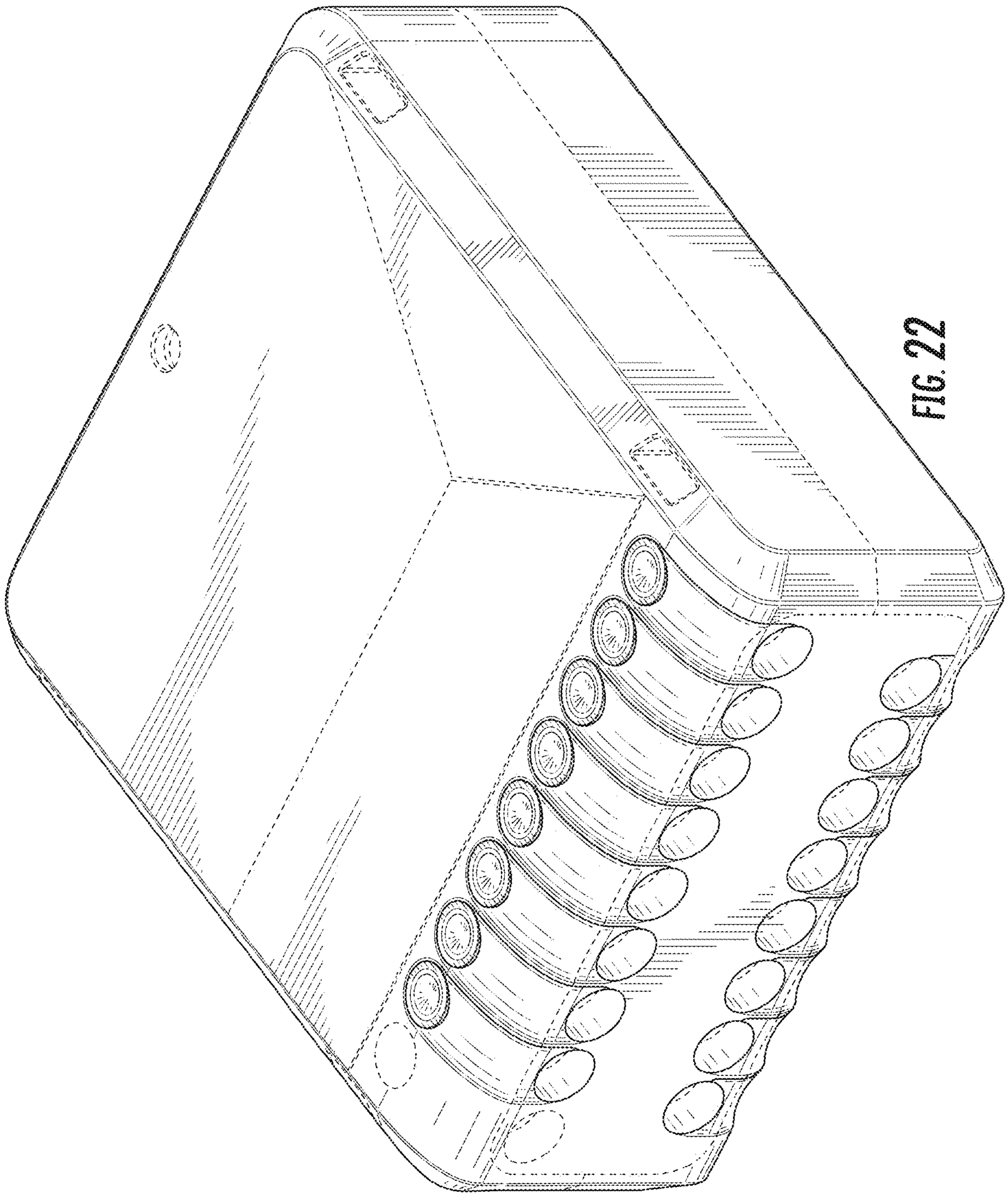

FIG. 22

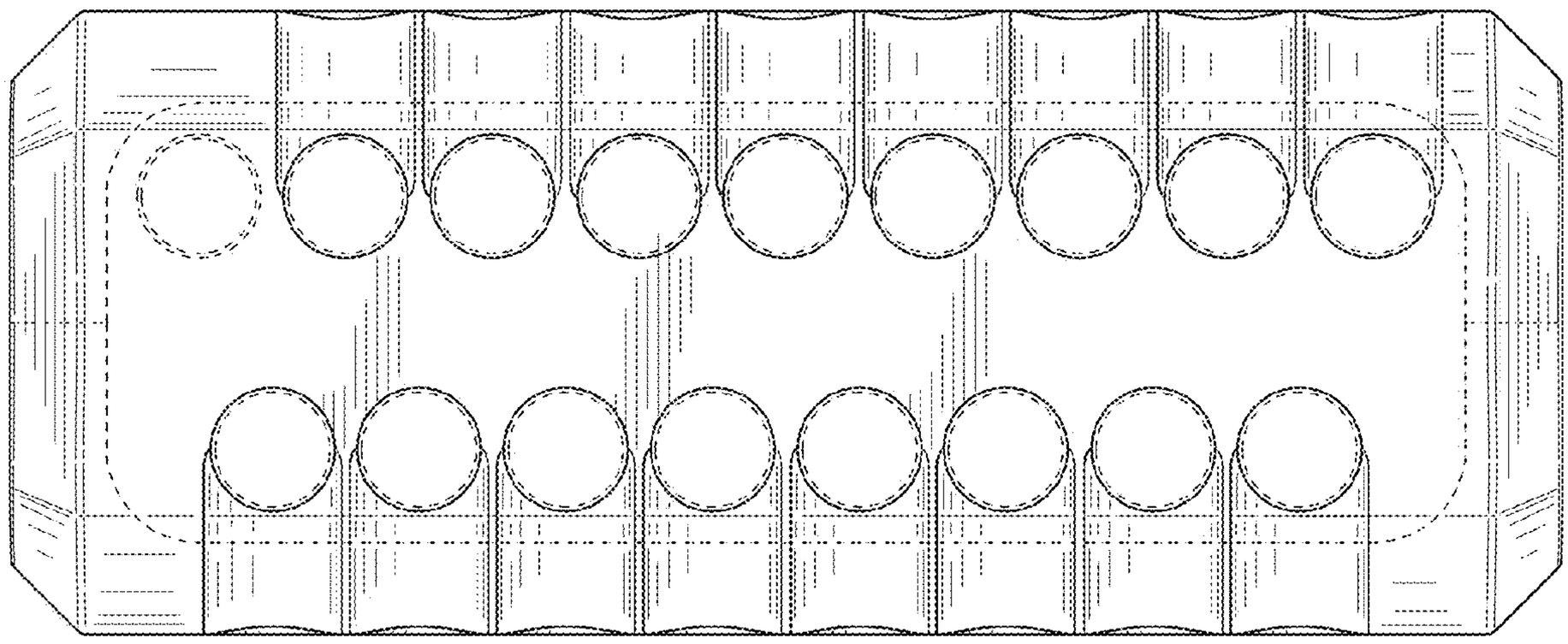

FIG. 23

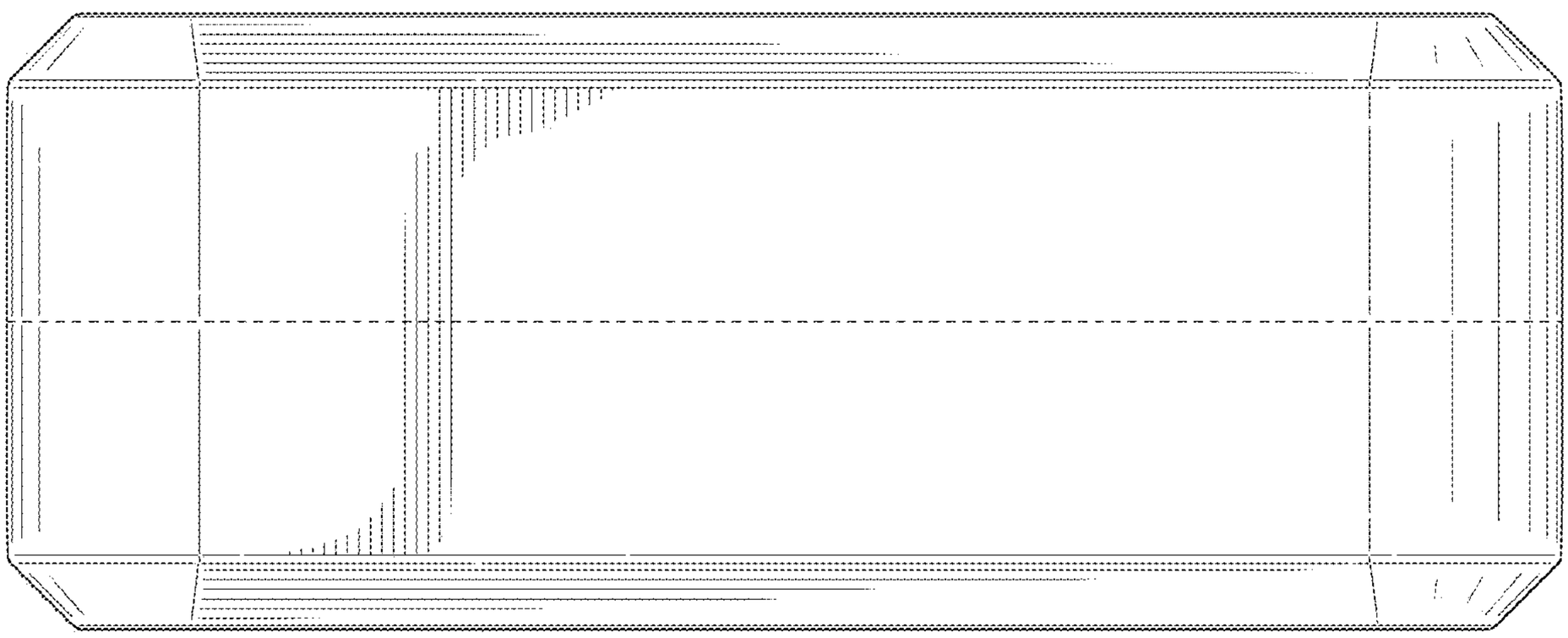

FIG. 24

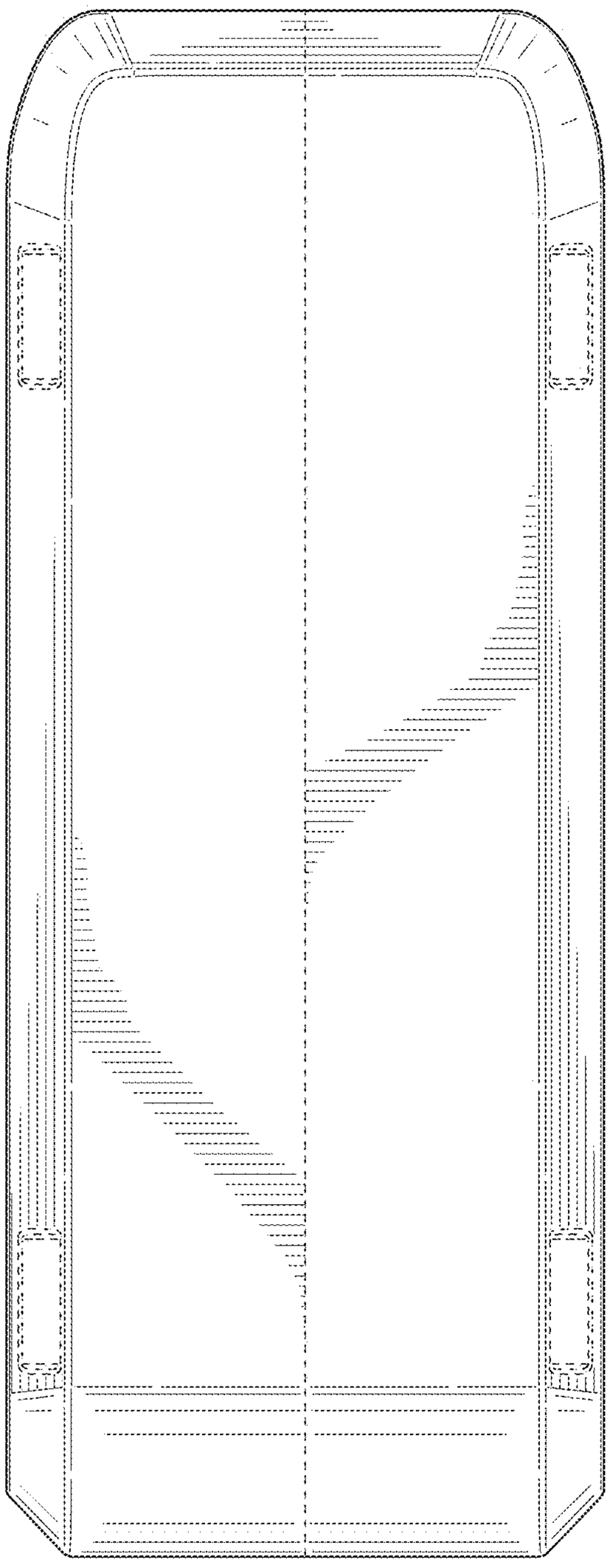

FIG. 25

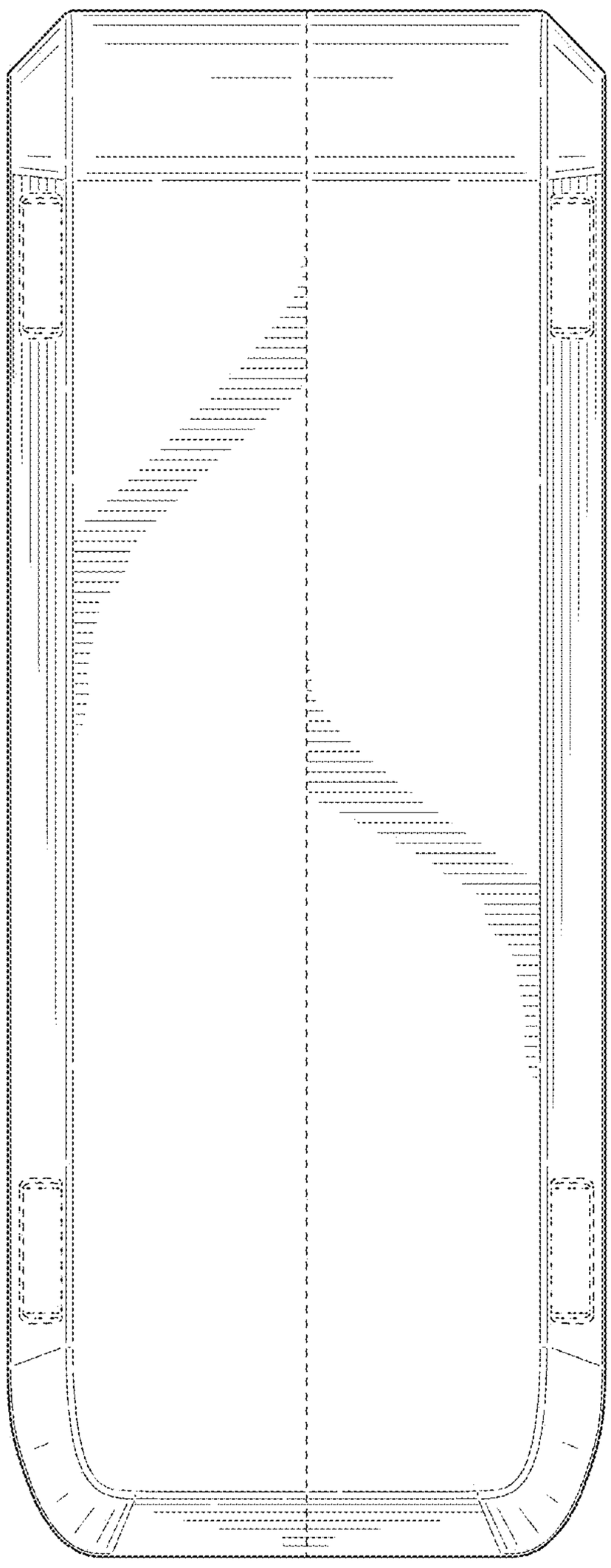

FIG. 26

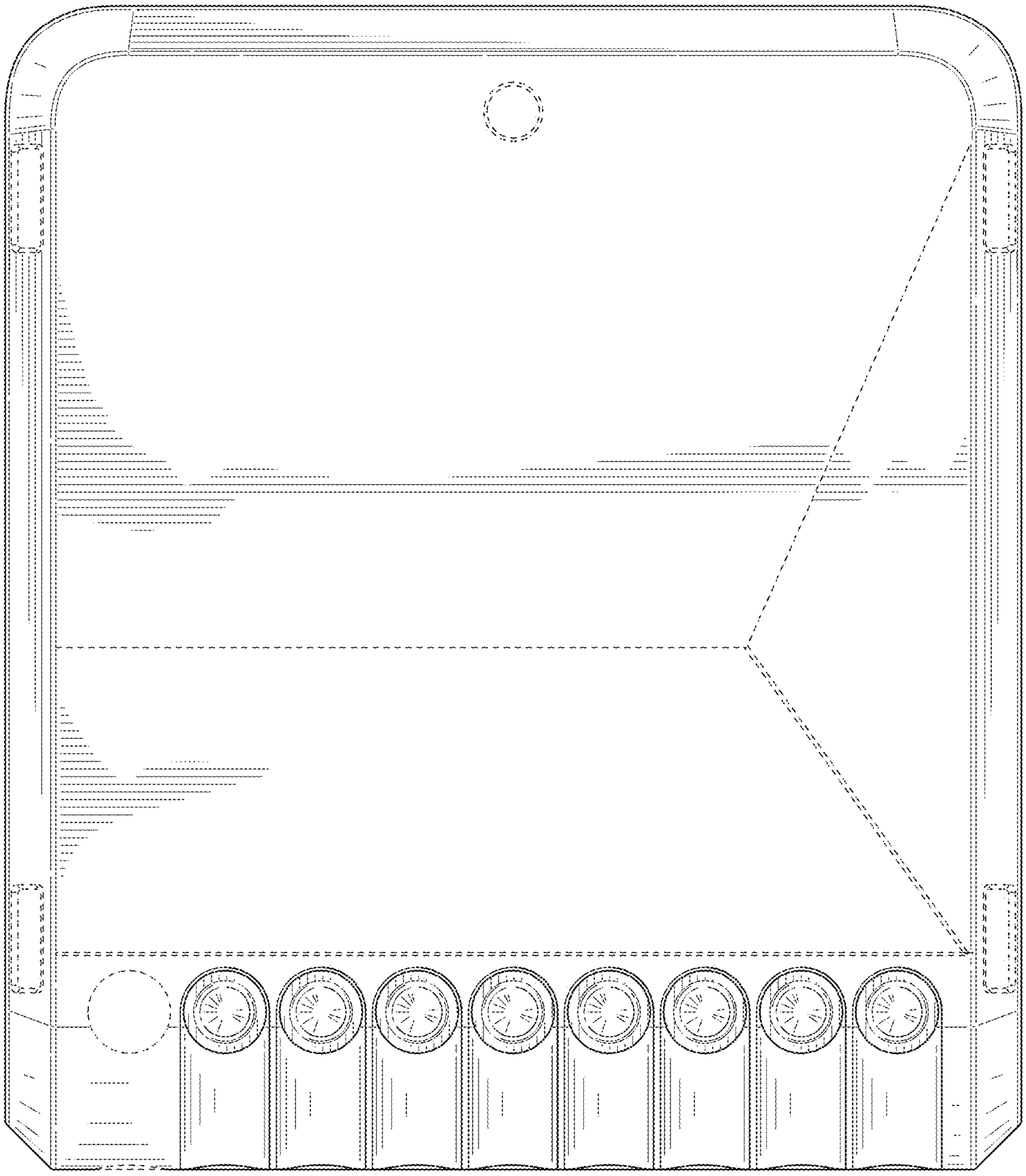

FIG. 27

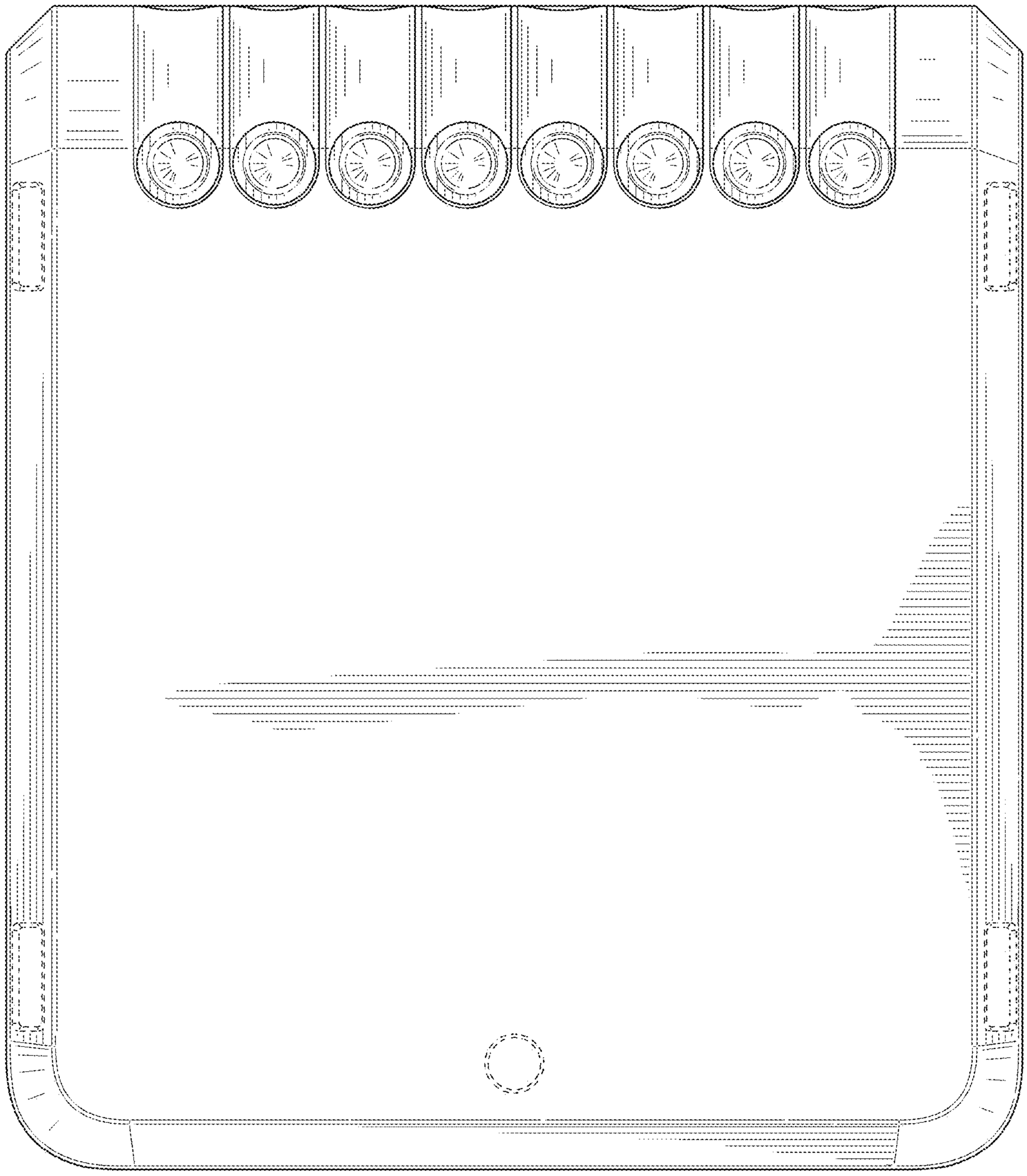

FIG. 28