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(12) **United States Design Patent** (10) **Patent No.:** **US D951,926 S**
O’Neil et al. (45) **Date of Patent:** **** May 17, 2022**

- (54) **ELECTRONIC GATEWAY**
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- (51) **LOC (13) Cl.** **14-03**
- (52) **U.S. Cl.**
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- (58) **Field of Classification Search**
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D14/242, 125, 129, 130, 142, 155, 167,
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D14/198, 230, 104.6, 300, 301, 312, 313,
D14/388, 474; D13/123, 162, 162.1, 168,
D13/173, 177, 184, 199, 152, 158, 146;
D10/104.1, 106.1, 106.95, 46, 52, 53, 60,
D10/75, 80, 83, 94, 103, 122, 123–125,
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CPC G06F 1/1686; G06F 1/1684; G06F 1/181;
H04L 12/2834; H04W 88/00; H04W
88/005; H04W 88/02; H04W 88/08;
H04W 88/12; H04W 88/14; H04W 88/16;
H04W 88/18; H05K 7/20; H05K 7/20154
See application file for complete search history.

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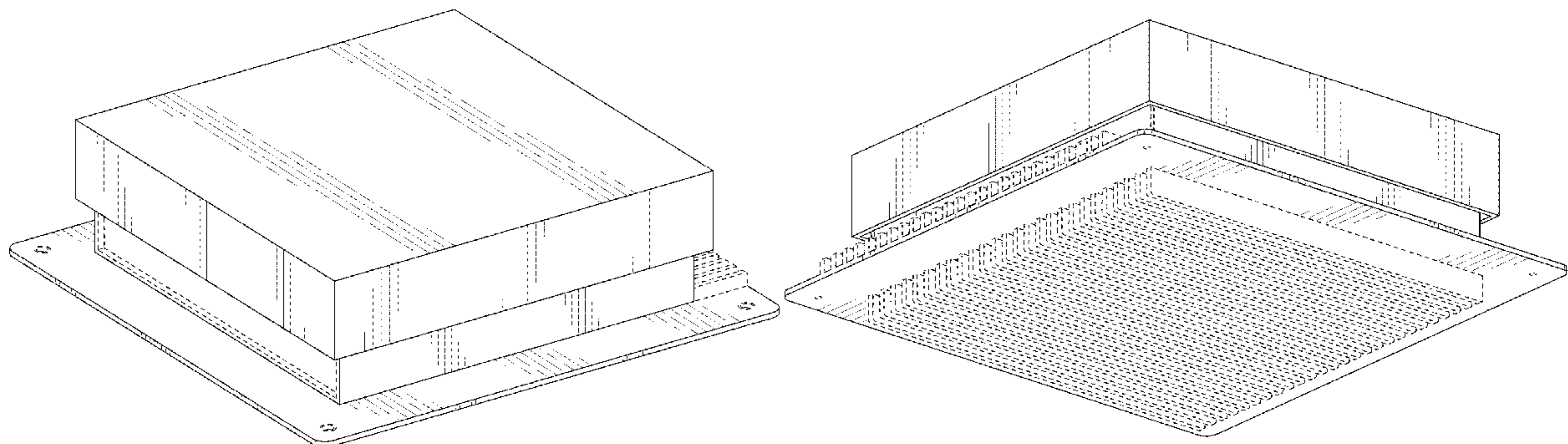
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(57) **CLAIM**
The ornamental design for an electronic gateway, as shown and described.

DESCRIPTION
FIG. 1 is a front top perspective view of an electronic gateway showing our new design;
FIG. 2 is a rear bottom perspective view thereof;
FIG. 3 is a left side view thereof;
FIG. 4 is a right side view thereof;
FIG. 5 is a front view thereof;
FIG. 6 is a rear view thereof;
FIG. 7 is a top view thereof; and,
FIG. 8 is a bottom view thereof.
The broken lines depict portions of the electronic gateway that form no part of the claimed design.

1 Claim, 6 Drawing Sheets



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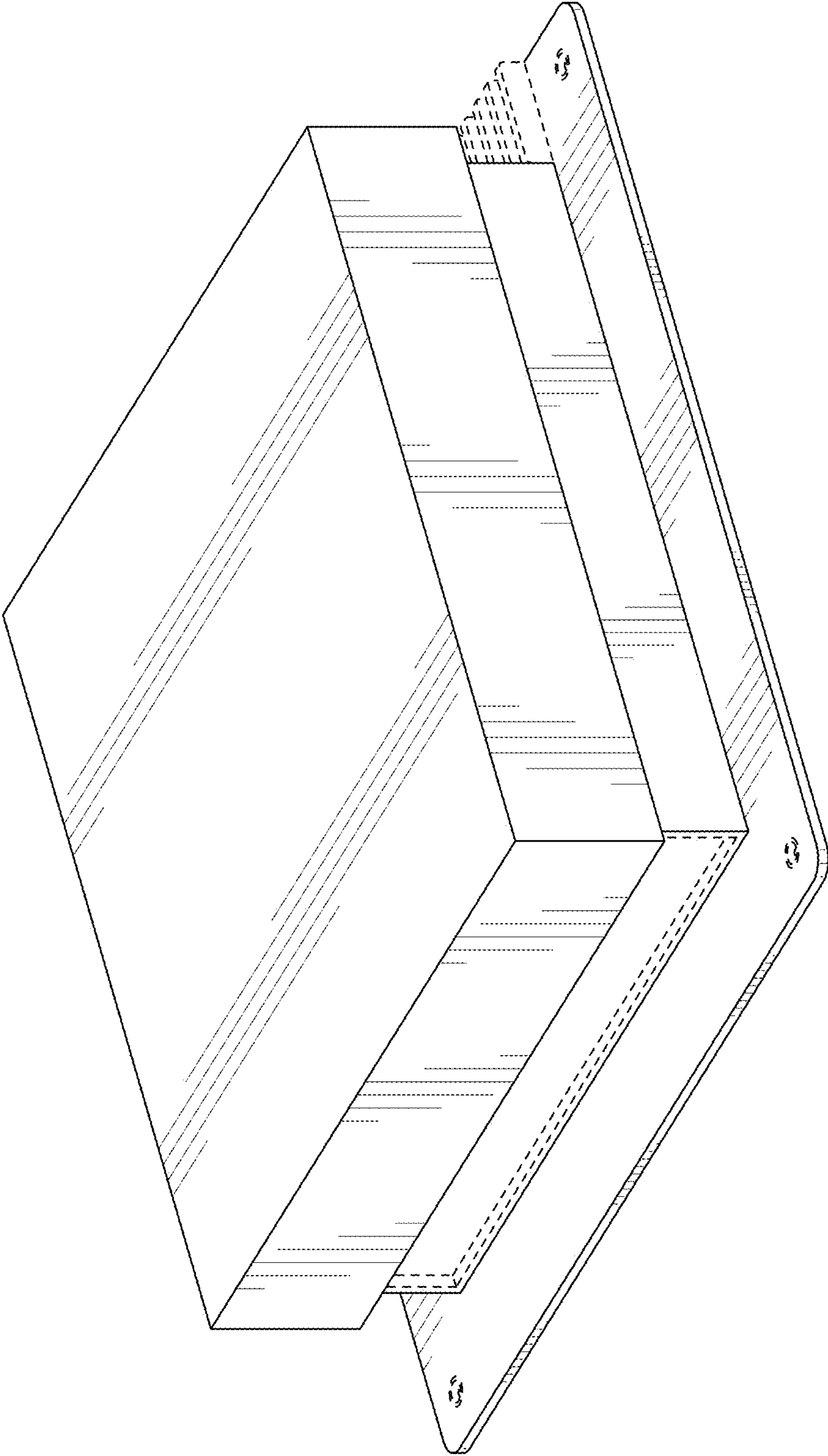


FIG. 1

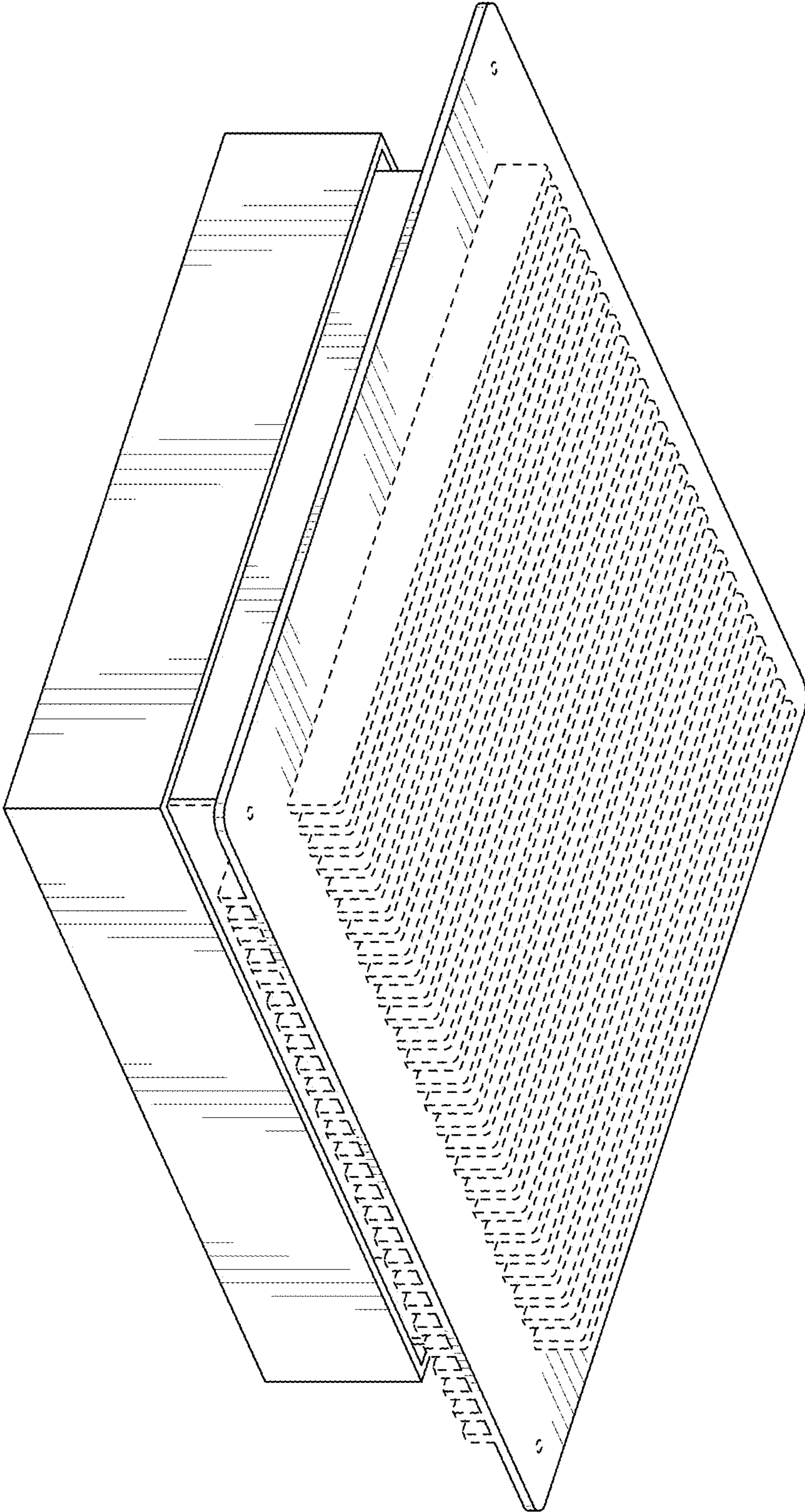


FIG. 2

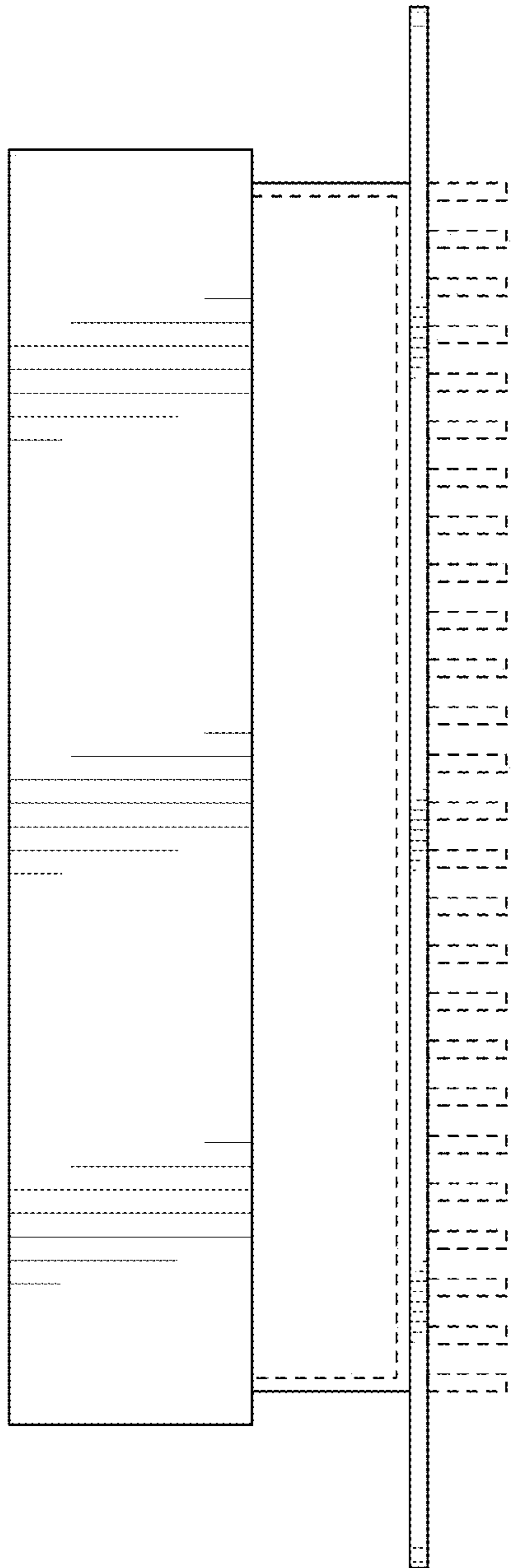


FIG. 3

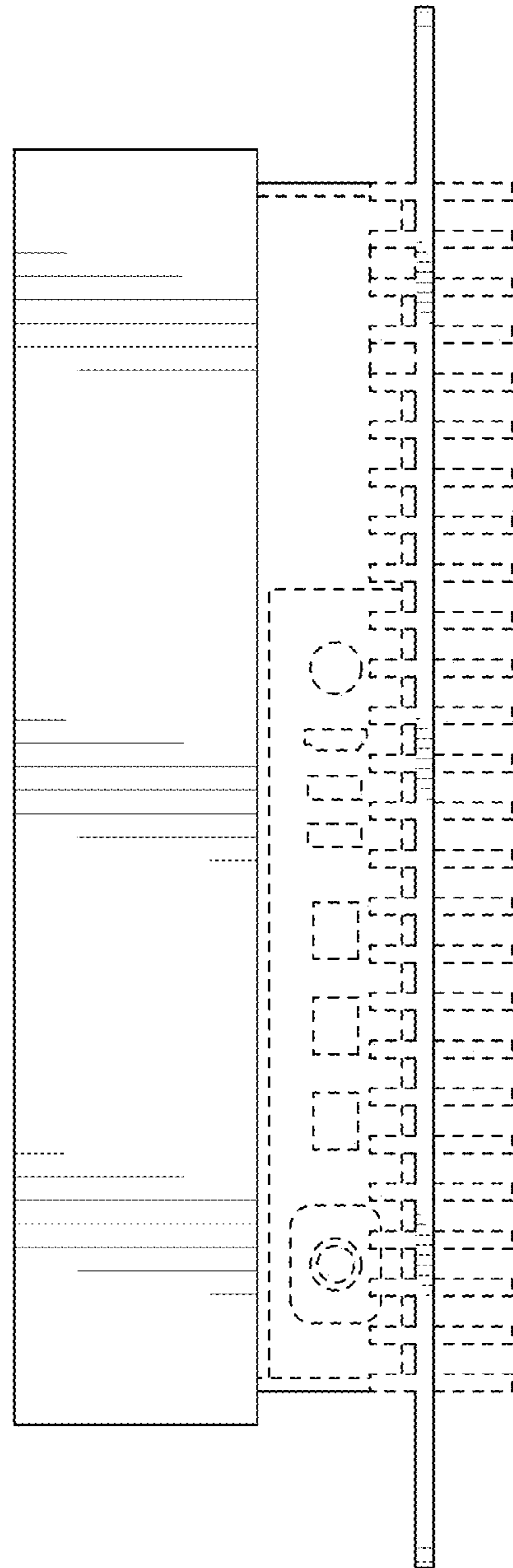


FIG. 4

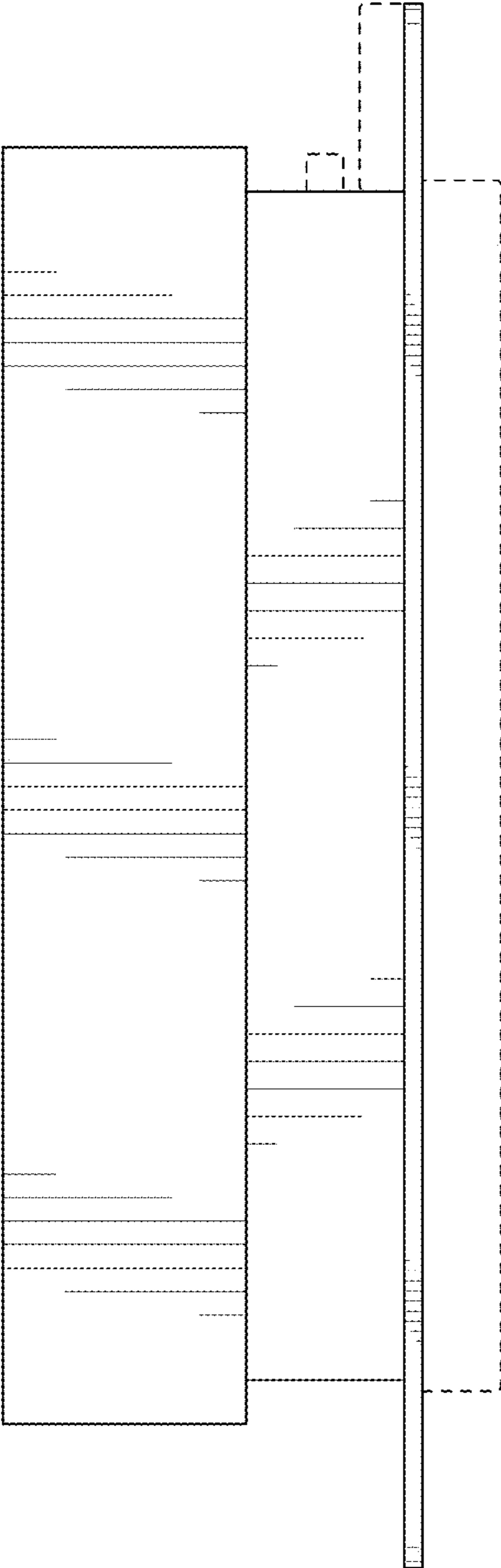


FIG. 5

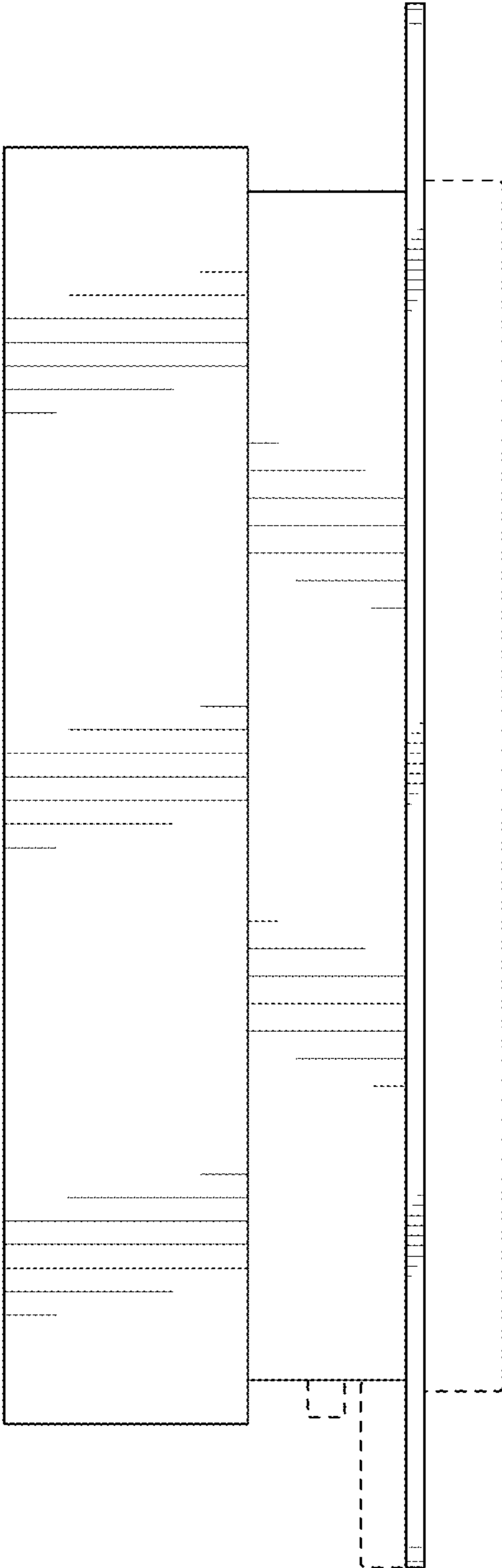
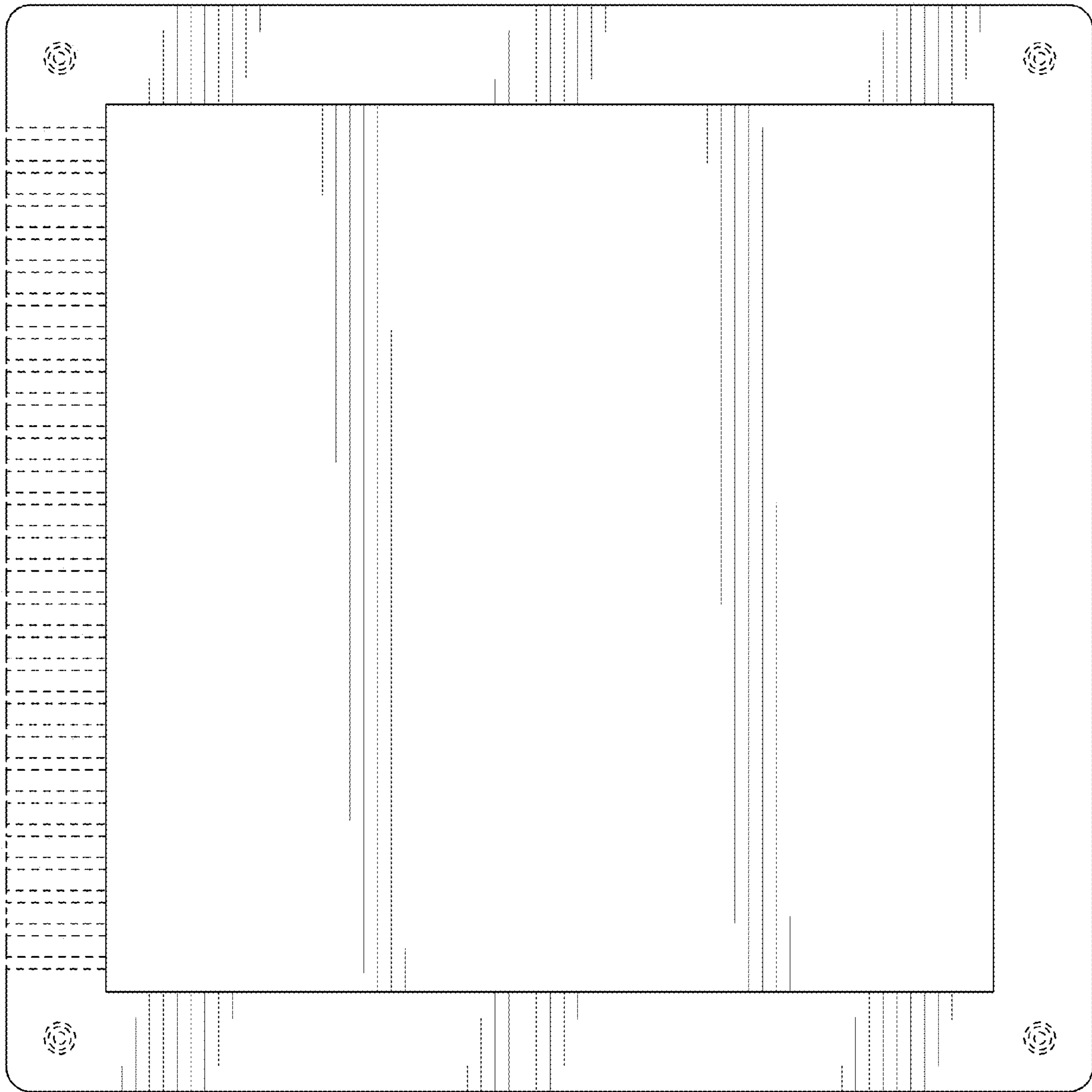


FIG. 6

FIG. 7



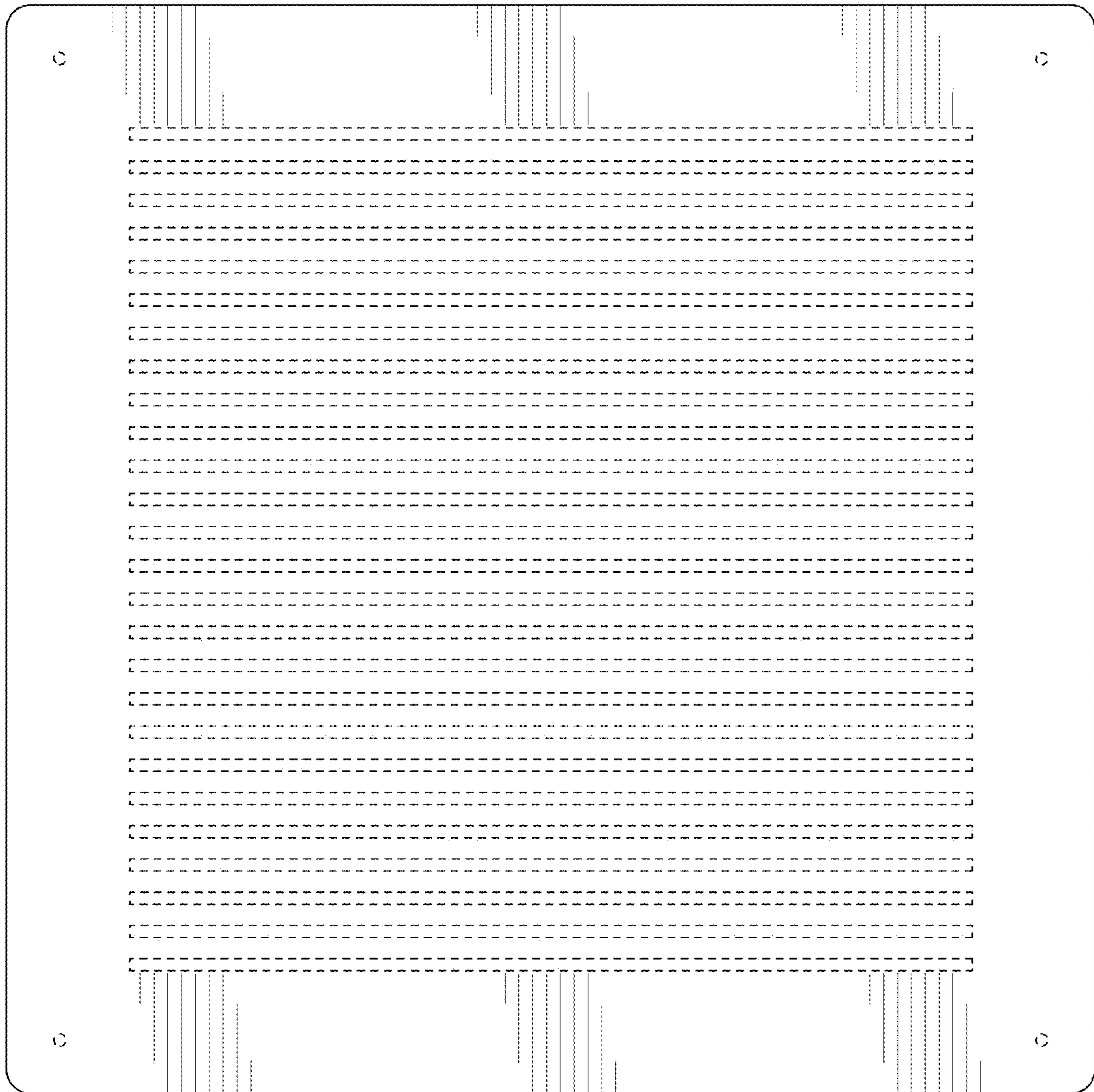


FIG. 8