

US00D949862S

(12) **United States Design Patent** (10) **Patent No.:** **US D949,862 S**
Yakushiji et al. (45) **Date of Patent:** **** Apr. 26, 2022**

(54) **TERMINAL BLOCK ADAPTER FOR
PROCESS CONTROL DEVICE**

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(**) Term: **15 Years**

(21) Appl. No.: **29/748,172**

(22) Filed: **Aug. 27, 2020**

(51) **LOC (13) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/432**

(58) **Field of Classification Search**
USPC D14/300–302, 308–313, 314, 348,
D14/353–355, 432, 439–446, 240;
361/679.02, 679.31, 679.5, 690, 694,
361/679.23, 801, 802; 312/332.1, 334.1;
D13/162; D10/50
CPC A45C 11/00; A45C 2011/003; B60R
11/0241; B60R 11/0252; F16M 13/00;
F16M 11/00

See application file for complete search history.

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Primary Examiner — Austin Murphy

(74) *Attorney, Agent, or Firm* — Wenderoth, Lind &
Ponack, L.L.P.

(57) **CLAIM**

The ornamental design for a terminal block adapter for process control device, as shown and described.

DESCRIPTION

FIG. 1 is a front, top and left side perspective view of a terminal block adapter for process control device showing our new design;

FIG. 2 is a front, bottom and right side perspective view thereof;

FIG. 3 is a front view thereof;

FIG. 4 is a rear view thereof;

FIG. 5 is a top plan view thereof;

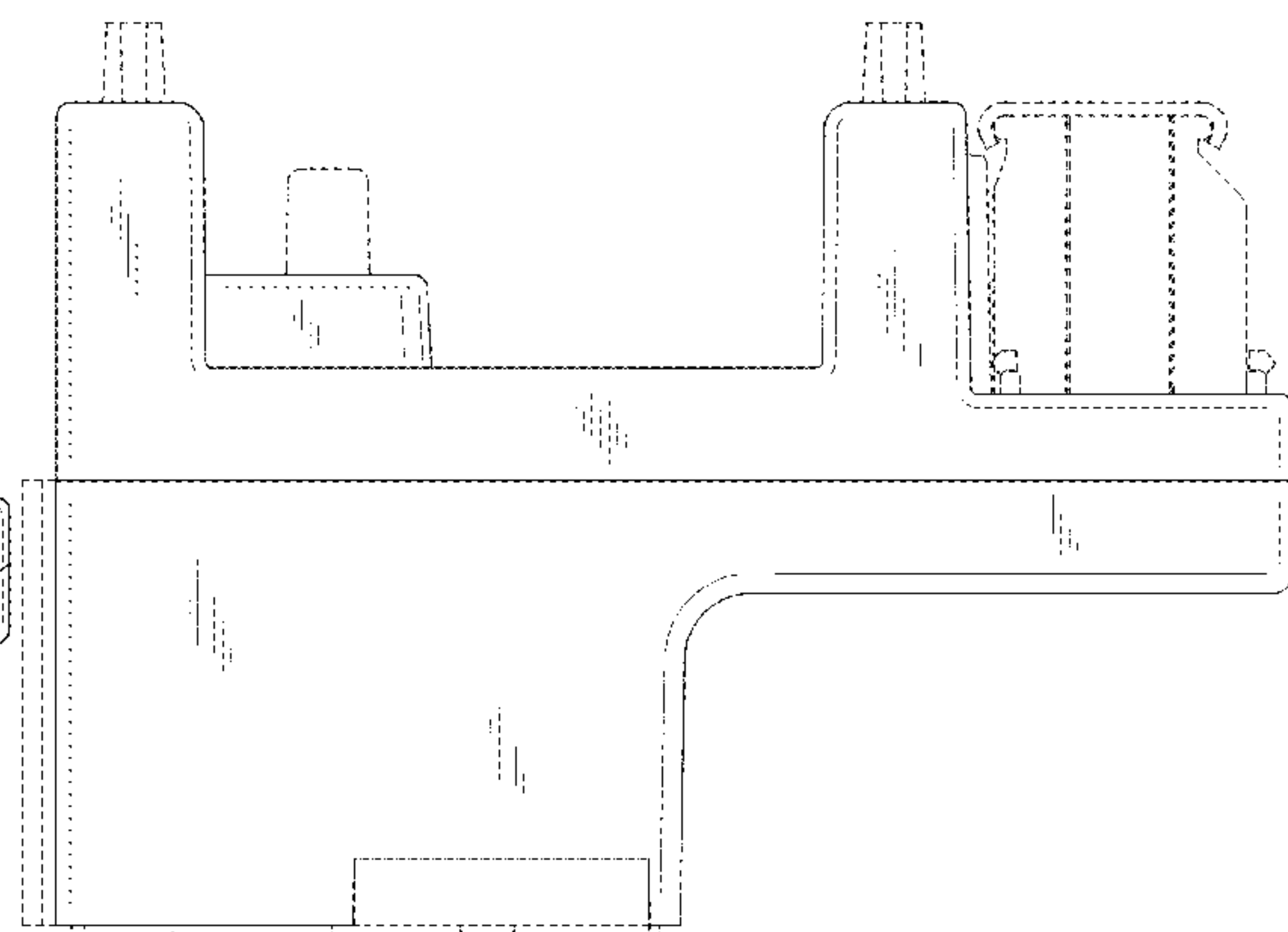
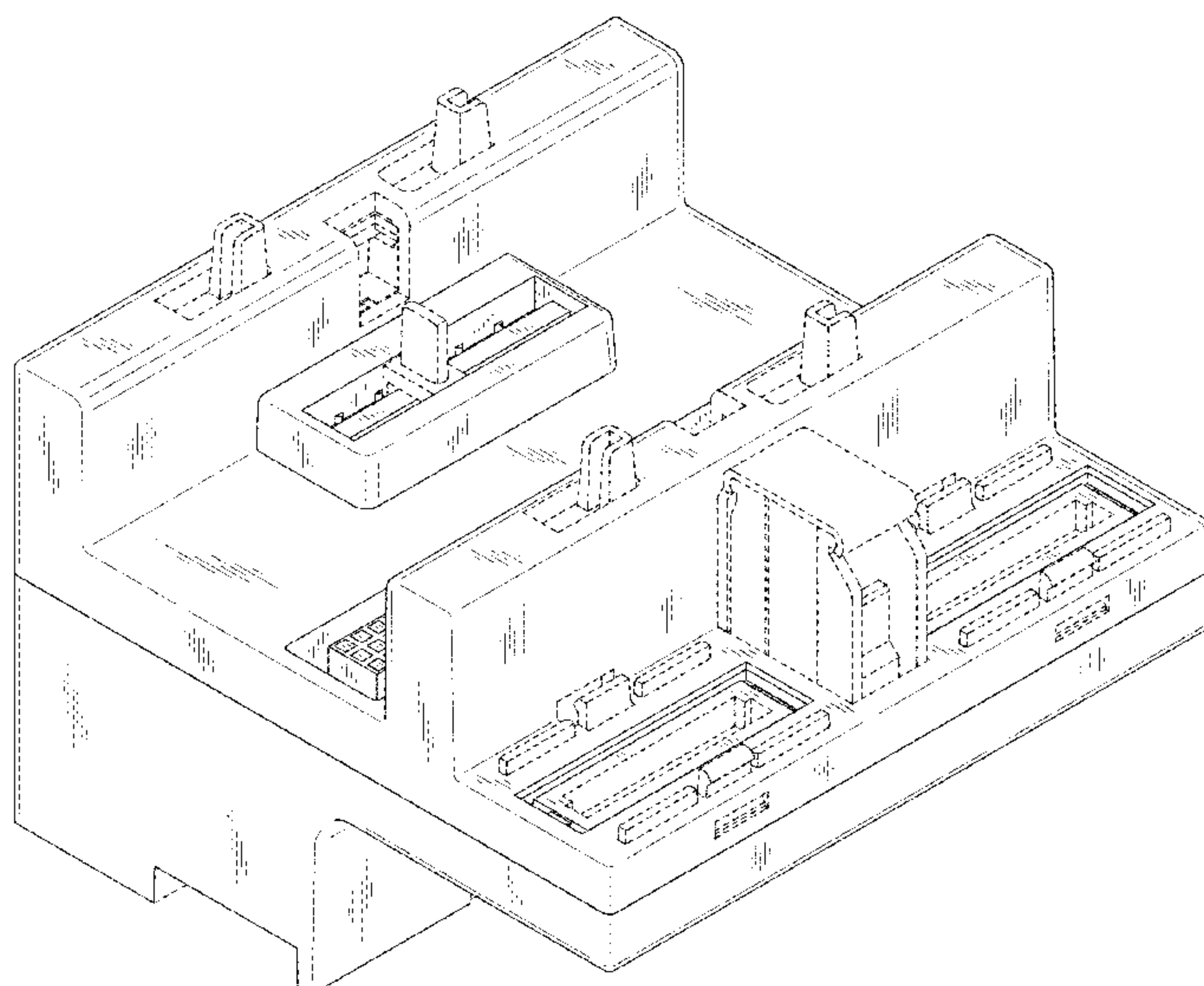
FIG. 6 is a bottom plan view thereof;

FIG. 7 is a right side view thereof; and,

FIG. 8 is a left side view thereof.

The broken lines illustrate portions of the terminal block adapter for process control device that form no part of the claimed design.

1 Claim, 8 Drawing Sheets



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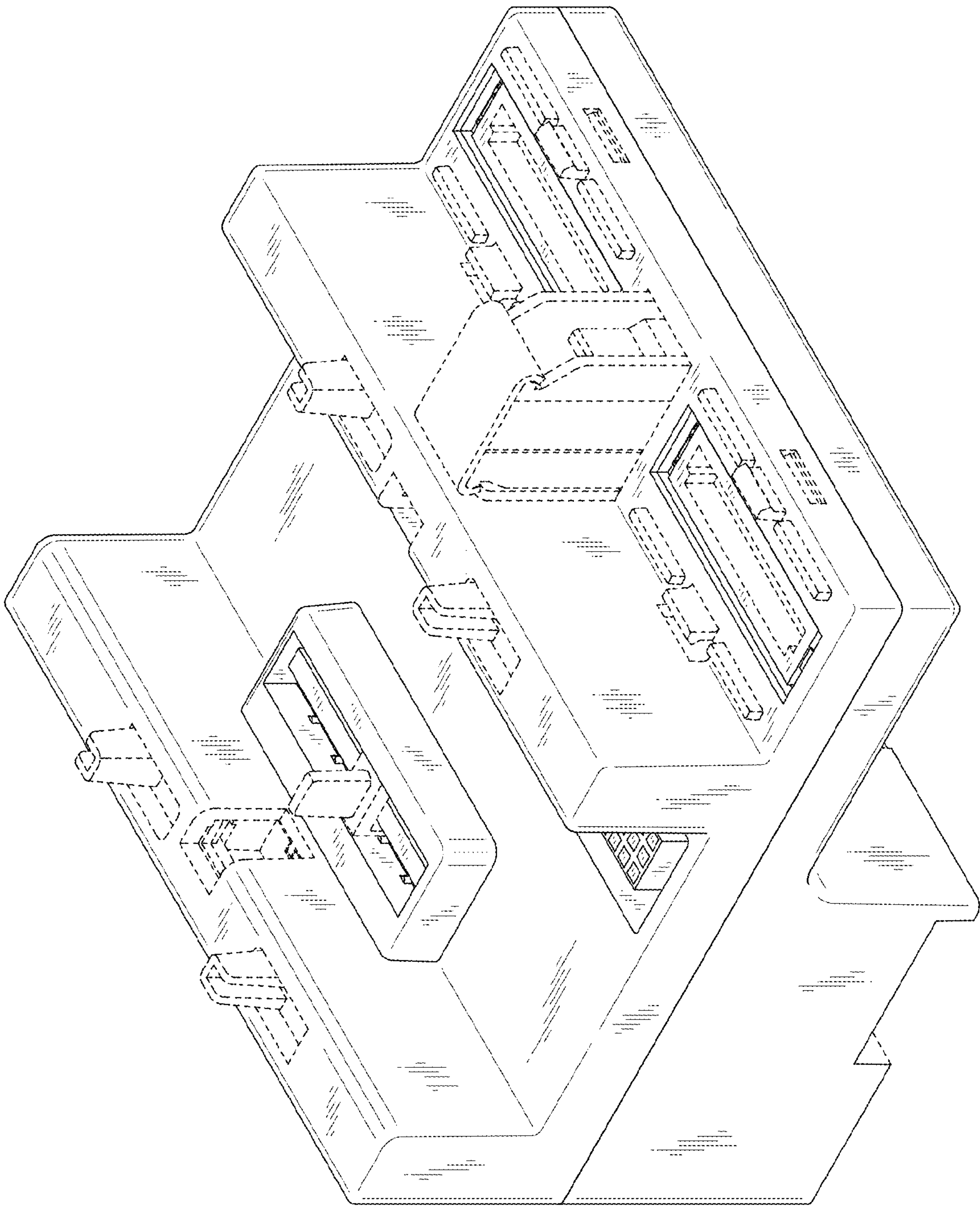


FIG.1

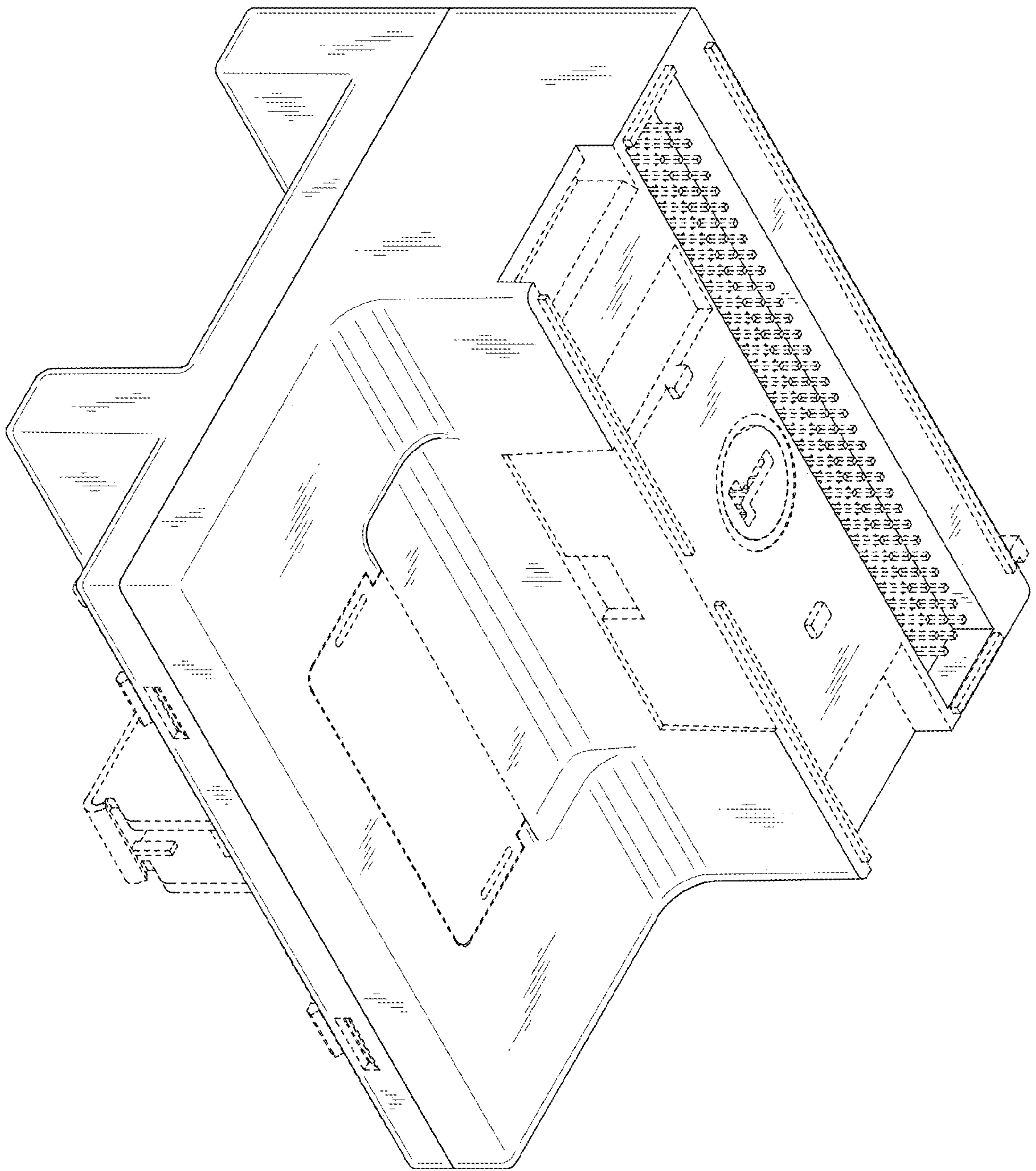
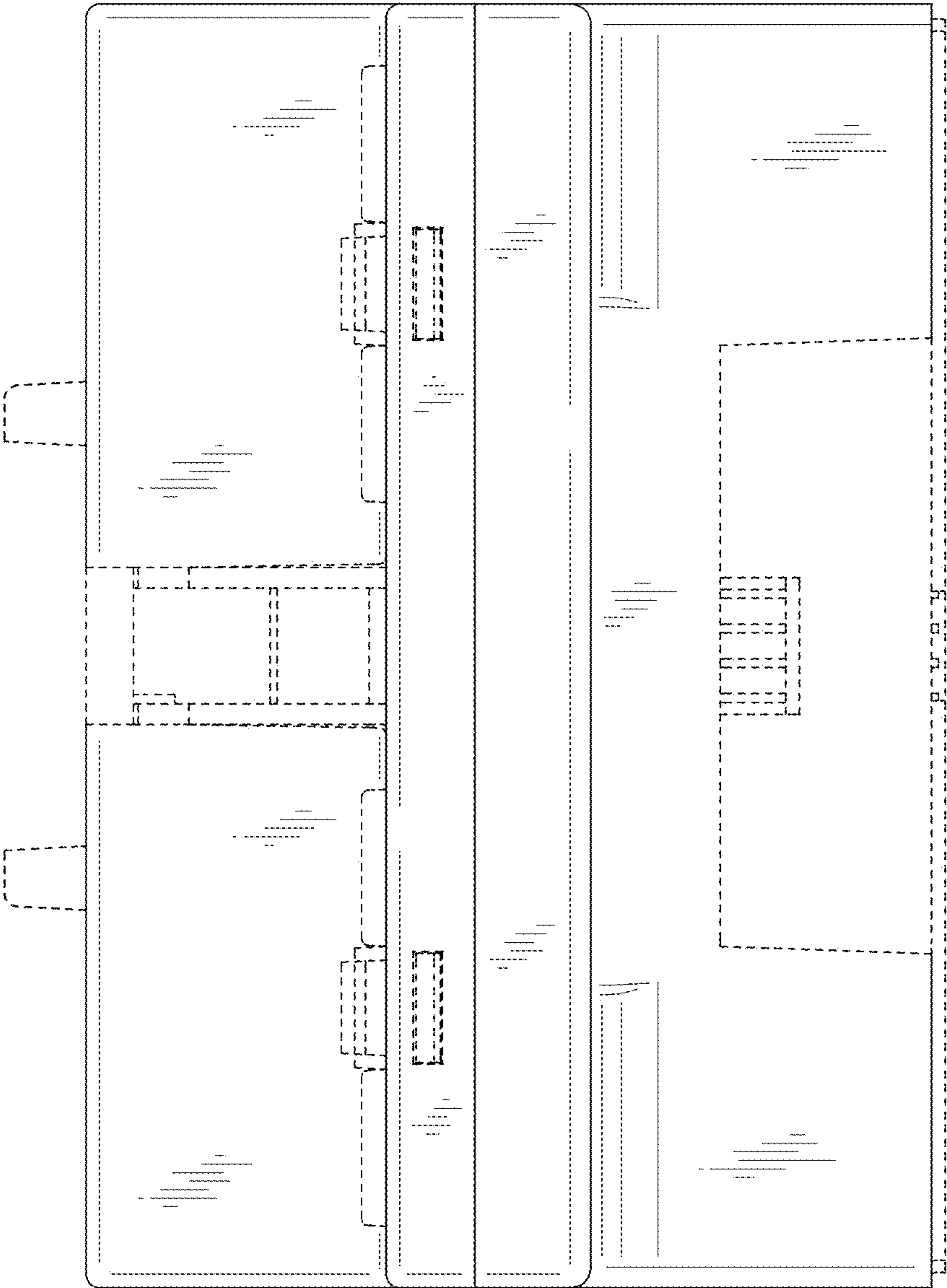


FIG.2

FIG.3



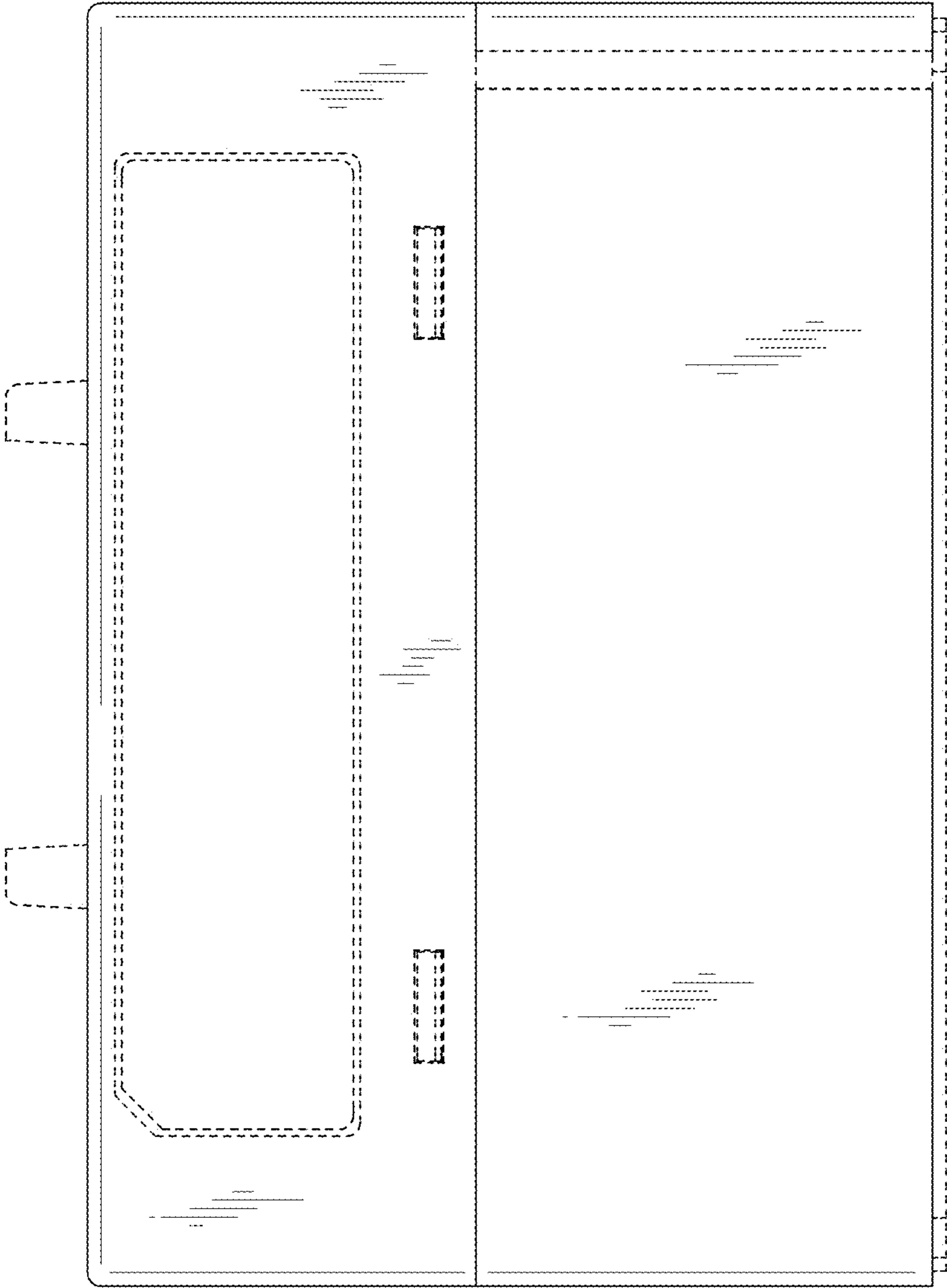


FIG.4

FIG.5

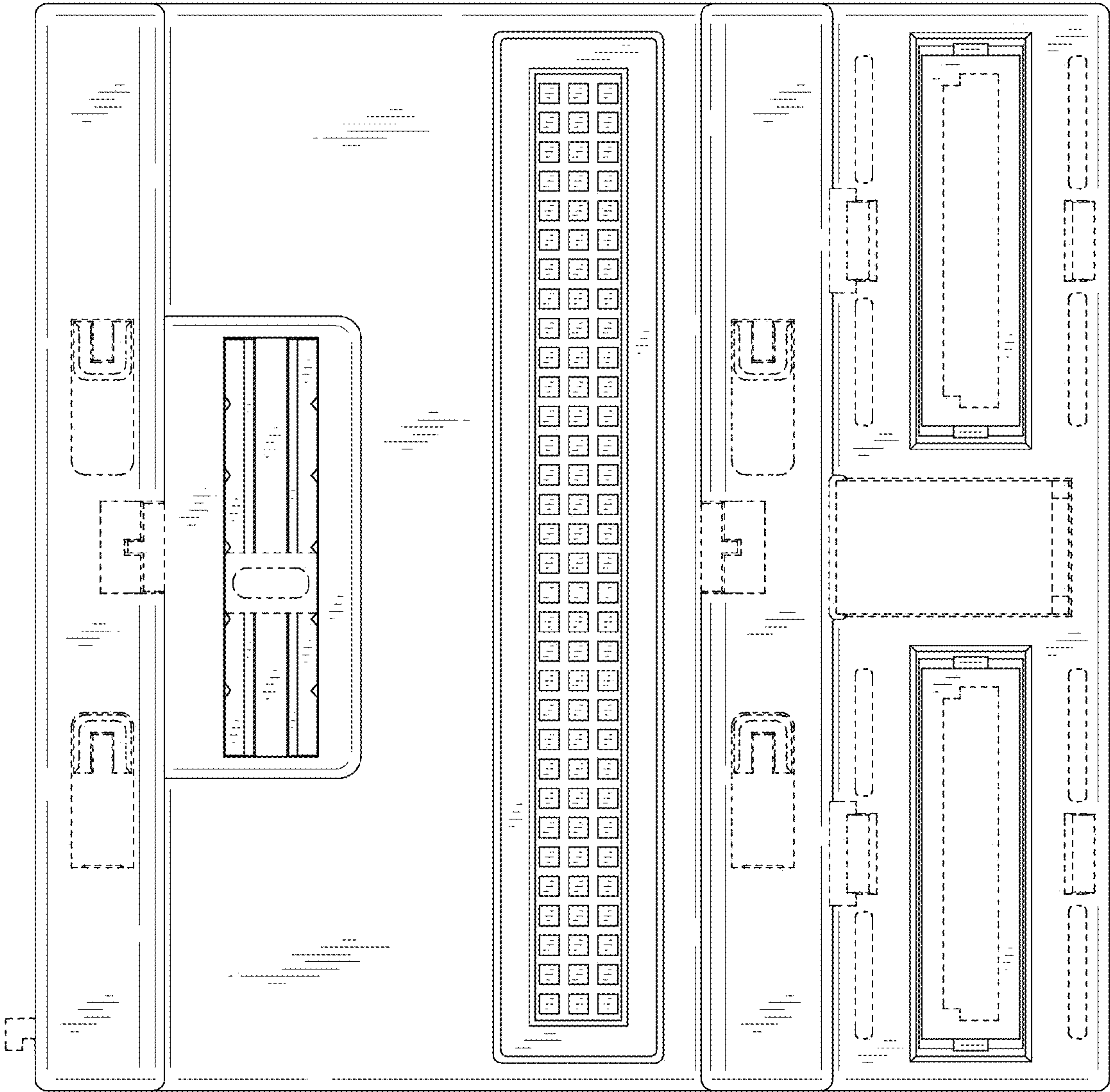
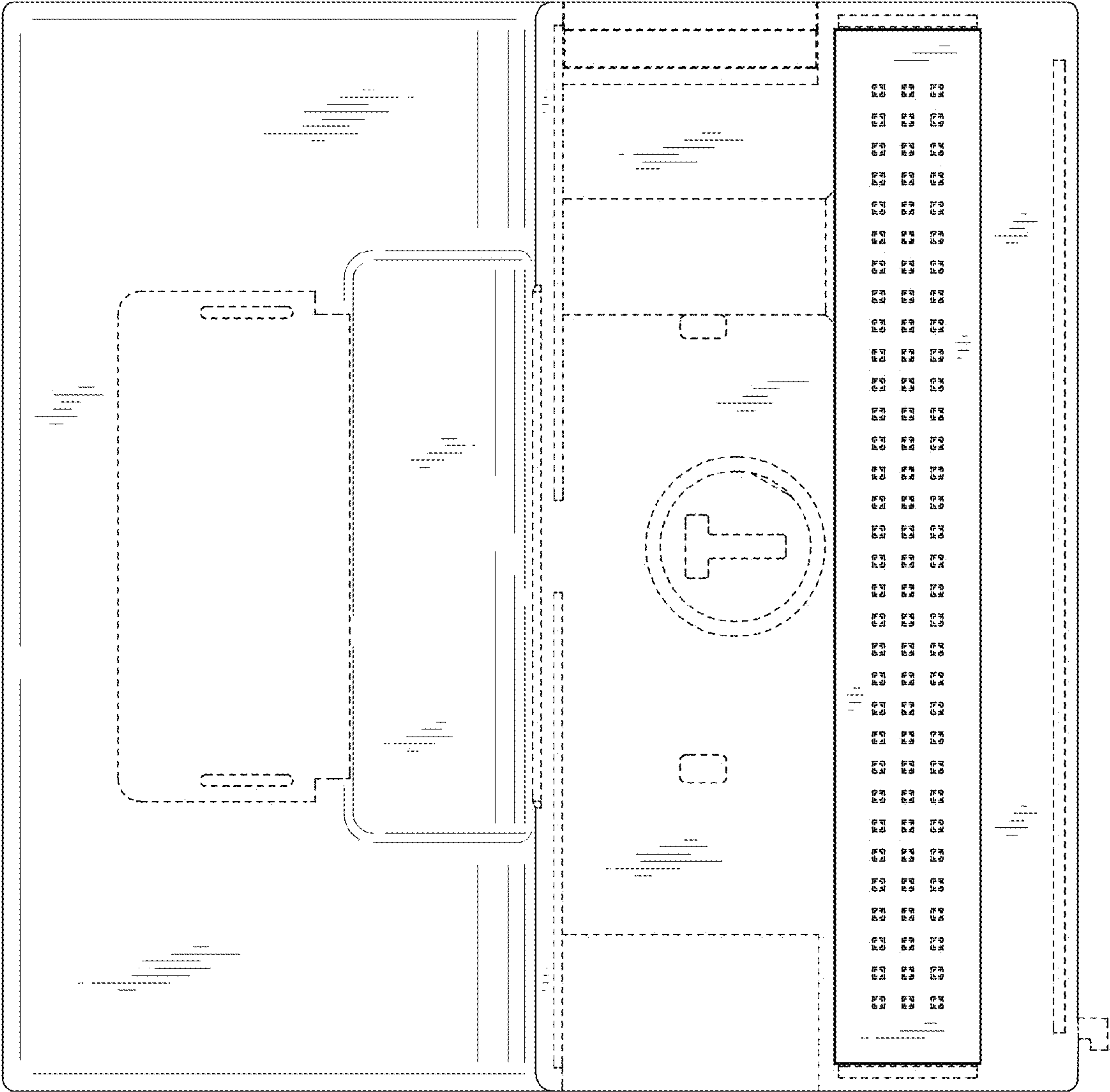


FIG.6



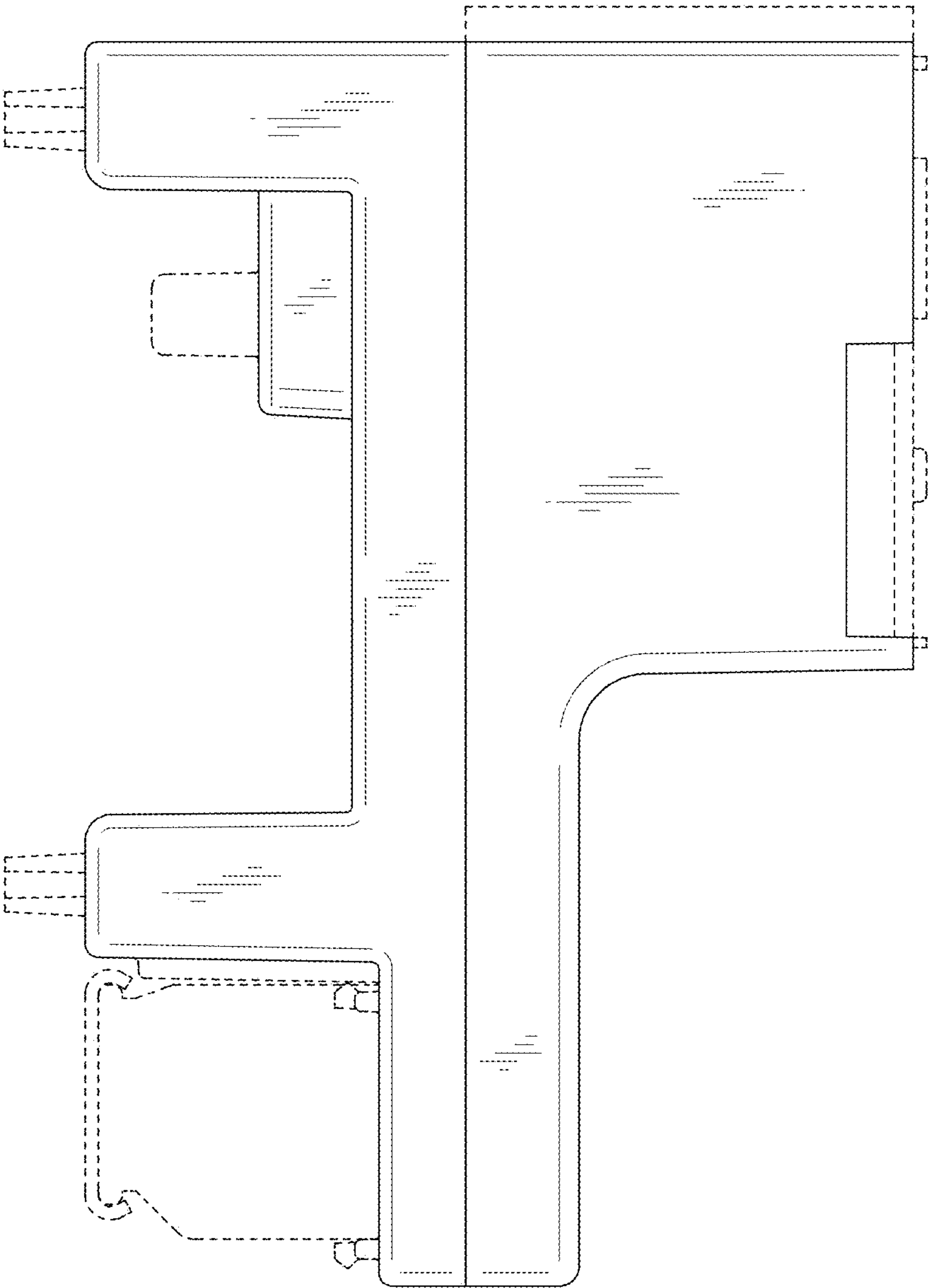
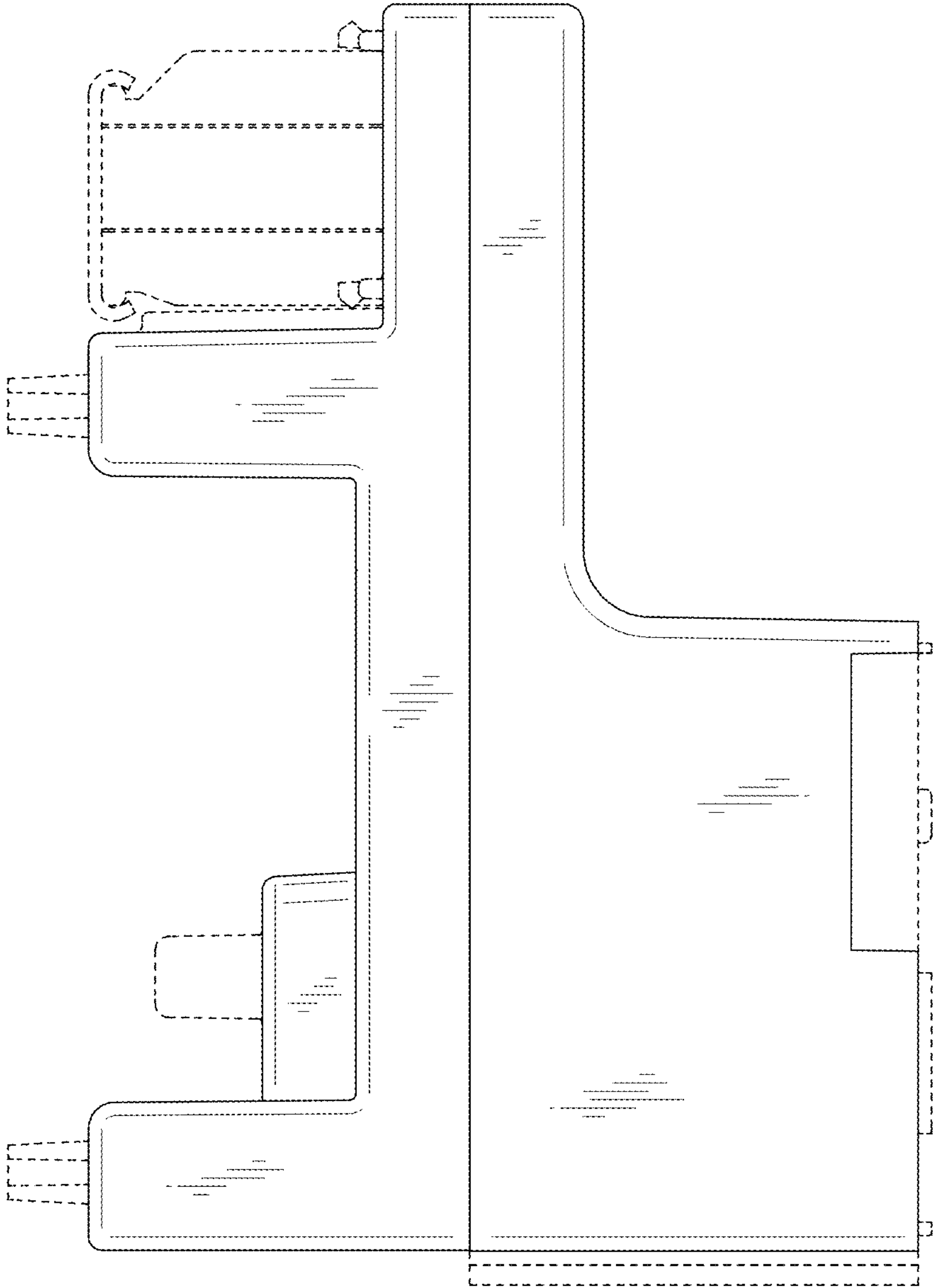


FIG. 7

FIG. 8



UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : D949,862 S
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INVENTOR(S) : Yuichi Yakushiji

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page

Column 1, Immediately after item (22), please insert the following missing section:

--(30) **Foreign Application Priority Data**

Feb. 28, 2020 (JP) 2020-003873--

Signed and Sealed this
Eighteenth Day of October, 2022



Katherine Kelly Vidal
Director of the United States Patent and Trademark Office