



US00D946754S

(12) **United States Design Patent**
Paramadilok

(10) **Patent No.:** **US D946,754 S**
(45) **Date of Patent:** **** Mar. 22, 2022**

- (54) **ELECTRONIC DEVICE**
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- (73) Assignee: **Triton Holdings, LLC**, Elmhurst, IL (US)
- (**) Term: **15 Years**
- (21) Appl. No.: **29/650,410**
- (22) Filed: **Jun. 6, 2018**

Related U.S. Application Data

- (63) Continuation-in-part of application No. 29/597,748, filed on Mar. 20, 2017, now Pat. No. Des. 863,554.
- (51) **LOC (13) Cl.** **24-02**
- (52) **U.S. Cl.**
USPC **D24/146**
- (58) **Field of Classification Search**
USPC D24/110, 112, 115, 133, 146, 147, 148;
D8/300, 310, 311; D28/52, 53, 54, 55,
D28/56, 57, 58, 59, 60, 61, 62, 63
CPC A61B 2018/00607; A61B 2018/00601;
A61B 17/34; A61B 17/32; A61B 17/16;
A61B 17/14
See application file for complete search history.

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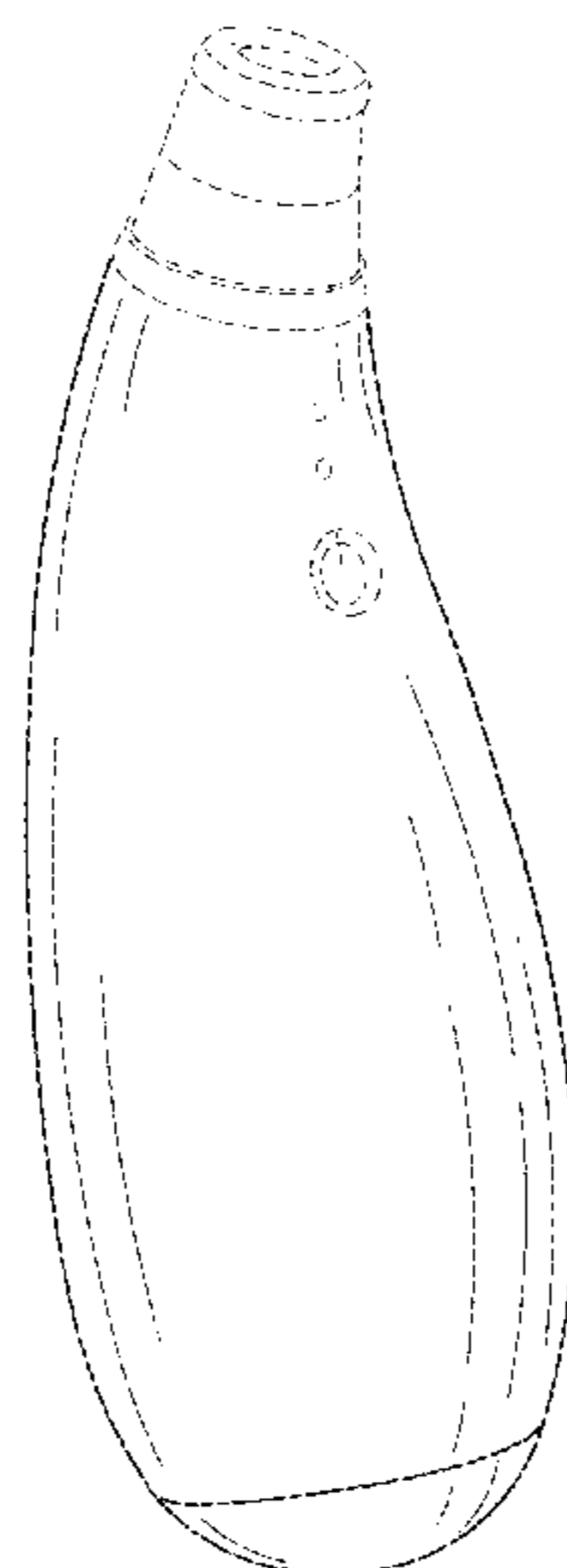
(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an electronic device in accordance with the present invention; FIG. 2 is a front elevation view thereof; FIG. 3 is a rear elevation view thereof; FIG. 4 is a top plan view thereof; FIG. 5 is a bottom plan view thereof; FIG. 6 is a left side elevation view thereof; and, FIG. 7 is a right side elevation view thereof. The broken lines shown in the drawings depict portions of the electronic device that form no part of the claimed design.

1 Claim, 3 Drawing Sheets



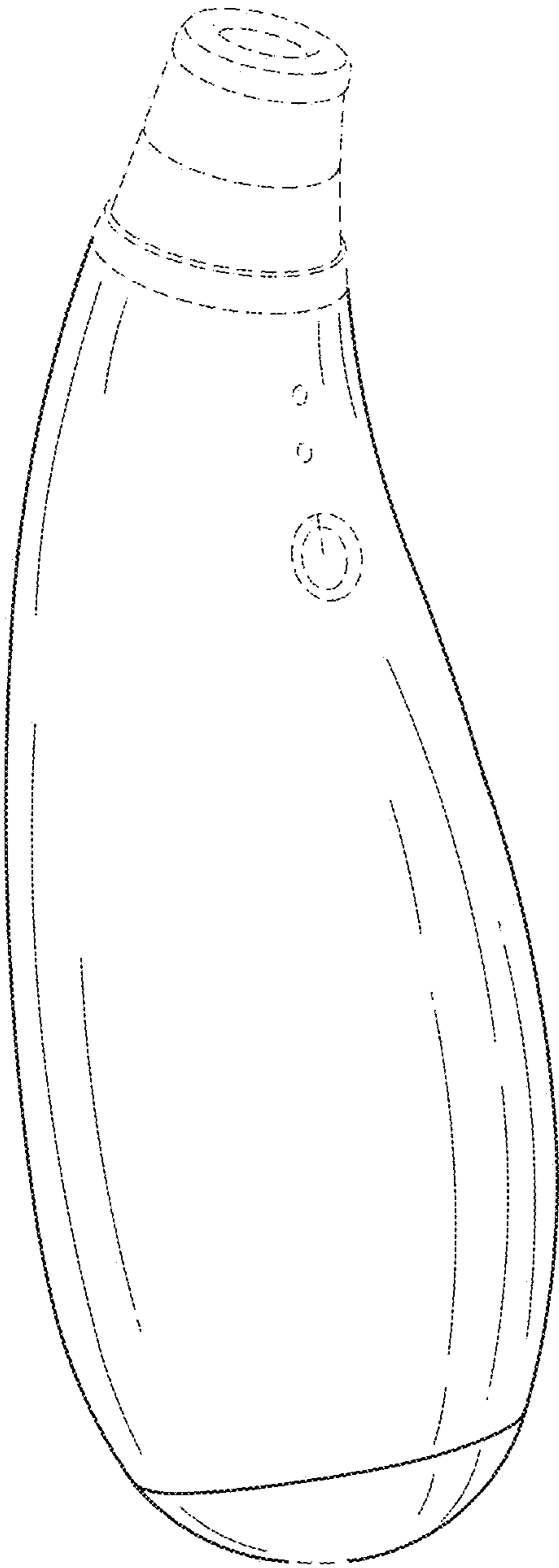


FIG. 1

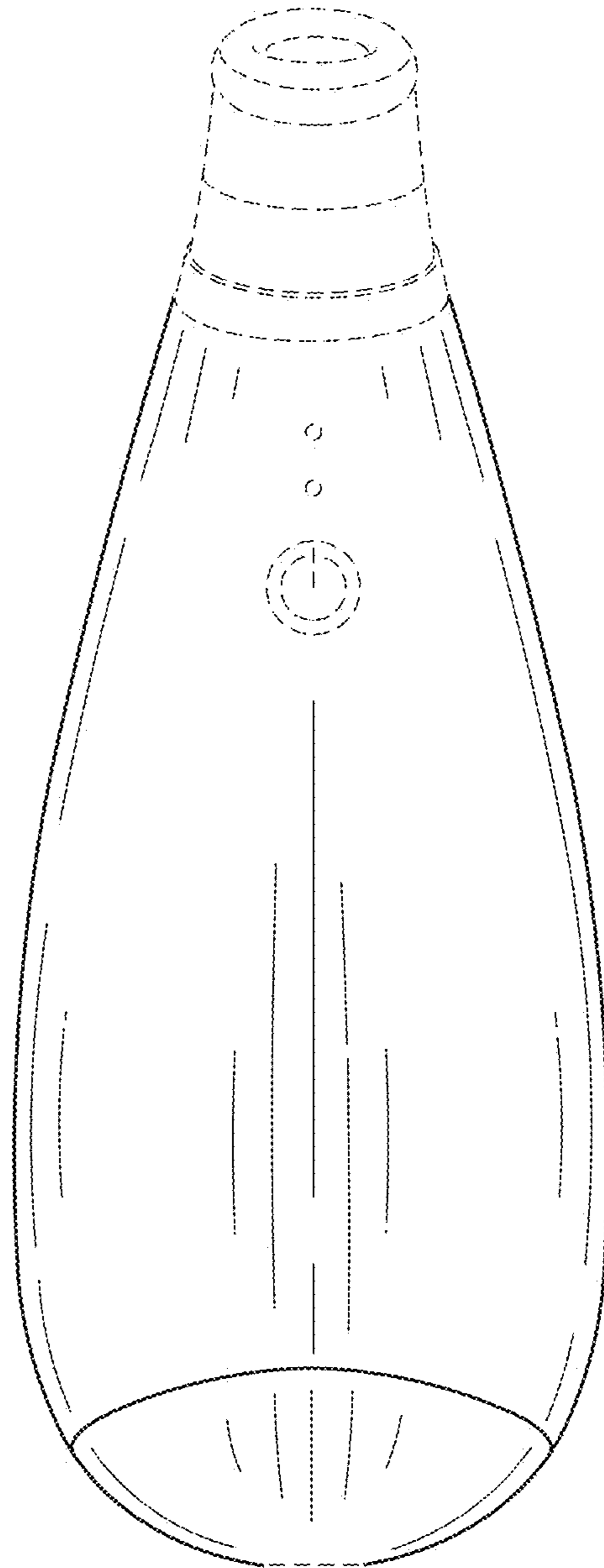


FIG. 2

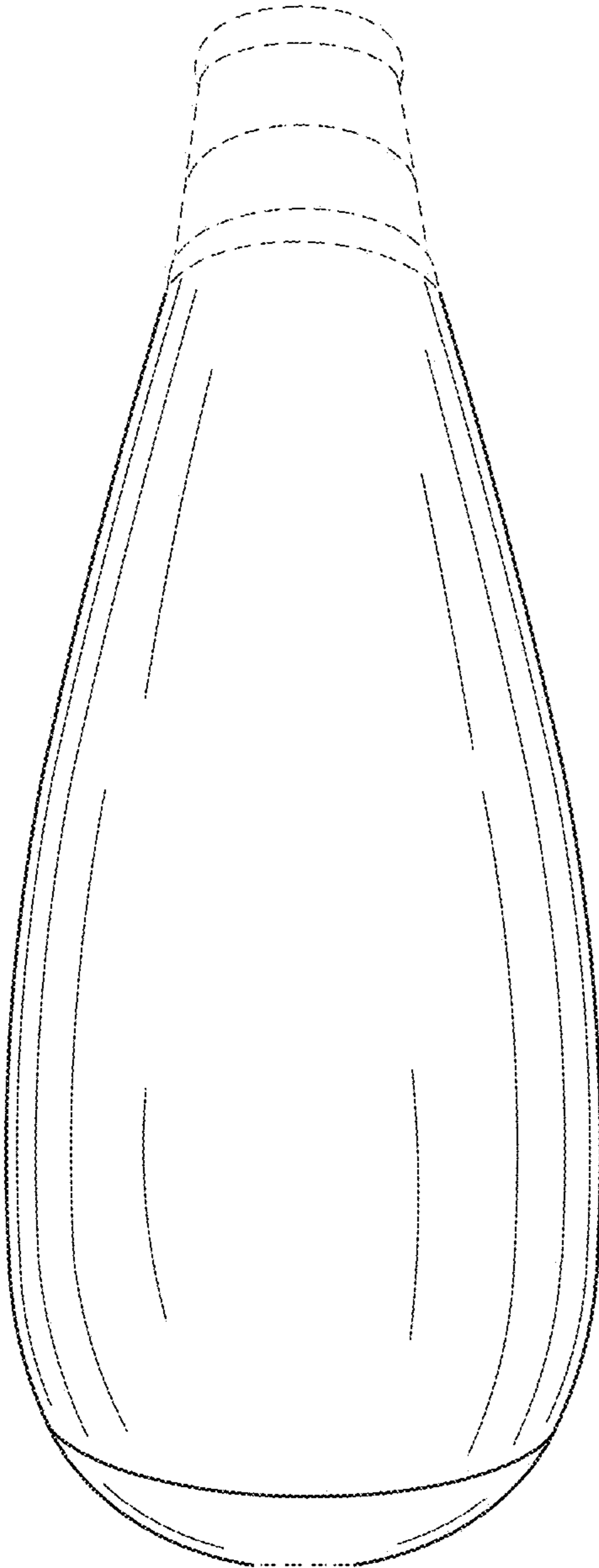


FIG. 3

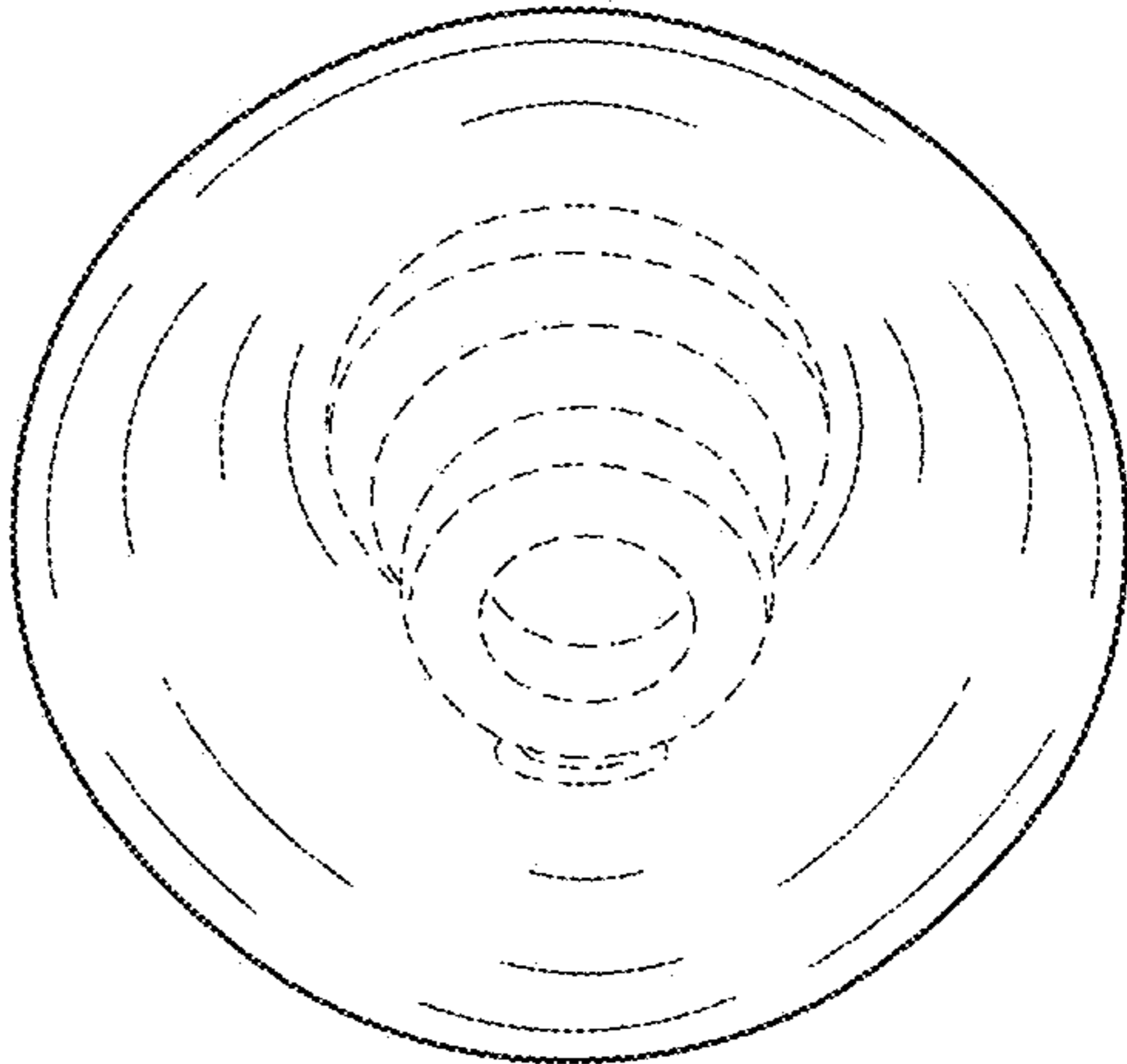


FIG. 4

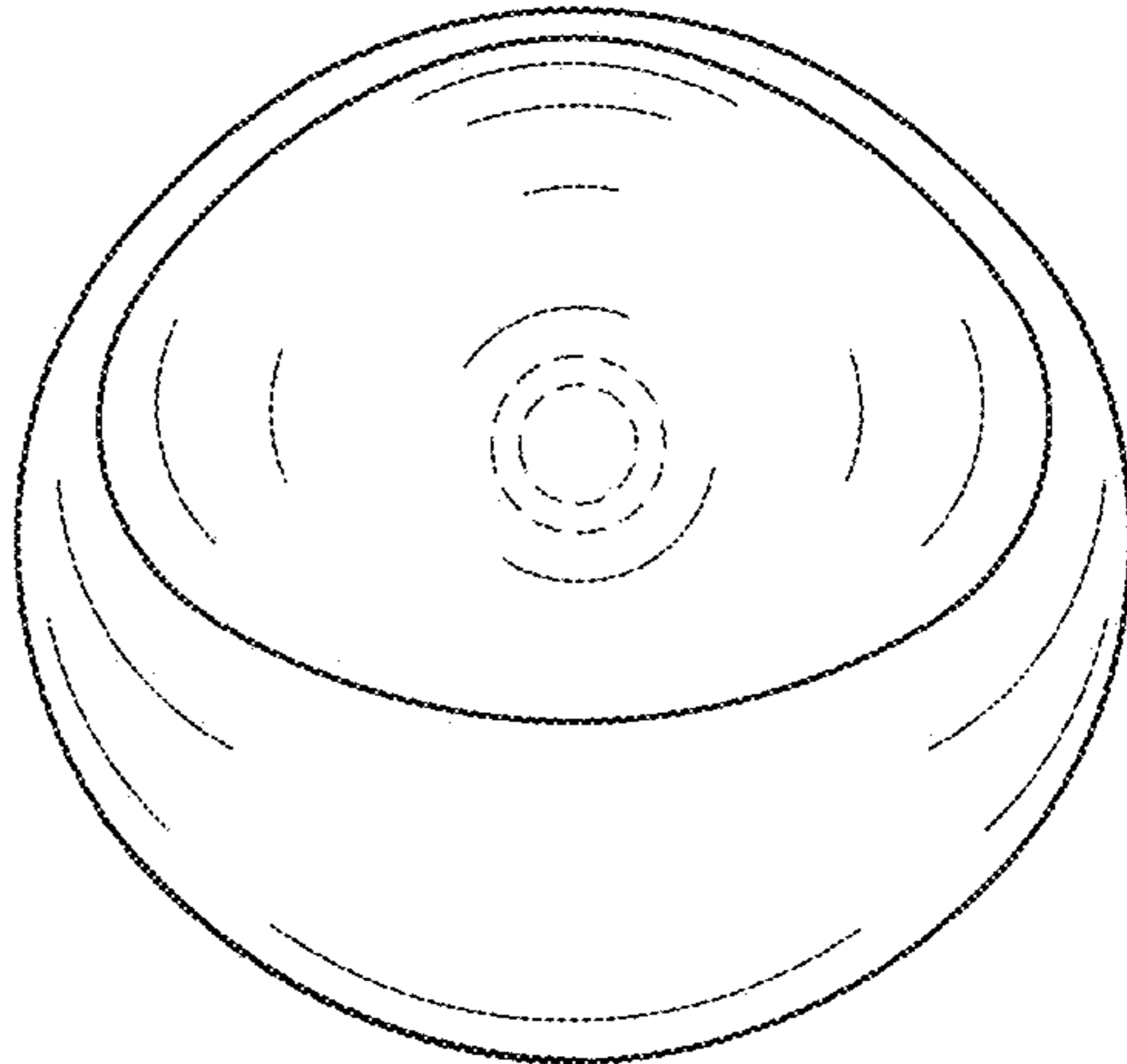


FIG. 5



FIG. 6

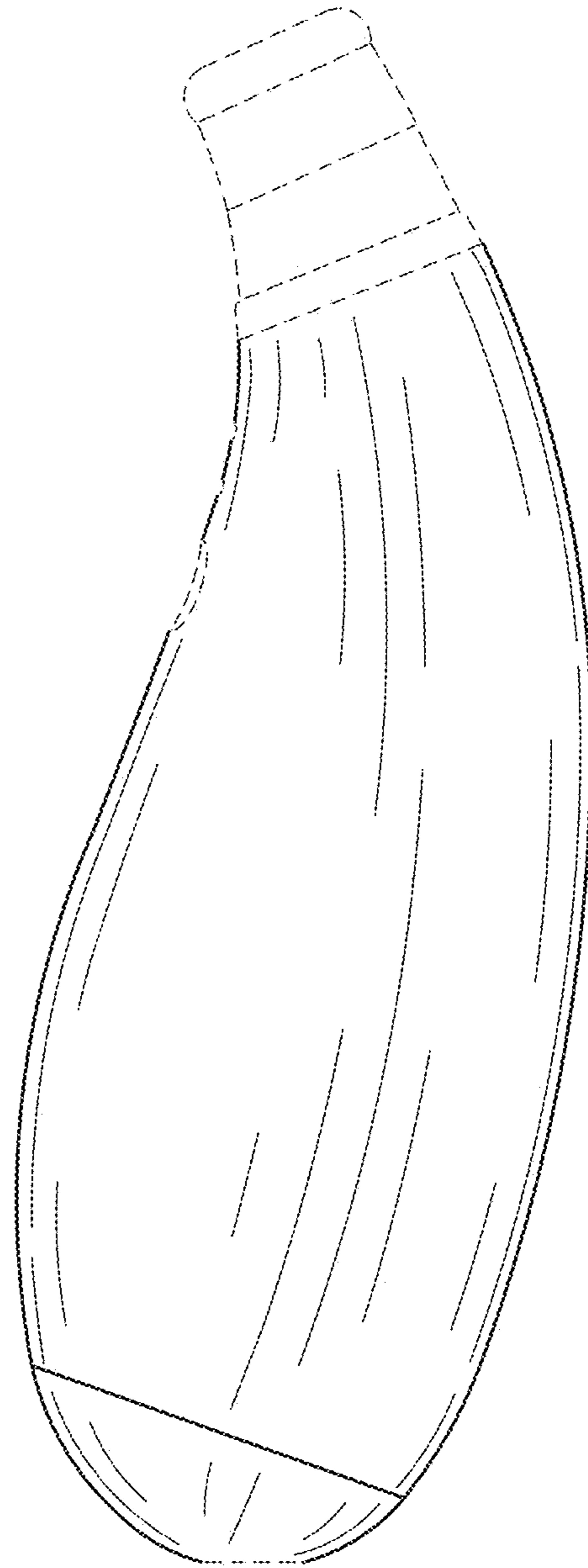


FIG. 7