



US00D943563S

(12) **United States Design Patent** (10) **Patent No.:** **US D943,563 S**
Williamson et al. (45) **Date of Patent:** **** Feb. 15, 2022**

(54) **SINGLE DUAL PORT NETWORK DEVICE**

(71) Applicant: **Hill Phoenix, Inc.**, Conyers, GA (US)

(72) Inventors: **Donald E. Williamson**, Conyers, GA (US); **Michael Hines**, Wayzata, MN (US)

(73) Assignee: **Hill Phoenix, Inc.**, Conyers, GA (US)

(**) Term: **15 Years**

(21) Appl. No.: **29/782,913**

(22) Filed: **May 10, 2021**

(51) **LOC (13) Cl.** **14-03**

(52) **U.S. Cl.**
USPC **D14/240**

(58) **Field of Classification Search**
USPC D14/240, 242, 357, 358, 140-140.9, 155, D14/125, 137, 139, 243, 348, 349, 351, D14/354, 355
CPC H04L 12/00; H03K 17/00; H04W 88/00; H04W 88/005; H04W 88/02; H04W 88/08; H04W 88/085; H04W 88/10; H04W 88/12; H04W 88/14; H04B 1/38
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D366,878 S *	2/1996	Wakabayashi	D13/147
D431,485 S *	10/2000	Huang	D14/357
D460,452 S *	7/2002	Yoshie	D14/358
D463,411 S *	9/2002	Stokes	D14/240
D464,339 S *	10/2002	Tomino	D14/240
D471,541 S *	3/2003	Tomino	D14/240

(Continued)

FOREIGN PATENT DOCUMENTS

CN	302949316	*	9/2014
CN	303159737	*	4/2015

(Continued)

OTHER PUBLICATIONS

POE Extender, Date First Available: Jun. 27, 2018, [online], [retrieved on Aug. 31, 2021]. Retrieved from Internet ,<URL: https://www.amazon.com/dp/B07F2RL2BQ/ref=sspa_dk_detail_4?psc=1&pd_rd_i=B07F2RL2BQ&pd_rd_ii=B07F2RL2BQ&pd_rd_w=POXmL&pf_rd_p=5d846283-ed3e-4512-a744-a30f97c5d738&pd_rd_wg=sG055&pf_rd_r=JMWS6MWCNJ0M4D25NIK3&pd_rd_r=cb06e523-e2c9-4813-9b8af3731f2e4b35&smid=A2C232BUL391Z5&spLa=ZW5jenlwdGVkUXVhbGlmaWVyPUEzOUdUN0FXVU9NOUJVJmVuY3J5cHRIZEikPUEwNjg1NDI4M09WQjIeRzBNOFZaUSZlbnNyeXB0ZWRBZEikPUEwMDA2NTAxMkJKOURNNkZWWIRUVyZ3aWRnZXROYW1lPXNwX2RldGFpbF90aGVfYXRpYyZhY3Rpb249Y2xpY2tSZWRpcmVjdCZkb05vdExvZ0NsaWNrPXRYdWU=>.*

(Continued)

Primary Examiner — Bridget L Eland

(74) *Attorney, Agent, or Firm* — Fish & Richardson P.C.

(57) **CLAIM**

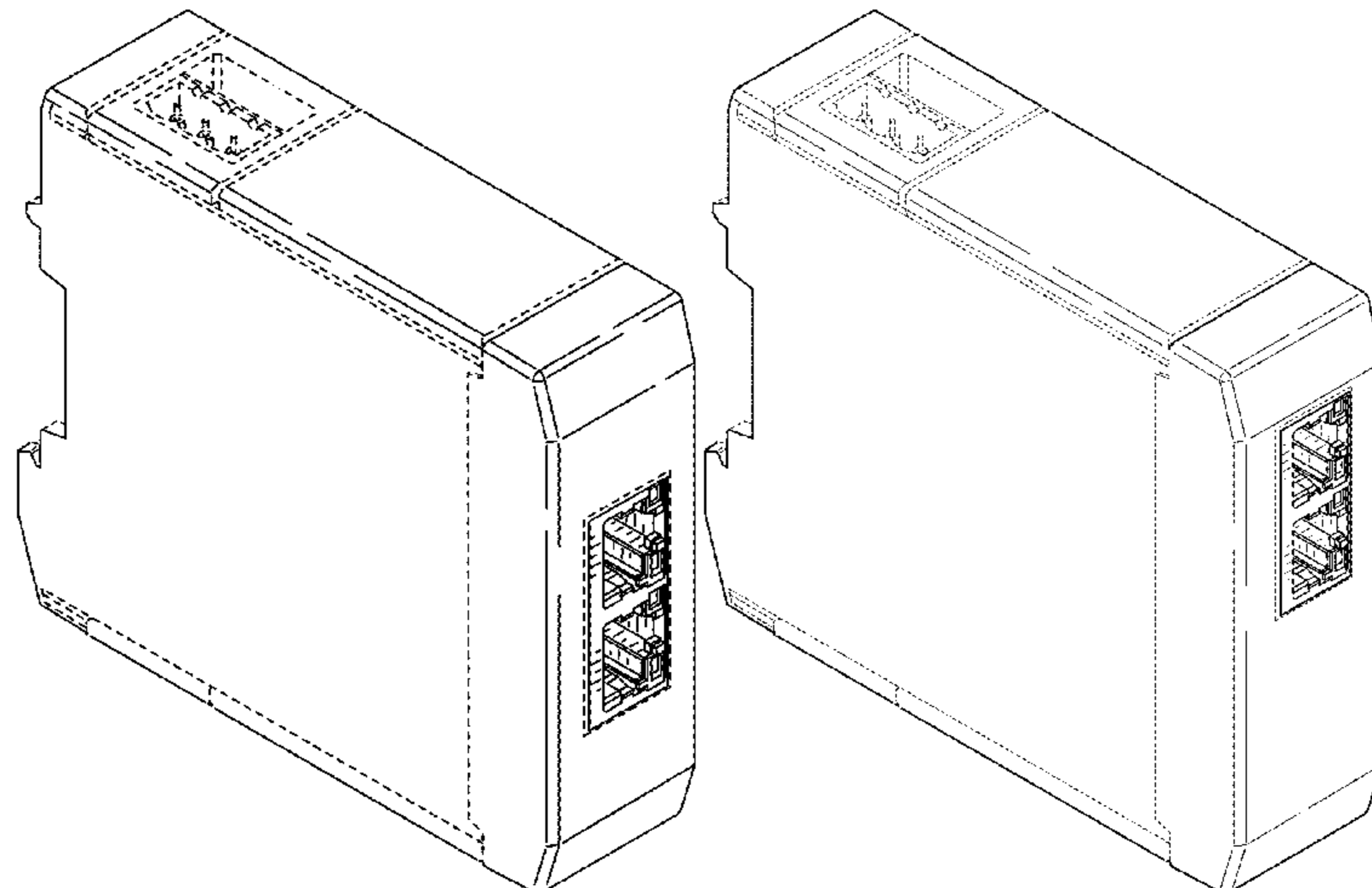
The ornamental design for an single dual port network device, substantially as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a first embodiment of a single dual port network device, showing our new design; FIG. 2 is a top view thereof; FIG. 3 is a bottom view thereof; FIG. 4 is a right side view thereof; FIG. 5 is a left side view thereof; FIG. 6 is a rear view thereof; and FIG. 7 is a front view thereof; FIG. 8 is a perspective view of a second embodiment of a single dual port network device showing our new design; FIG. 9 is a top view thereof; FIG. 10 is a perspective view of a third embodiment of a single dual port network device, showing our new design; and, FIG. 11 is a top view thereof.

The broken lines depict portions of the single dual port network device that form no part of the claimed design of the emobodiments, in which they are shown. The bottom, right

(Continued)



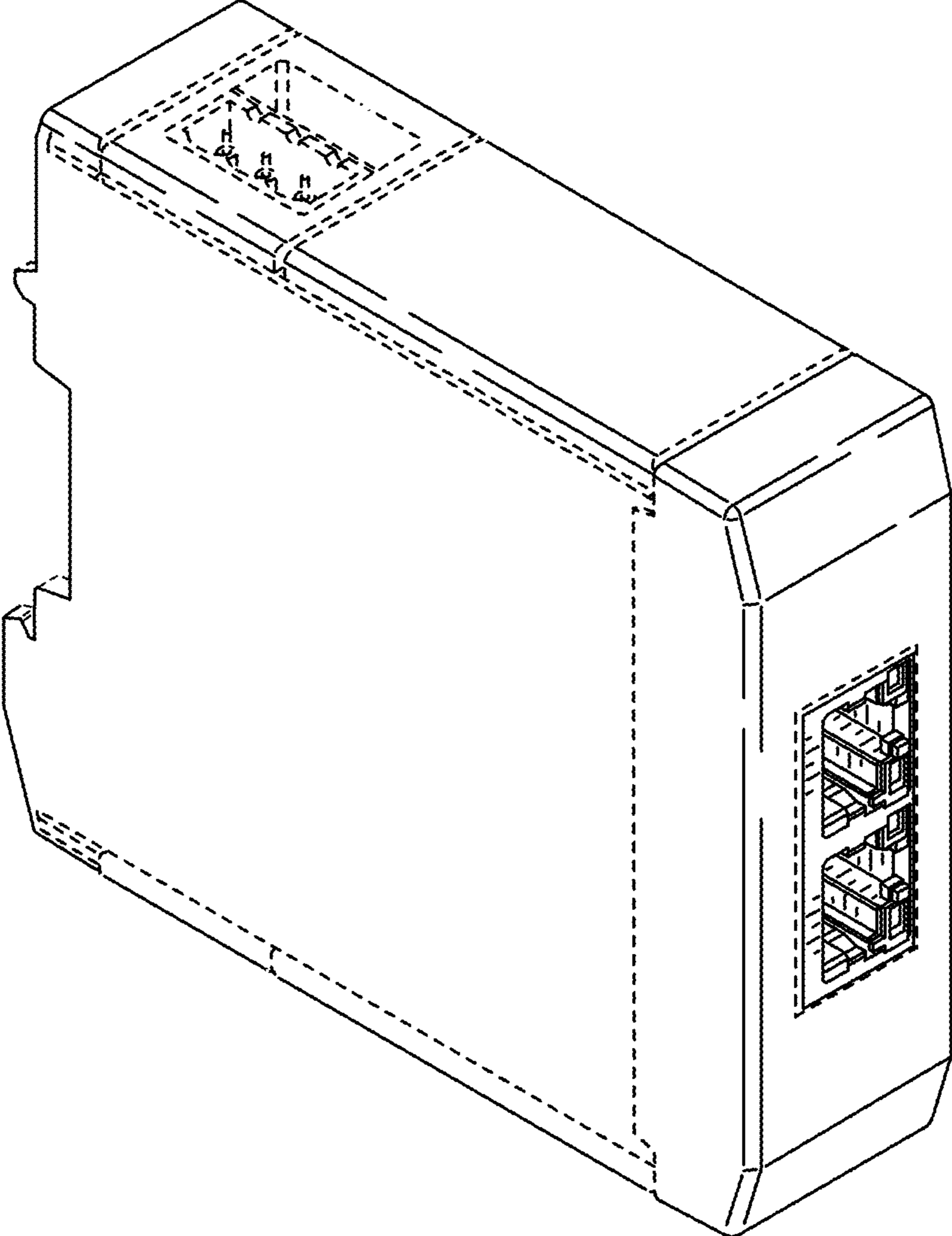


FIG. 1

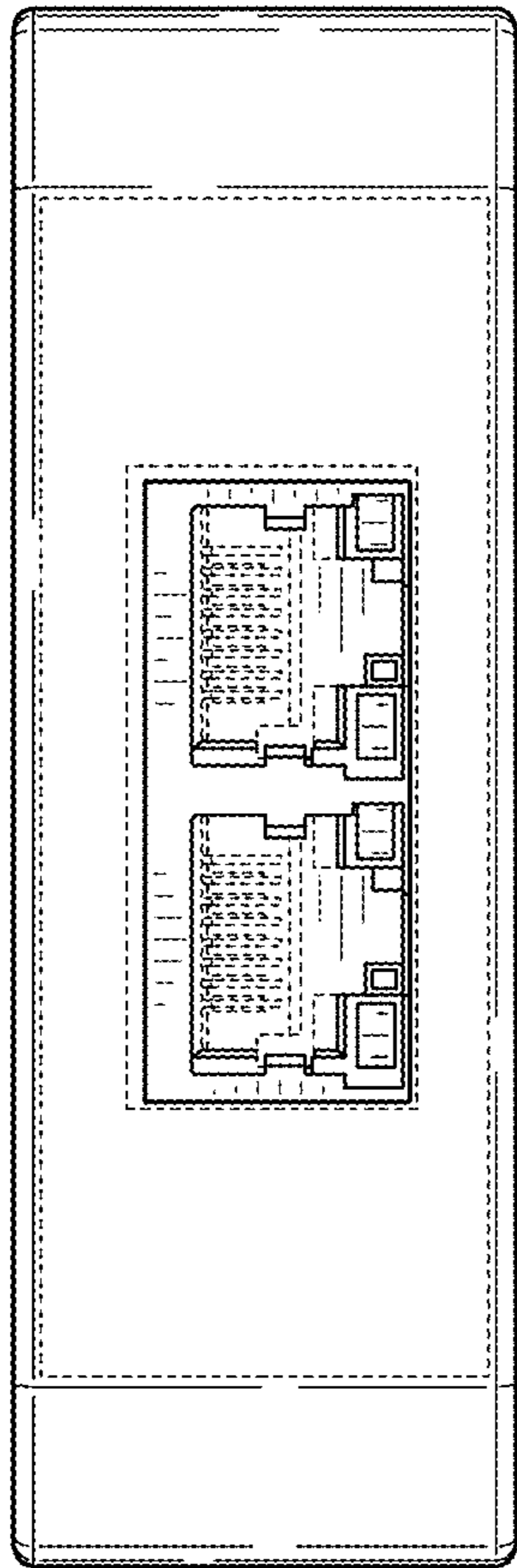


FIG. 2

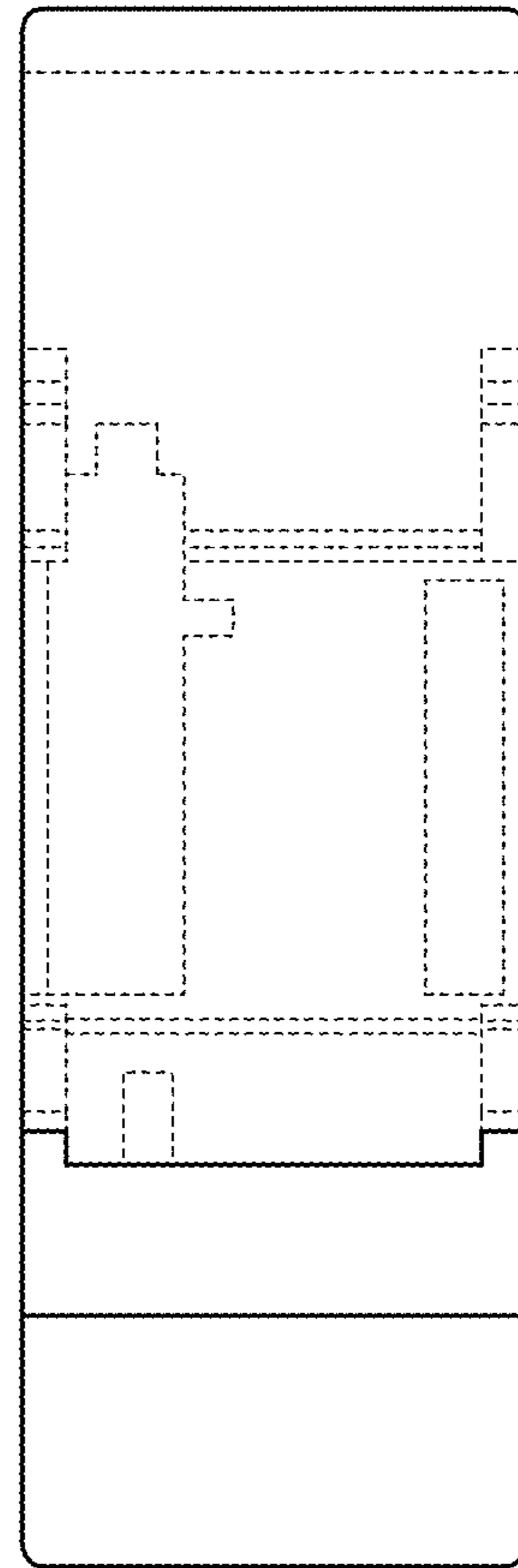


FIG. 3

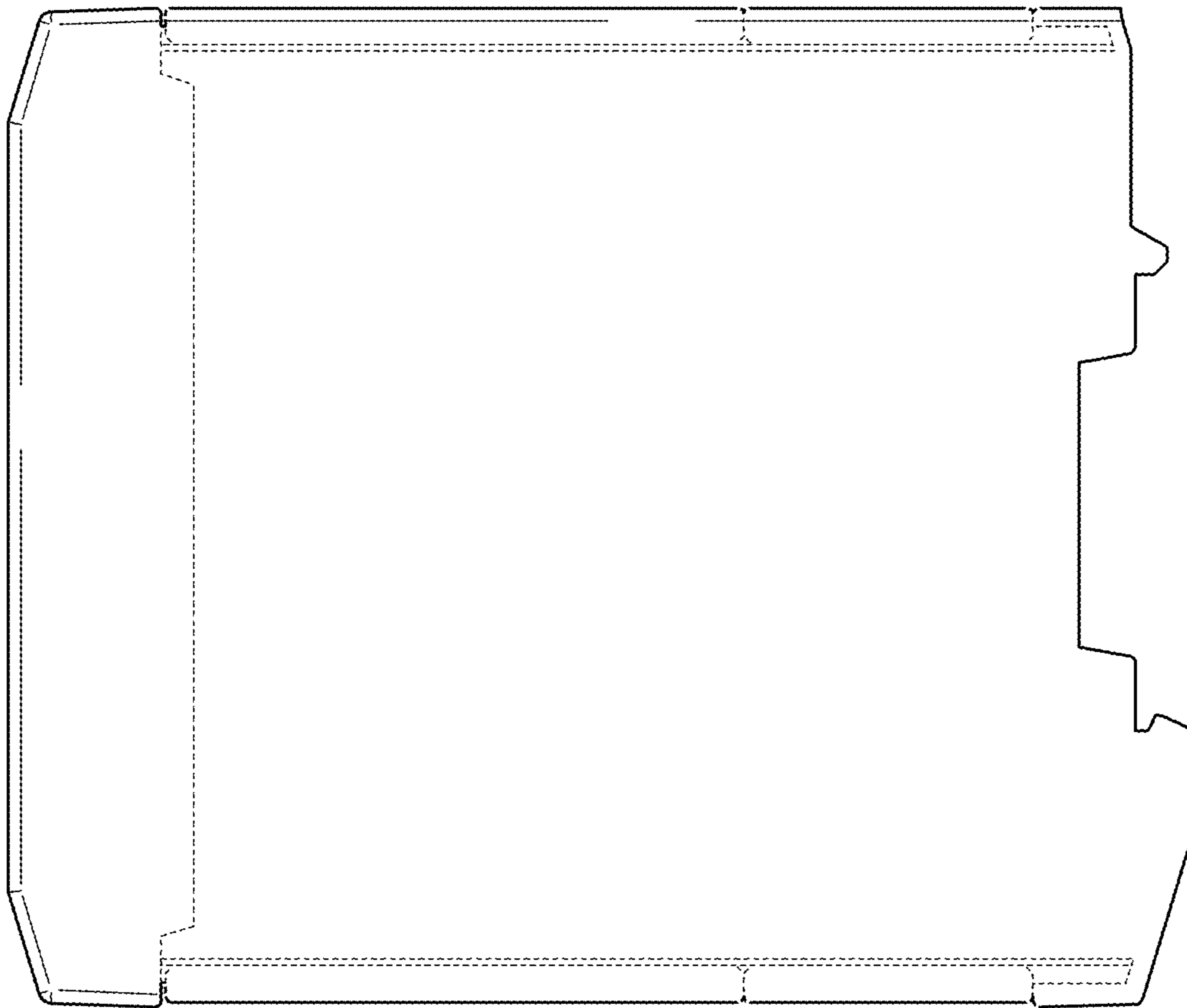


FIG. 4

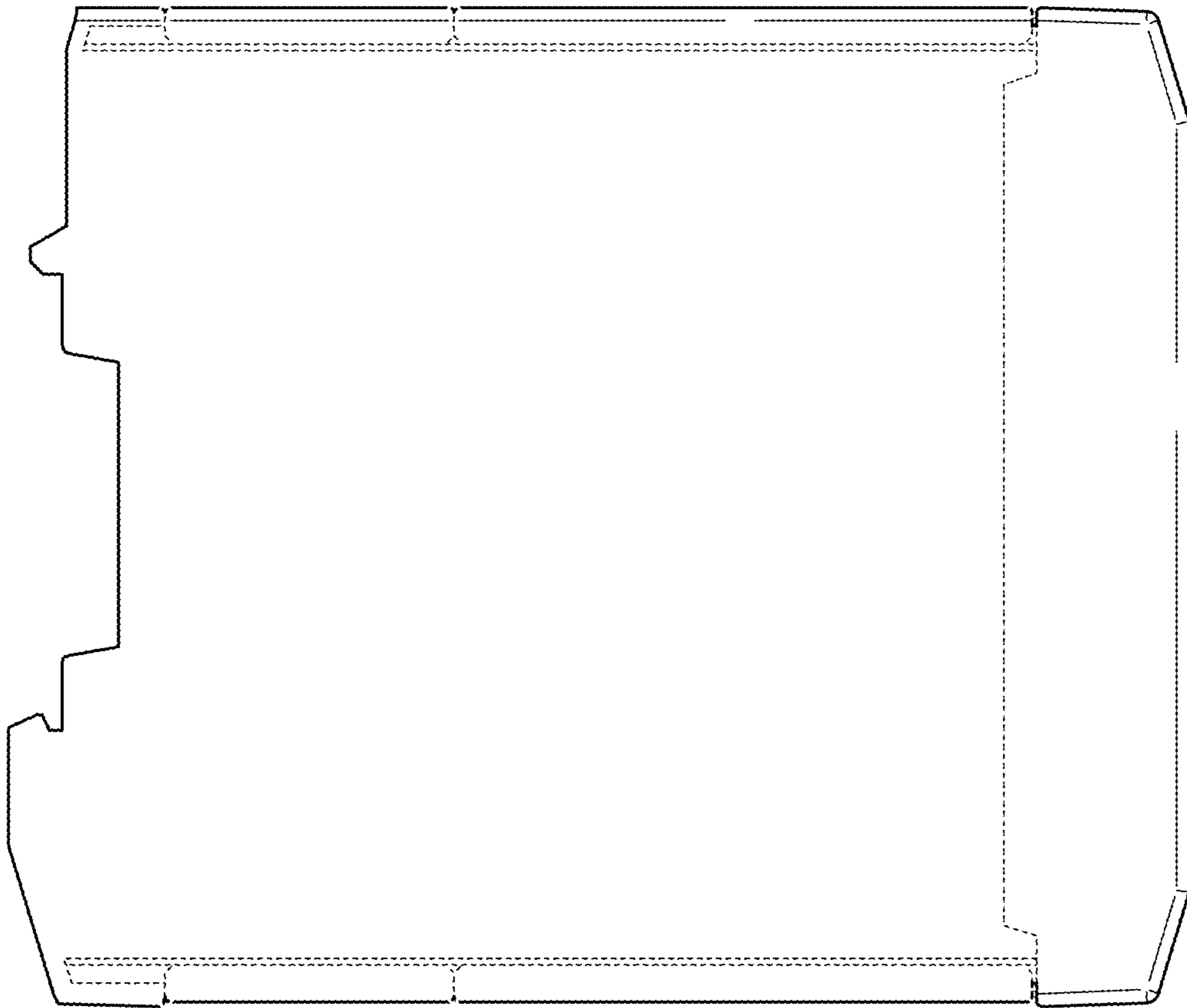


FIG. 5

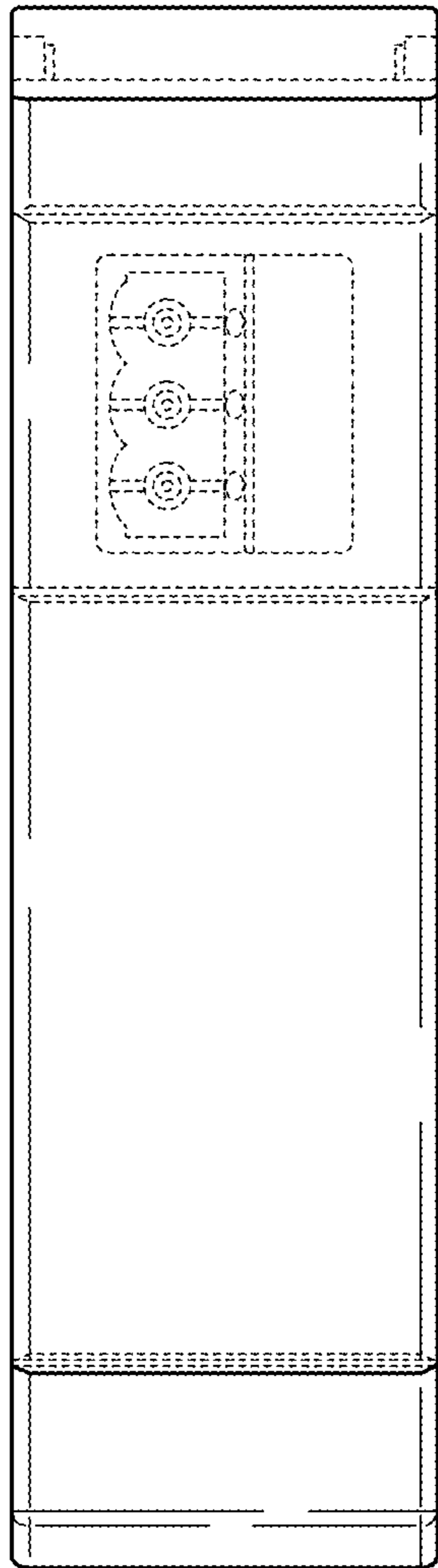


FIG. 6

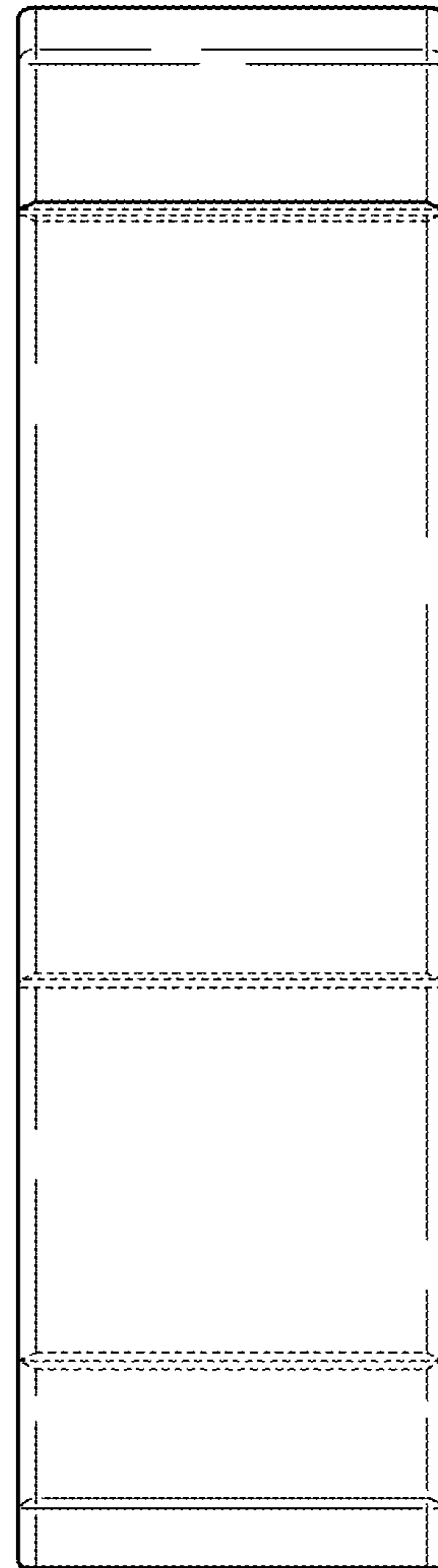


FIG. 7

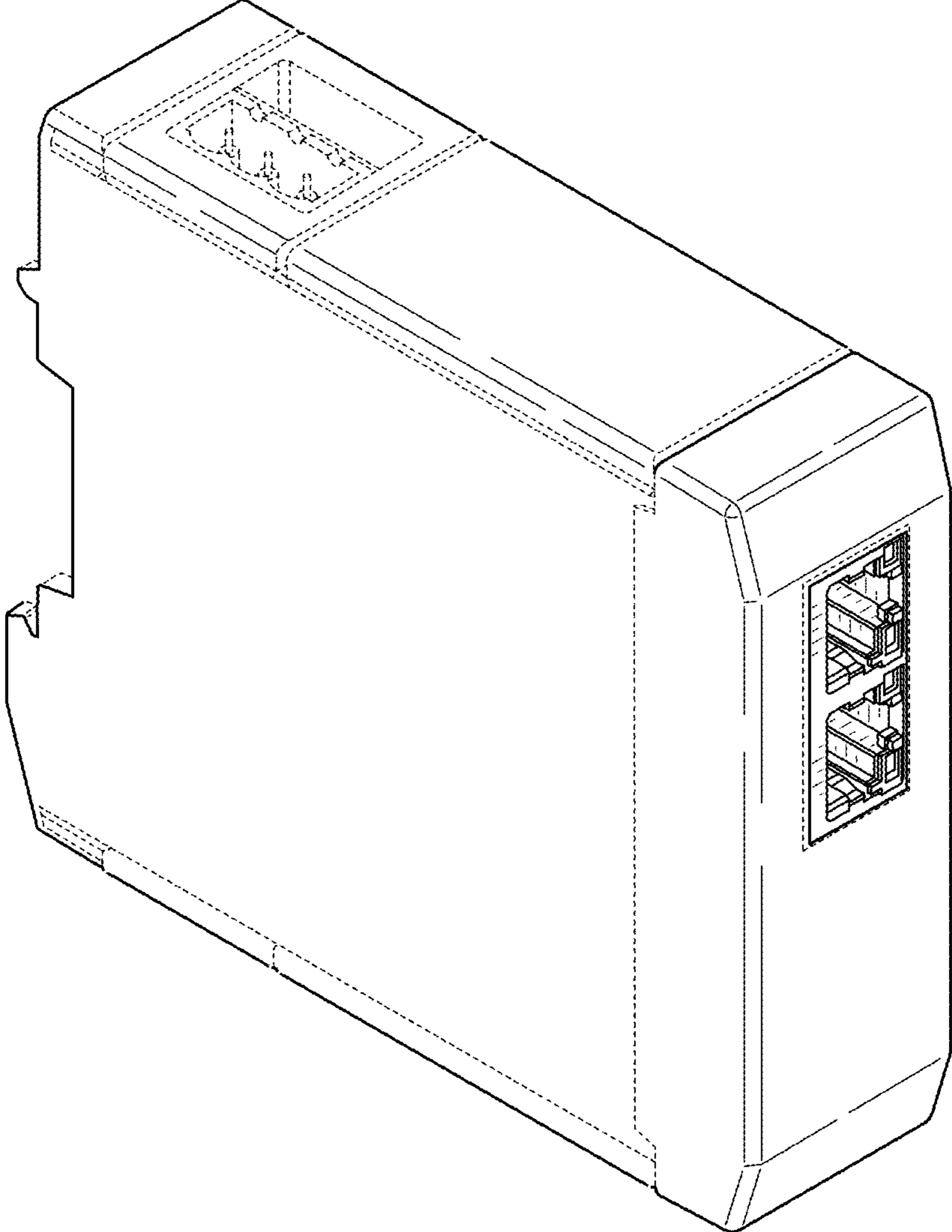


FIG. 8

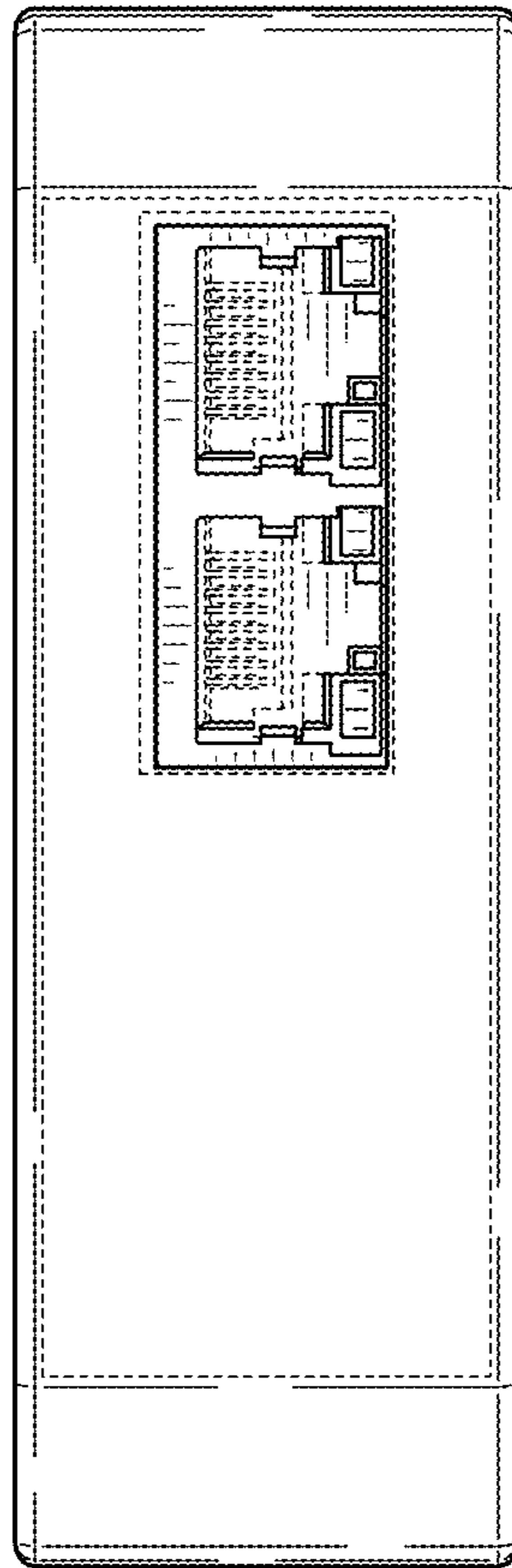


FIG. 9

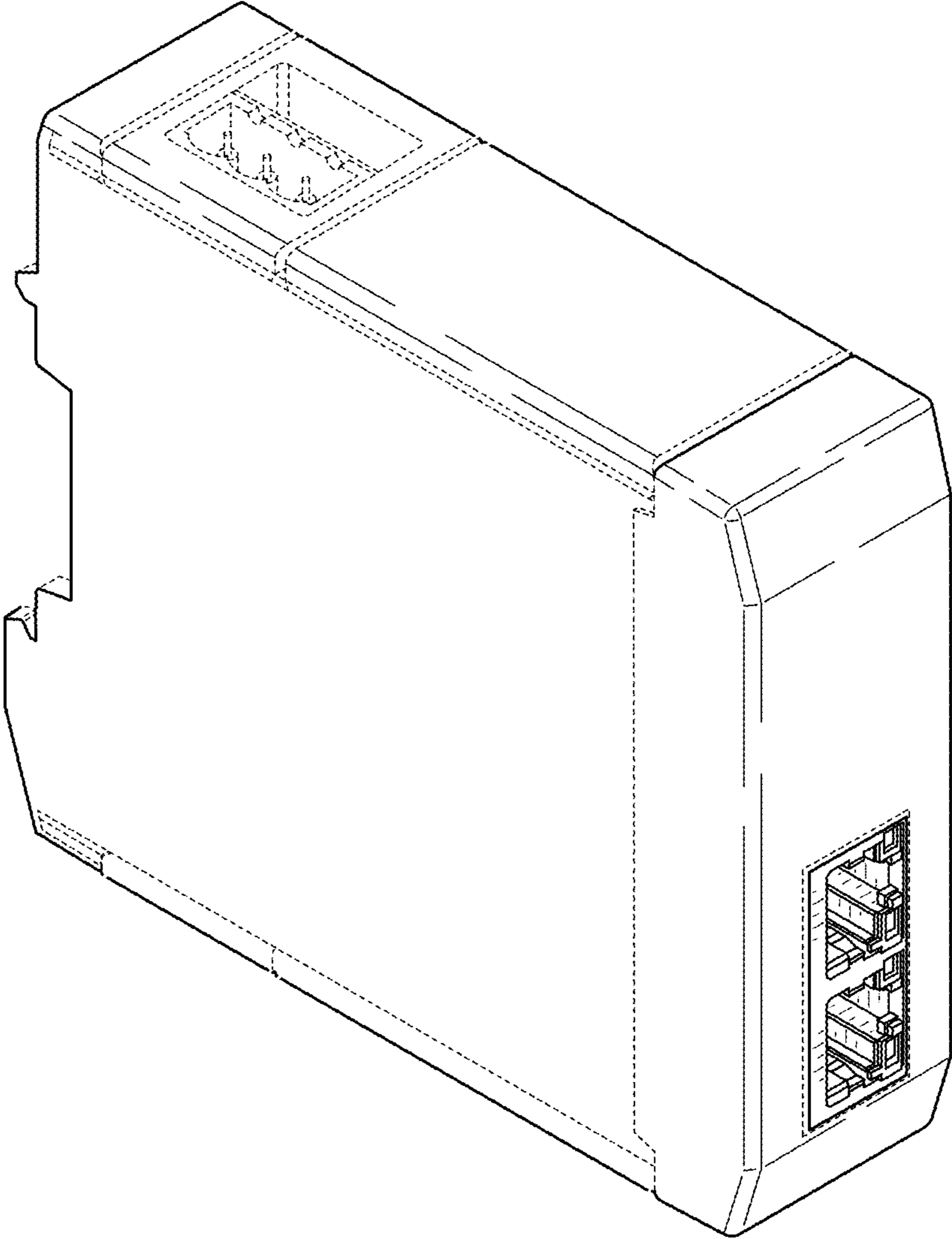


FIG. 10

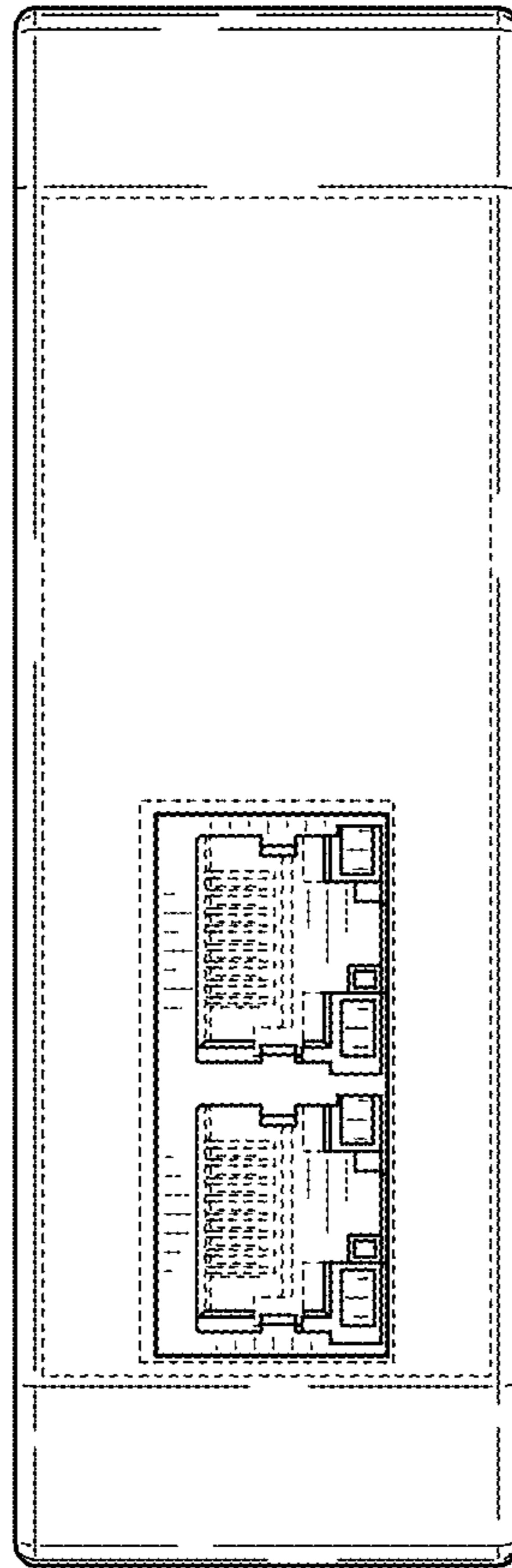


FIG. 11