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(12) **United States Design Patent** (10) **Patent No.:** **US D941,490 S**
Thompson, II et al. (45) **Date of Patent:** **** *Jan. 18, 2022**

(54) **MICROFLUIDIC CHIP WITH MICROCHANNELS**

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- (*) Notice: This patent is subject to a terminal disclaimer.
- (**) Term: **15 Years**
- (21) Appl. No.: **29/736,067**
- (22) Filed: **May 27, 2020**

Related U.S. Application Data

- (63) Continuation of application No. 29/675,951, filed on Jan. 7, 2019, now Pat. No. Des. 888,273, which is a continuation of application No. 29/576,811, filed on Sep. 7, 2016, now Pat. No. Des. 842,493.
- (51) **LOC (13) Cl.** **24-02**
- (52) **U.S. Cl.**
USPC **D24/224**
- (58) **Field of Classification Search**
USPC D13/180, 182, 184, 199; D24/216, 224, D24/225, 226, 227, 229, 230, 231, 232
CPC B01L 3/5027; B01L 3/502715; B01L 3/502723; B01L 3/50273; B01L 3/502738; B01L 3/502761; B01L 3/502784; C12M 23/16
See application file for complete search history.

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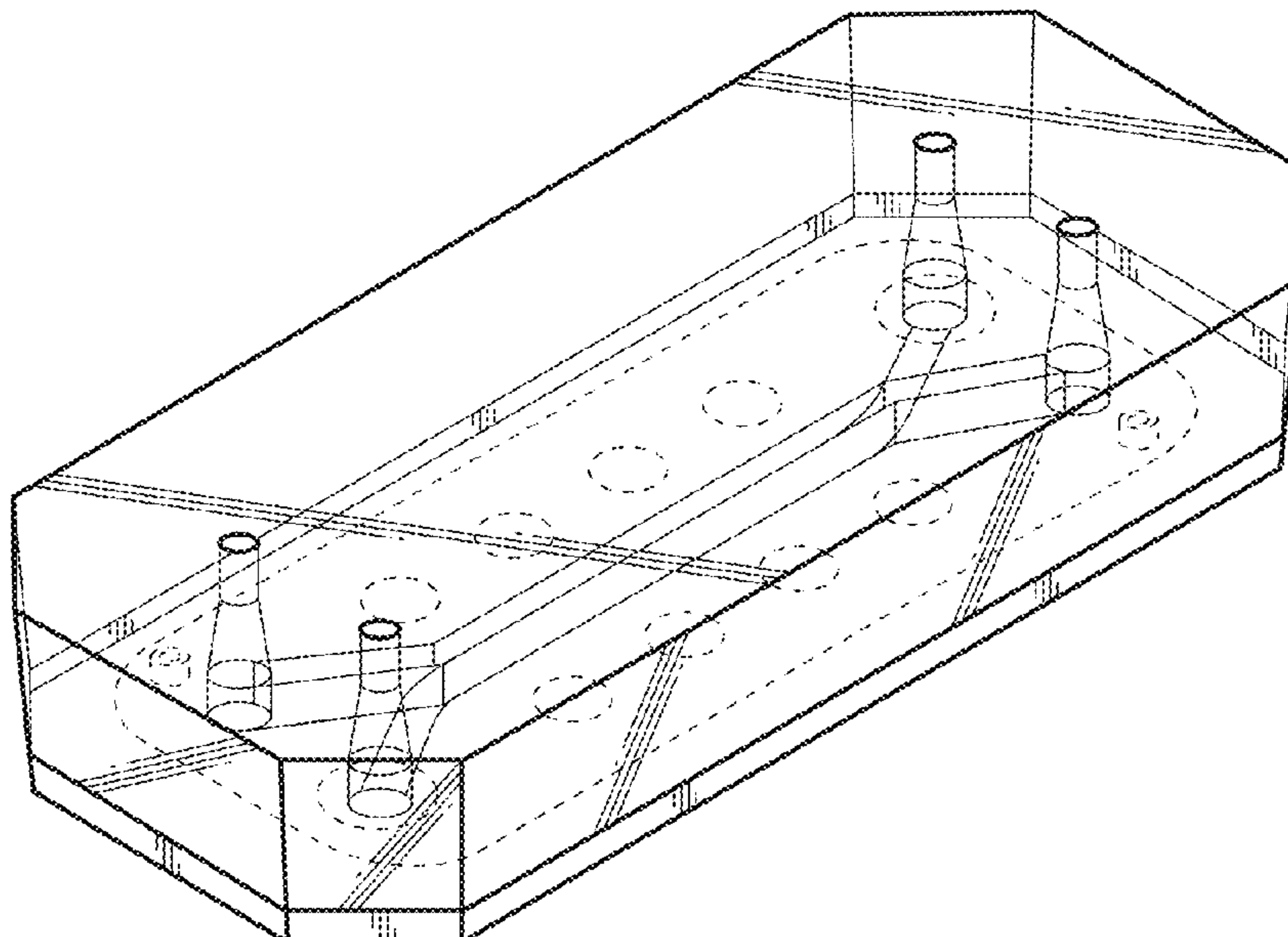
(57) **CLAIM**

The ornamental design for a microfluidic chip with microchannels, as shown and described.

DESCRIPTION

FIG. 1 is a top plan view of an ornamental microfluidic chip with microchannels; and, FIG. 2 is a top perspective view, thereof. The broken lines indicate features that do not form part of the claimed design.

1 Claim, 2 Drawing Sheets



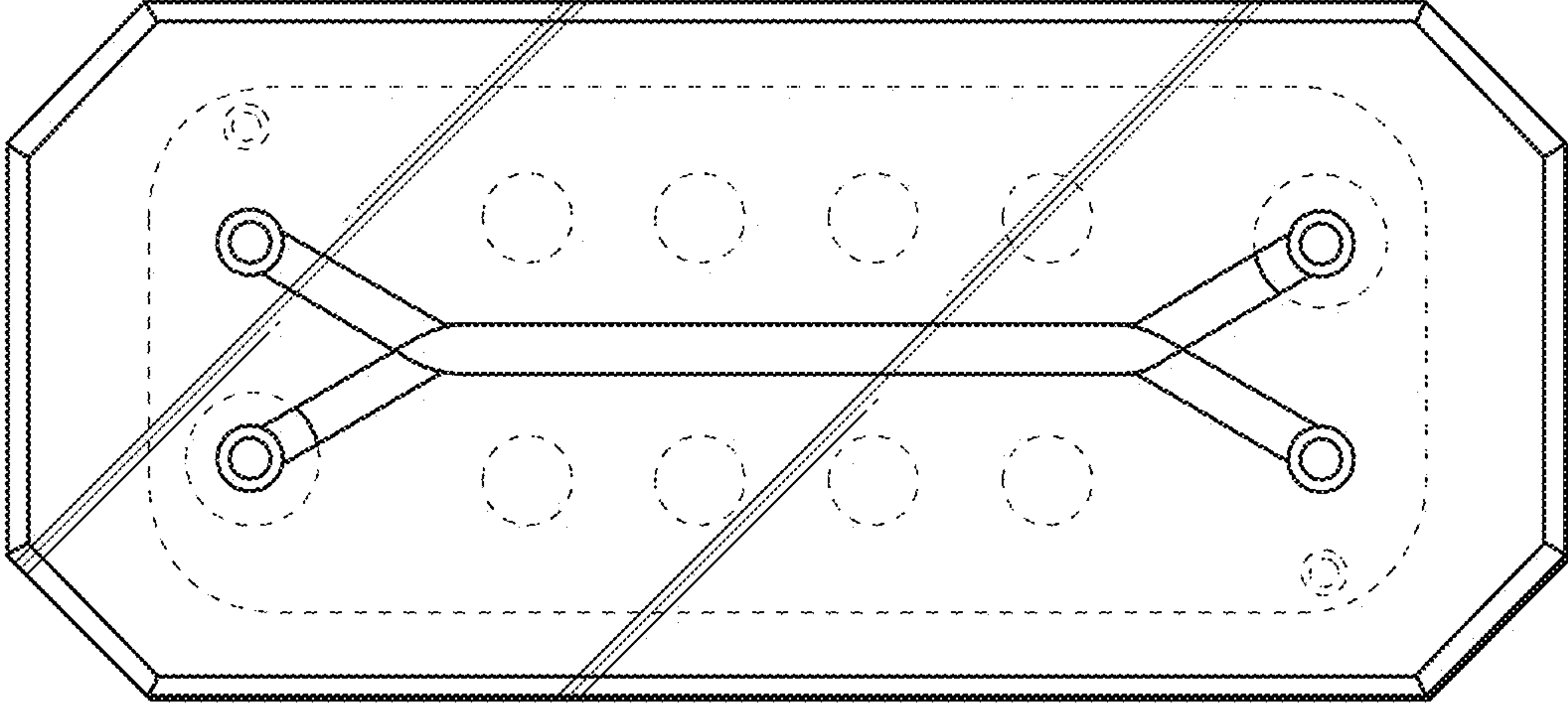


FIG. 1

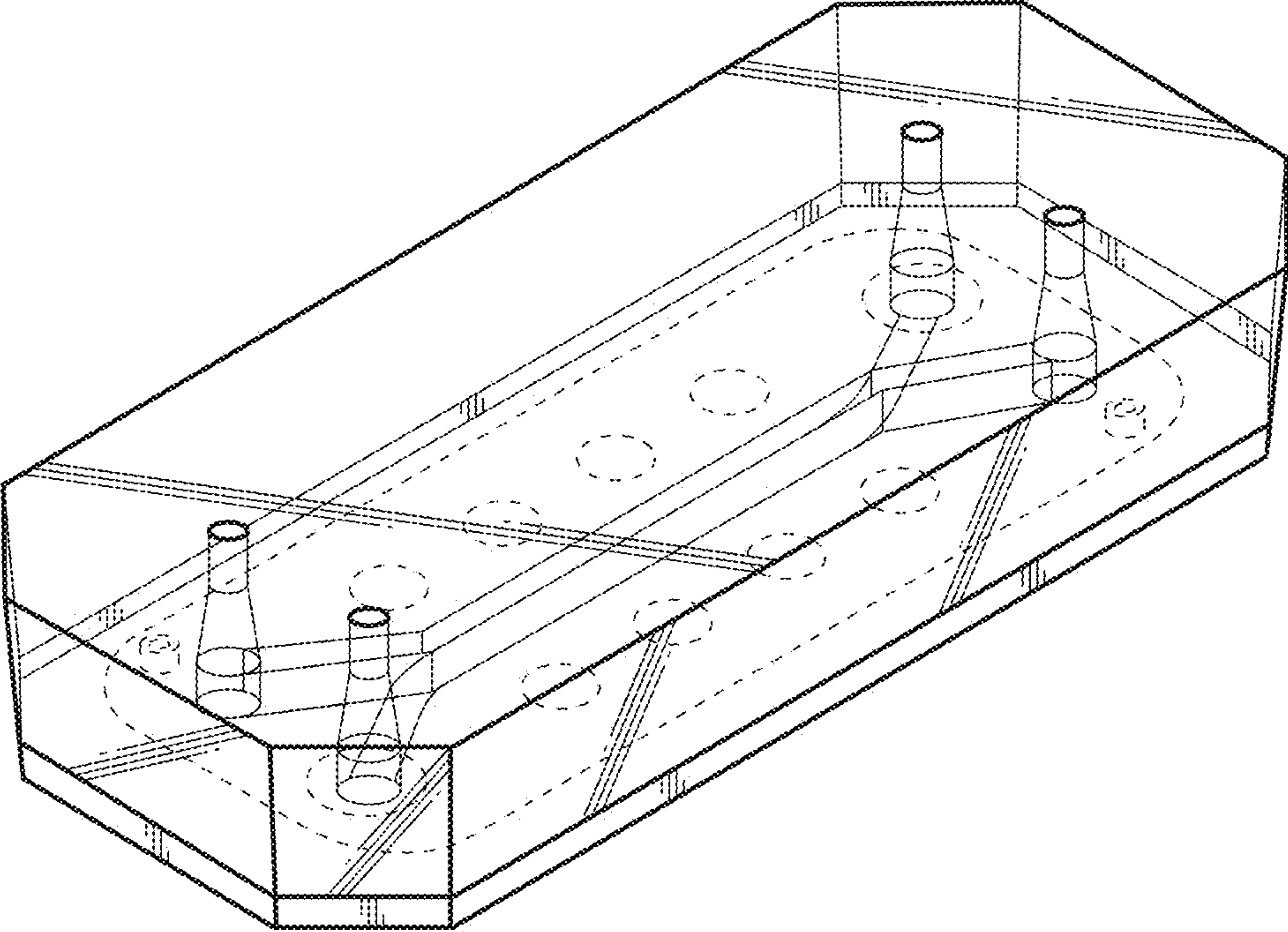


FIG. 2