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(12) **United States Design Patent** (10) **Patent No.:** **US D938,374 S**
McBride et al. (45) **Date of Patent:** **** Dec. 14, 2021**

(54) **CIRCUIT BOARD**

(56) **References Cited**

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(72) Inventors: **James P. McBride**, Phoenix, AZ (US);
Matthew Michael Bosway, Gilbert, AZ (US)

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(**) Term: **15 Years**

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Related U.S. Application Data

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(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/182; 361/748, 752, 760, 761, 807, 361/808, 736, 719, 720; 174/250, 253, 174/255, 68.1; 363/144, 147
CPC H05K 3/30; H05K 3/301; H05K 3/284; H05K 3/303; H05K 1/18; H05K 1/181; H05K 1/182; H05K 1/183; H05K 1/185; H05K 1/00; H05K 7/1417; H05K 7/02; H05K 7/12; H05K 7/06; H01L 2924/14

See application file for complete search history.

(Continued)

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(57) **CLAIM**

The ornamental design for a circuit board, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a circuit board, showing our new design;

FIG. 2 is a front elevational view thereof;

FIG. 3 is a right side elevational view thereof;

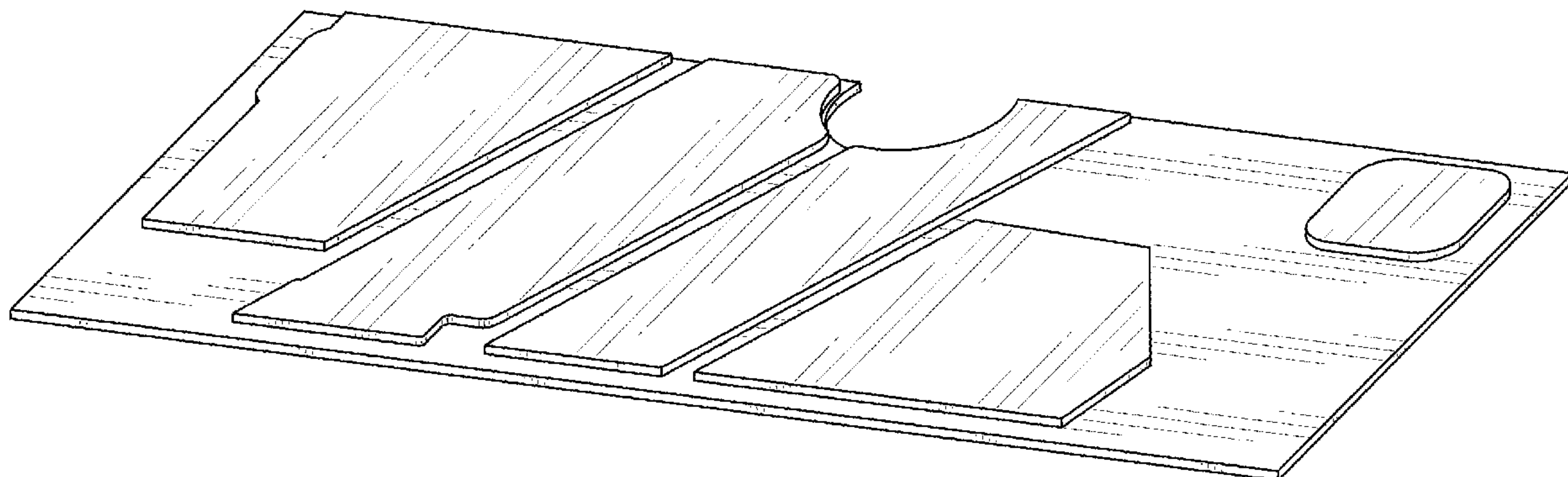
FIG. 4 is a left side elevational view thereof;

FIG. 5 is a top plan view thereof; and,

FIG. 6 is a bottom plan view thereof.

All surfaces not shown form no part of the claimed design.

1 Claim, 6 Drawing Sheets



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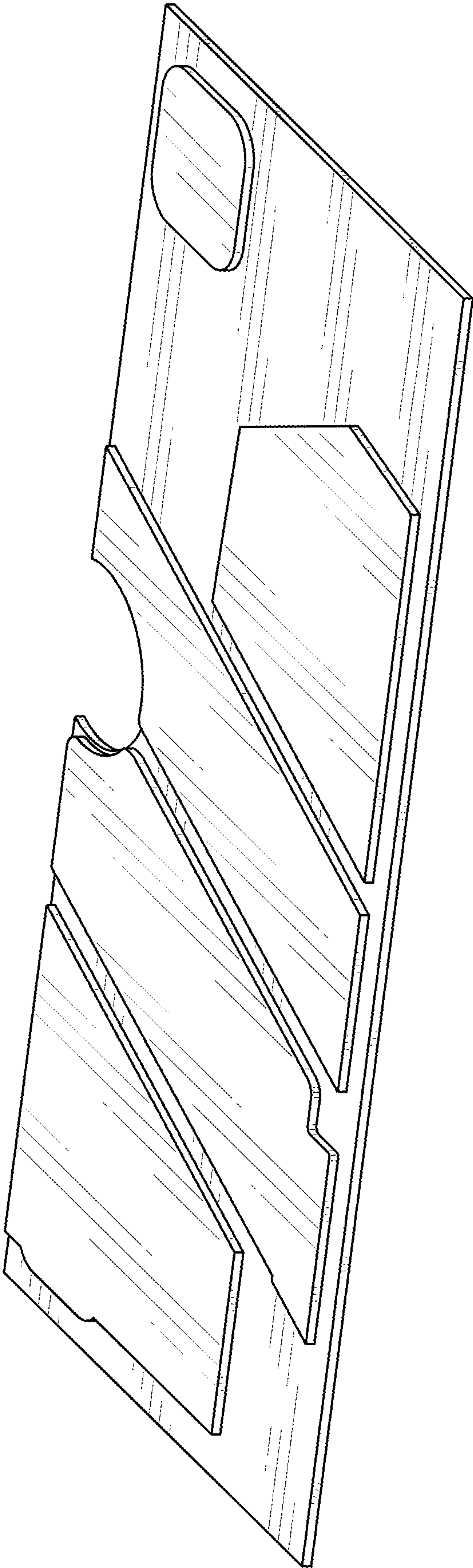


FIG. 1

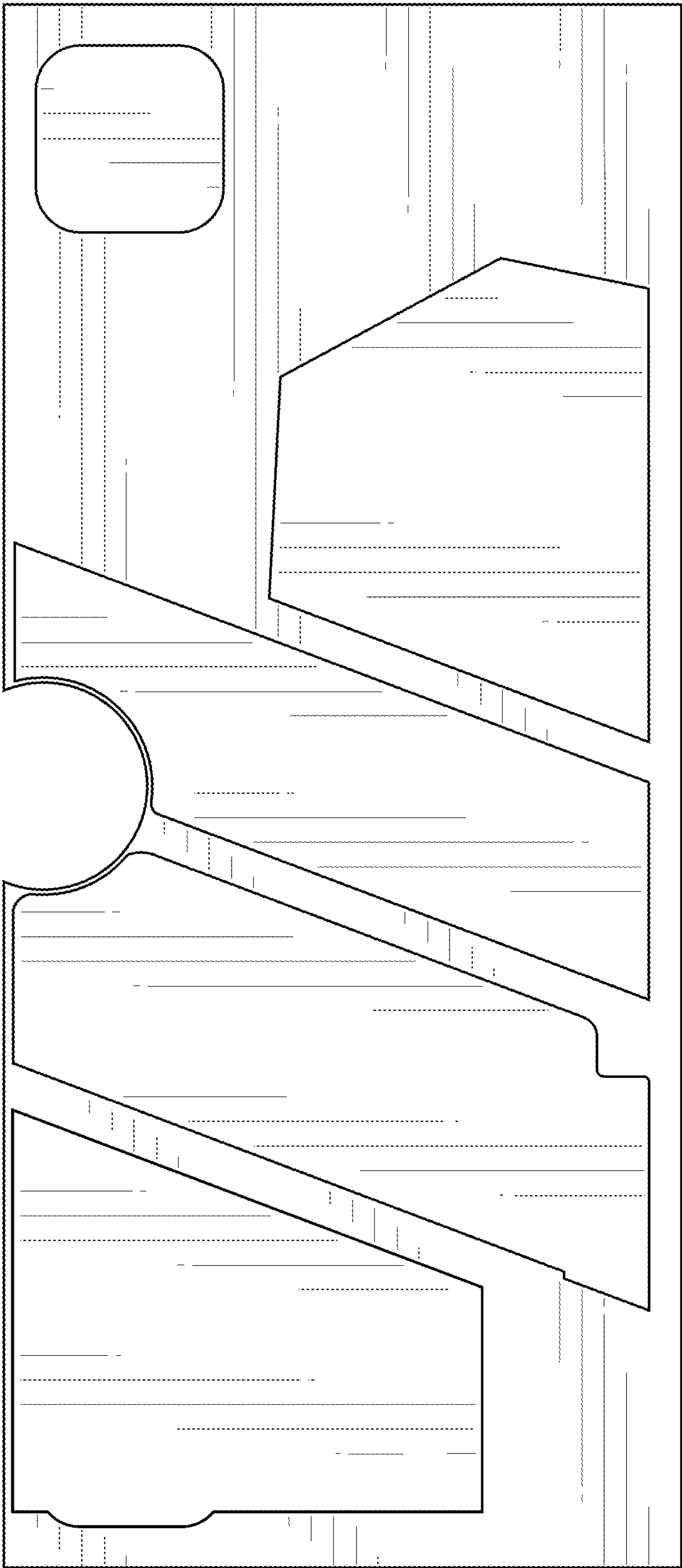


FIG. 2

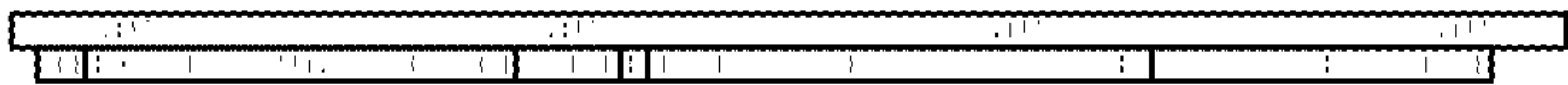


FIG. 3

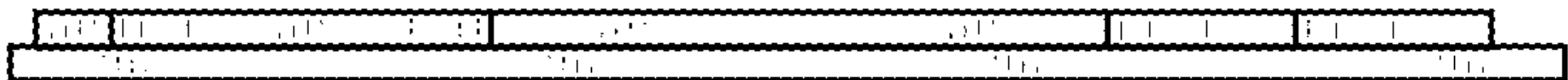


FIG. 4



FIG. 5



FIG. 6