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(12) **United States Design Patent**  
**DeMaio**

(10) **Patent No.:** **US D937,256 S**

(45) **Date of Patent:** **\*\* Nov. 30, 2021**

(54) **CONNECTABLE PRIMARY COMPUTING DEVICE**

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(\*\*) Term: **15 Years**

(21) Appl. No.: **29/788,670**

(22) Filed: **Jul. 13, 2021**

**Related U.S. Application Data**

(60) Continuation-in-part of application No. 29/769,530, filed on Feb. 5, 2021, now Pat. No. Des. 928,778, which is a division of application No. 29/706,209, filed on Sep. 18, 2019, now Pat. No. Des. 918,898.

(51) **LOC (13) Cl.** ..... **14-02**

(52) **U.S. Cl.**  
USPC ..... **D14/341**

(58) **Field of Classification Search**  
USPC ..... D14/341, 315-318, 371, 374, 388-389, D14/138 G, 138 R, 138 AD, 138 C, D14/138 AA, 496, 203; D21/330, 324, D21/329  
CPC .... G06F 1/1652; G06F 1/1616; G06F 1/1643; G06F 1/1647; H04M 1/0268; H04M 1/0235; G09F 9/301; H01R 24/68  
See application file for complete search history.

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(57) **CLAIM**

I claim the ornamental design for connectable primary computing device, as shown and described.

**DESCRIPTION**

FIG. 1 is a front, bottom, right perspective view of a connectable primary computing device according to a first embodiment in which the connectors are in a contracted position, showing my new design;

FIG. 2 is a front elevation view thereof;

FIG. 3 is a rear elevation view thereof;

FIG. 4 is a left elevation view thereof;

FIG. 5 is a right elevation view thereof;

FIG. 6 is a top plan view thereof;

FIG. 7 is a bottom plan view thereof;

FIG. 8 is a front, bottom, right perspective view of the connectable primary computing device according to a second embodiment in which the connectors are in an extended position, showing my new design;

FIG. 9 is a front elevation view thereof;

FIG. 10 is a rear elevation view thereof;

FIG. 11 is a left elevation view thereof;

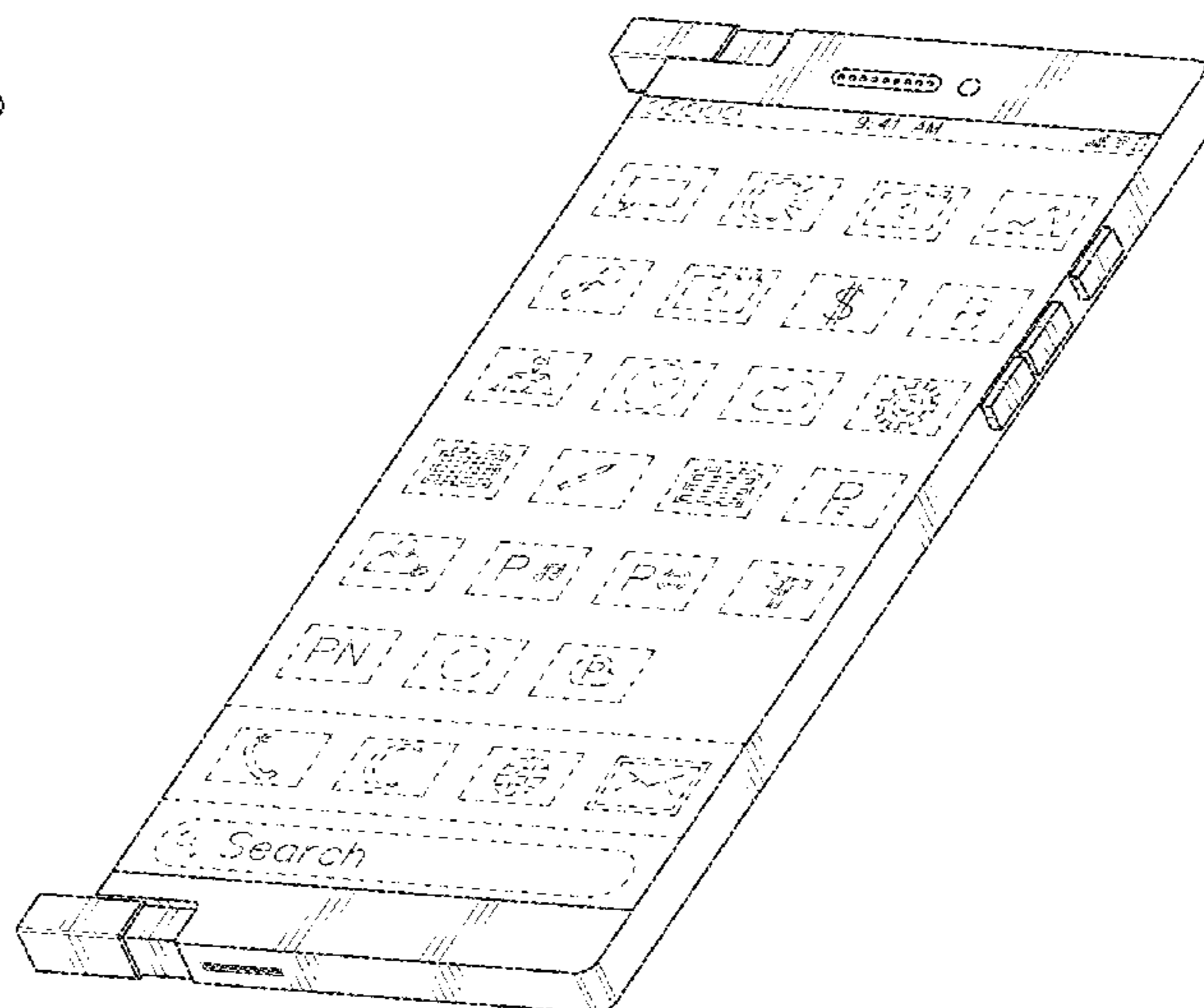
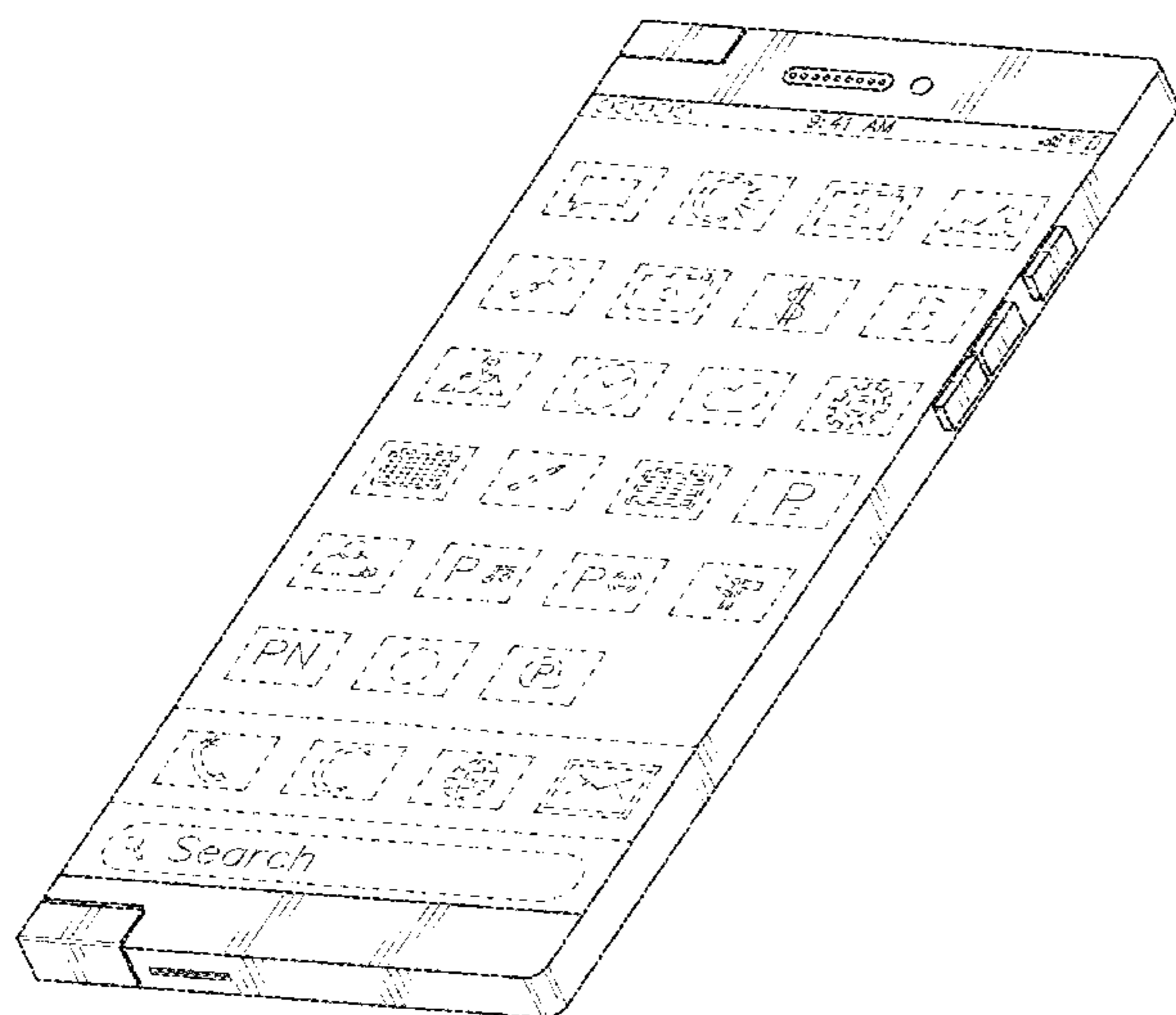
FIG. 12 is a right elevation view thereof;

FIG. 13 is a top plan view thereof; and,

FIG. 14 is a bottom plan view thereof.

The broken lines shown in FIGS. 3-7 and 10-14 illustrate portions of the Two-sided connectable primary computing device that form no part of the claimed design. The additional broken lines that show the graphical user interface shown in FIGS. 1-2 and 8-9 illustrate environment and form no part of the claimed design.

**1 Claim, 12 Drawing Sheets**



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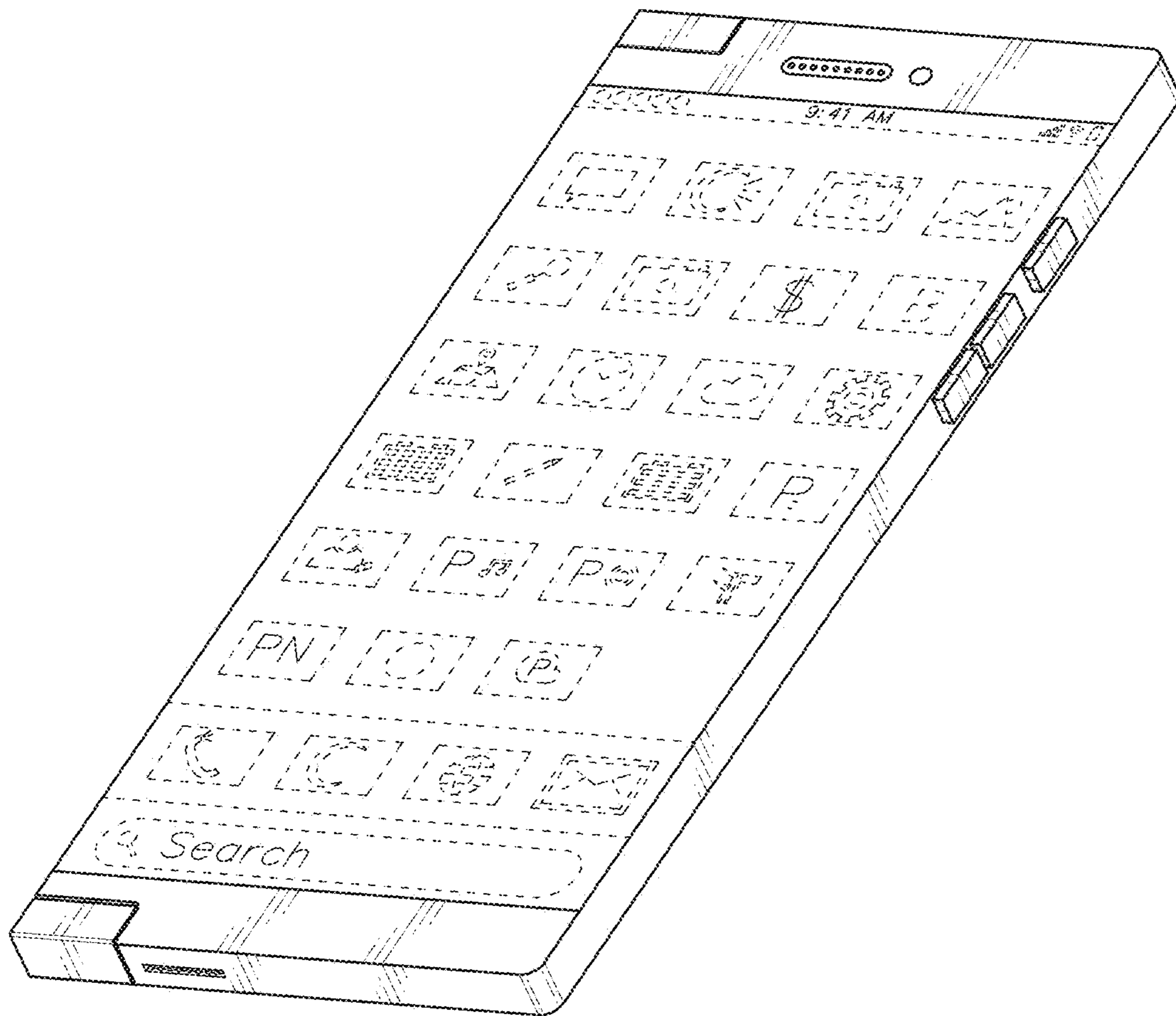


FIG. 1

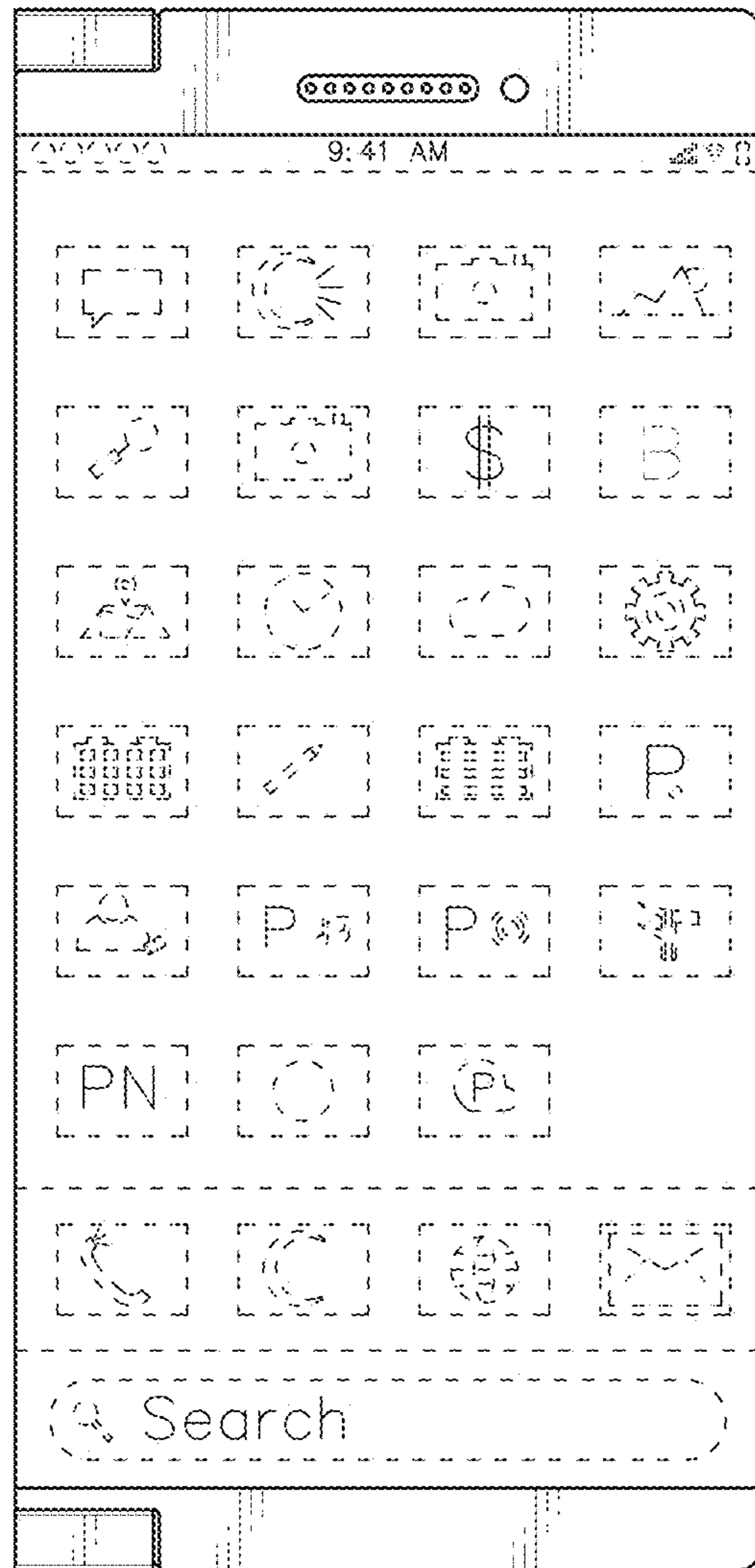


FIG. 2

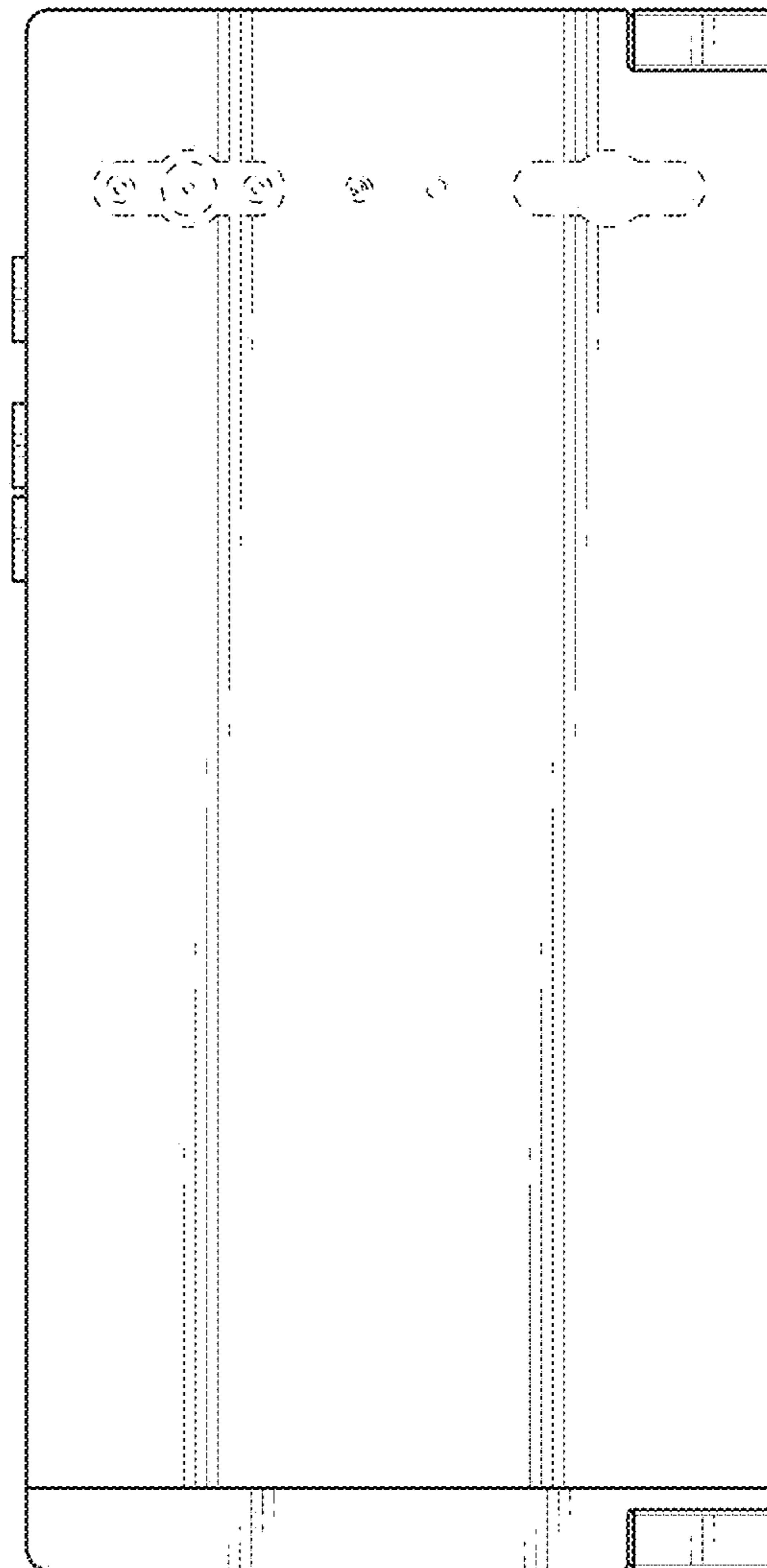


FIG. 3



FIG. 4

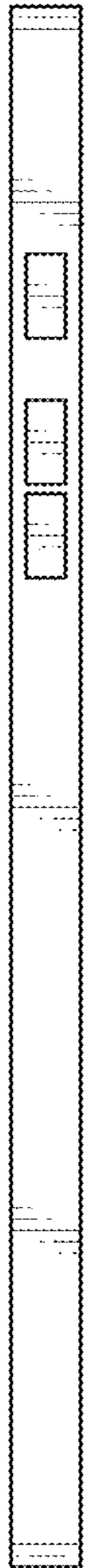


FIG. 5



FIG. 6



FIG. 7



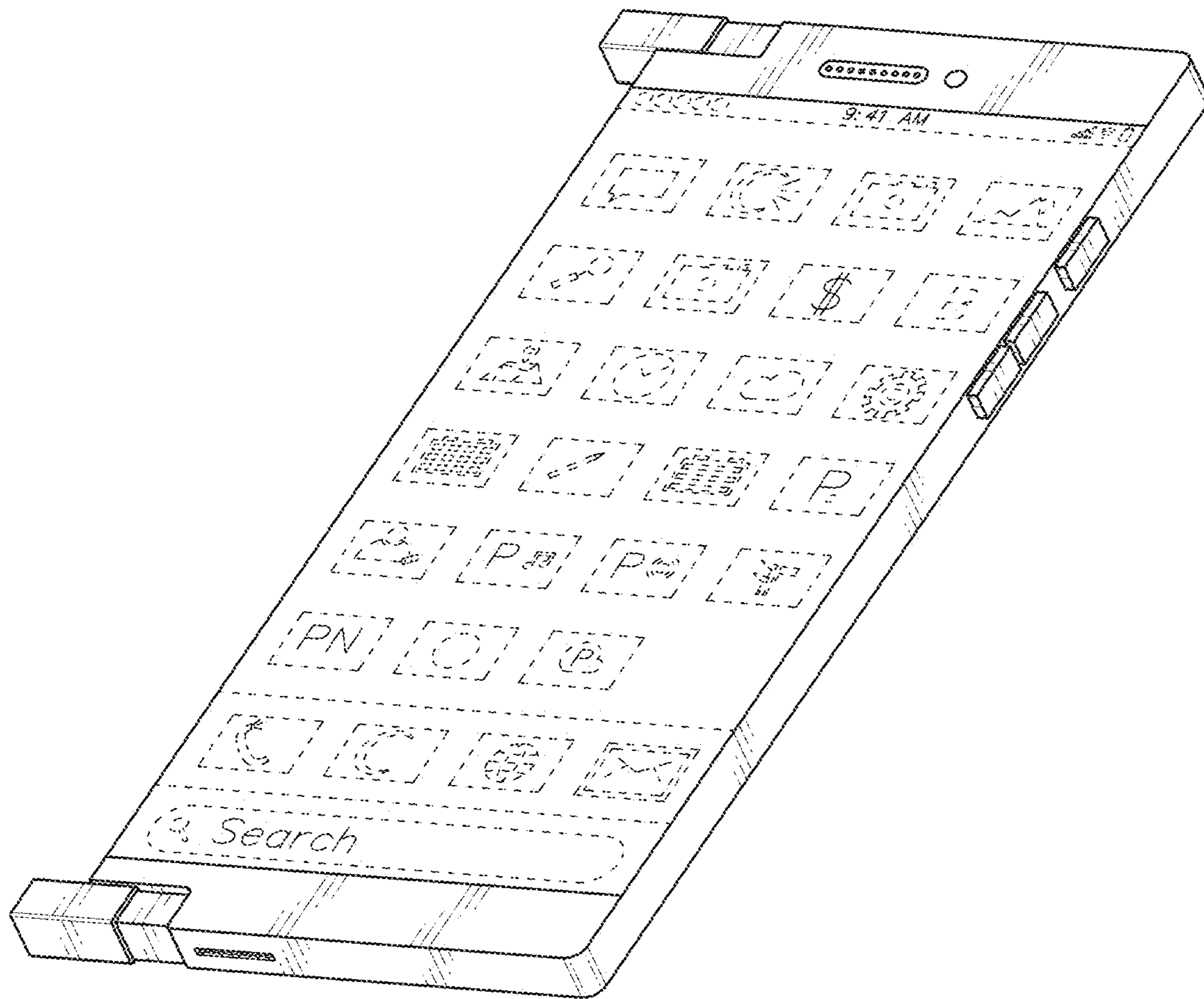


FIG. 8

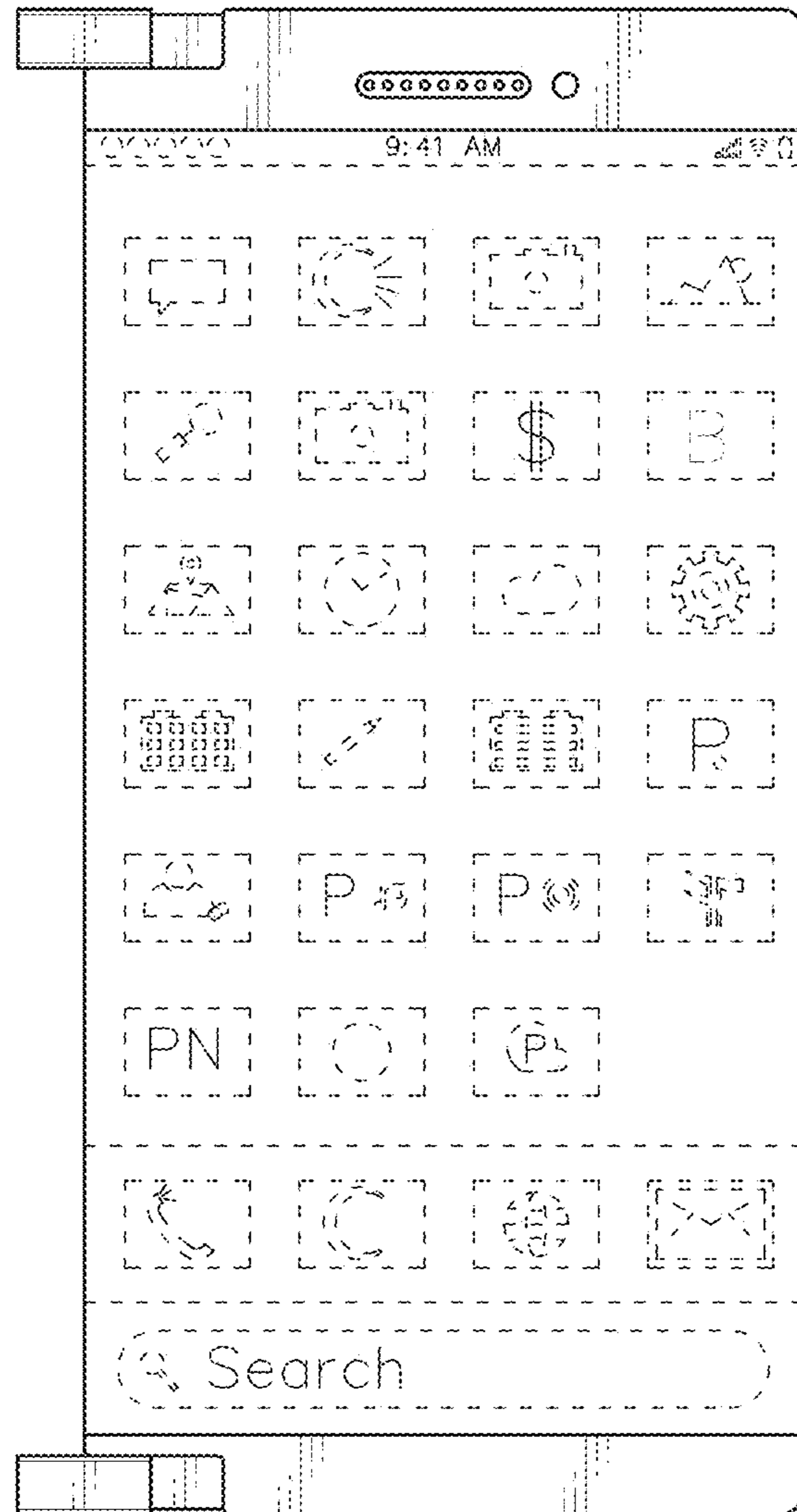


FIG. 9

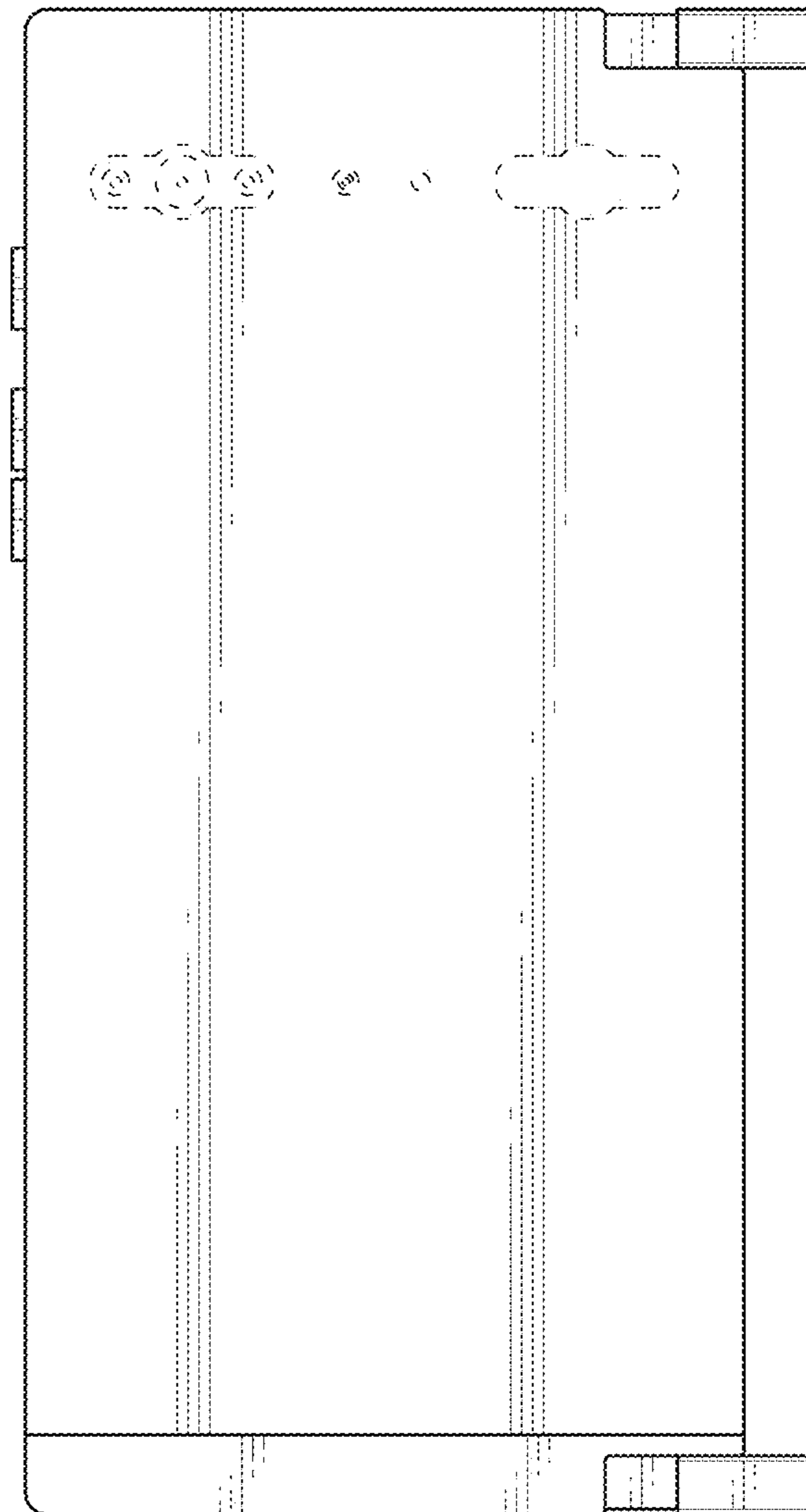


FIG. 10



FIG. 11

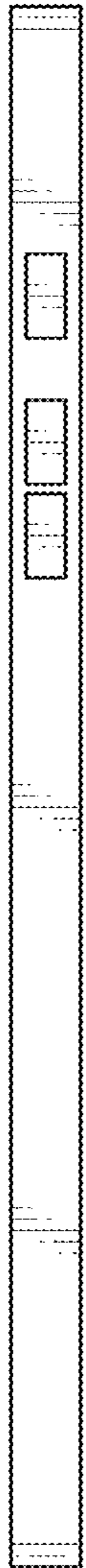


FIG. 12



FIG. 13



FIG. 14