



(12) **United States Design Patent**
Kulkarni et al.

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(54) **CONVERTIBLE ELECTRONIC DEVICE**

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(73) Assignee: **Intel Corporation**, Santa Clara, CA (US)

(**) Term: **15 Years**

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(51) **LOC (13) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/327**; D14/316

(58) **Field of Classification Search**
USPC D14/315–327, 331, 334–336, 339–340, D14/371, 373–374; D18/1–2, 7, 11, D18/12.2–12.3
CPC G06F 1/1618; G06F 1/166; G06F 1/1613; G06F 1/1616; G06F 1/162; G06F 1/1622; G06F 1/1624; G06F 1/1633; G06F 1/1615; G06F 1/1643; G06F 1/1654; G06F 1/1656; G06F 1/1669; G06F 1/203; G06F 1/1688; G06F 1/1601; G06F 1/1637; G06F 1/181; G06F 1/1662; G06F 1/1666; G06F 1/1671; H05K 5/0047; H05K 5/0052

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for “convertible electronic device”, as shown and described.

DESCRIPTION

FIG. 1 is a right, front perspective view of a convertible electronic device.

FIG. 2 is a front view of the convertible electronic device of FIG. 1.

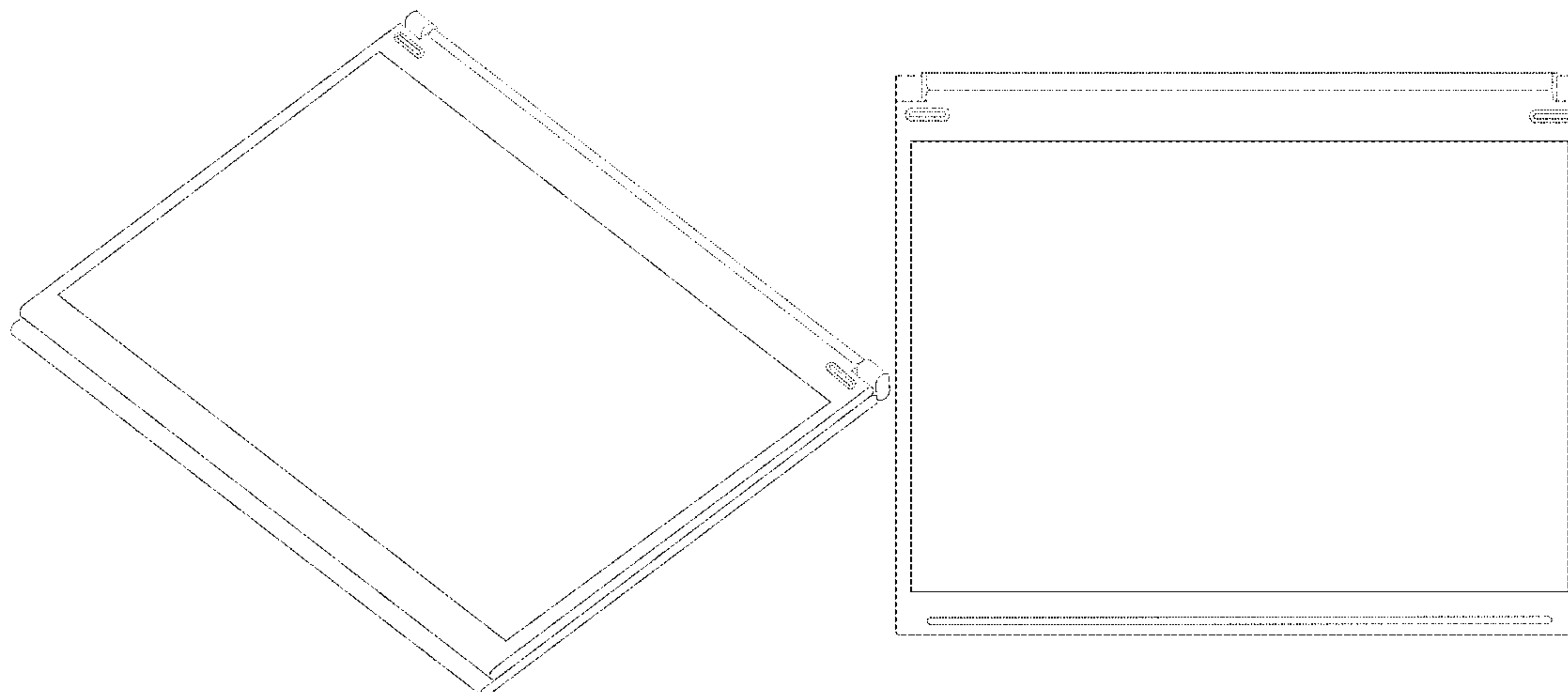
FIG. 3 is a rear view of the convertible electronic device of FIG. 1.

FIG. 4 is a top view of the convertible electronic device of FIG. 1; and,

FIG. 5 is a bottom view of the convertible electronic device of FIG. 1.

The broken lines show portions of the convertible electronic device that form no part of the claimed design. The surfaces adjacent one or more broken lines form no part of the claimed design.

1 Claim, 5 Drawing Sheets



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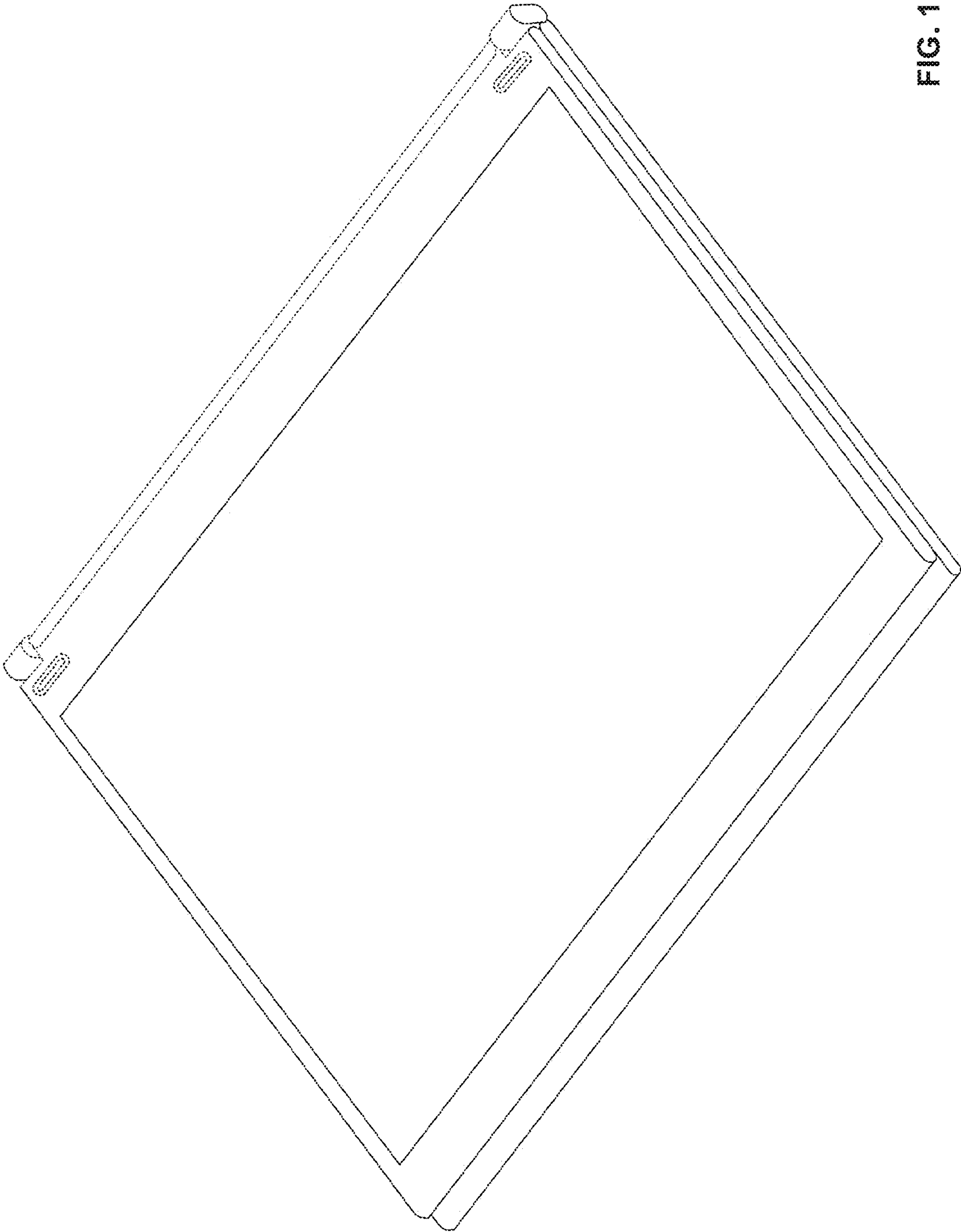


FIG. 1

FIG. 2

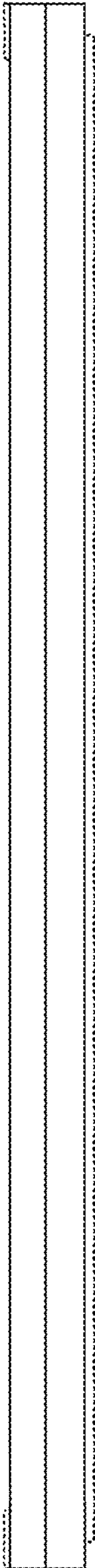
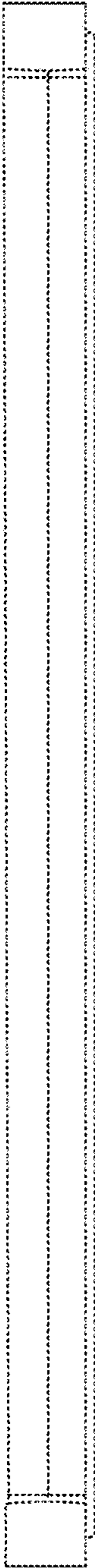


FIG. 3



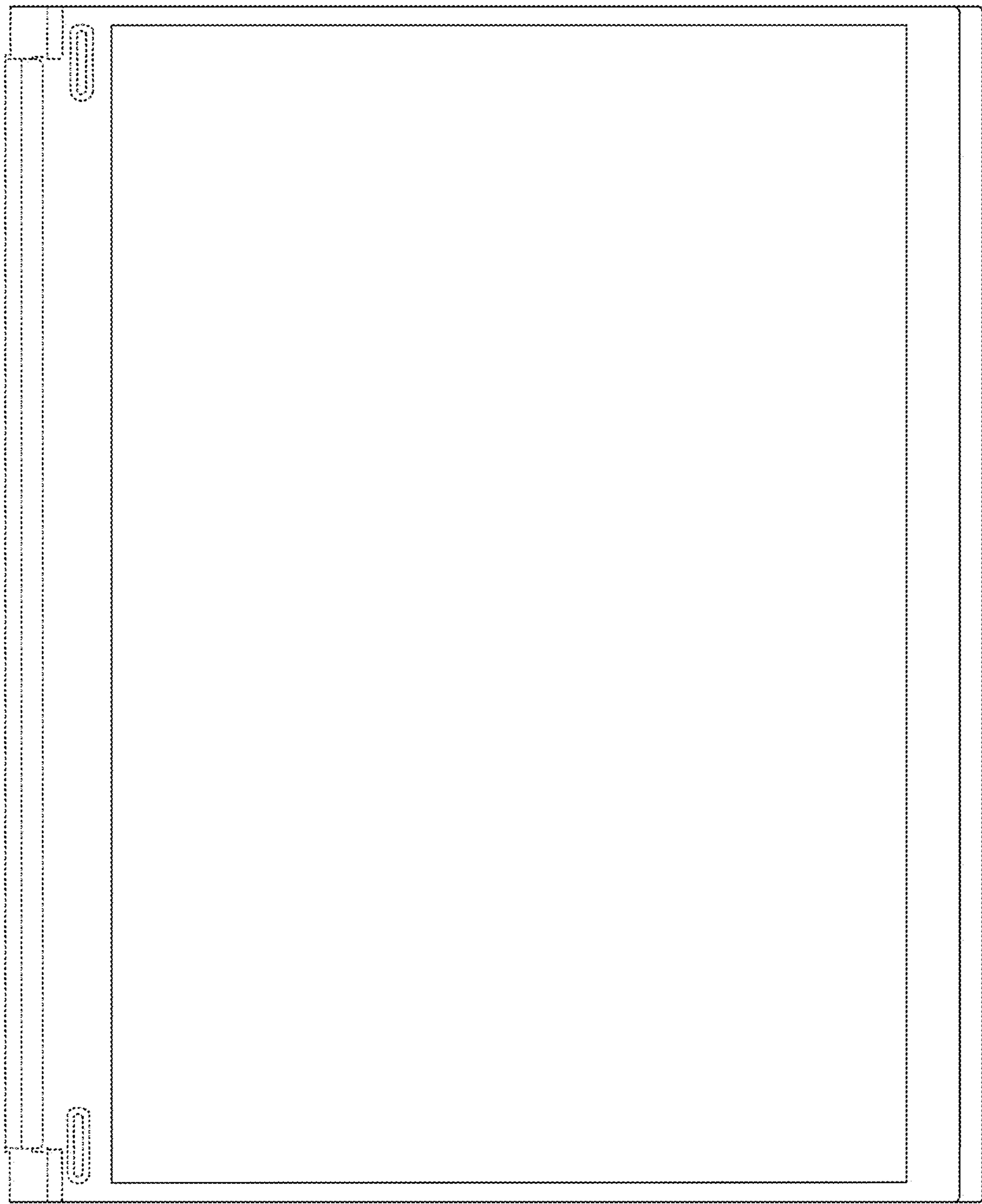


FIG. 4

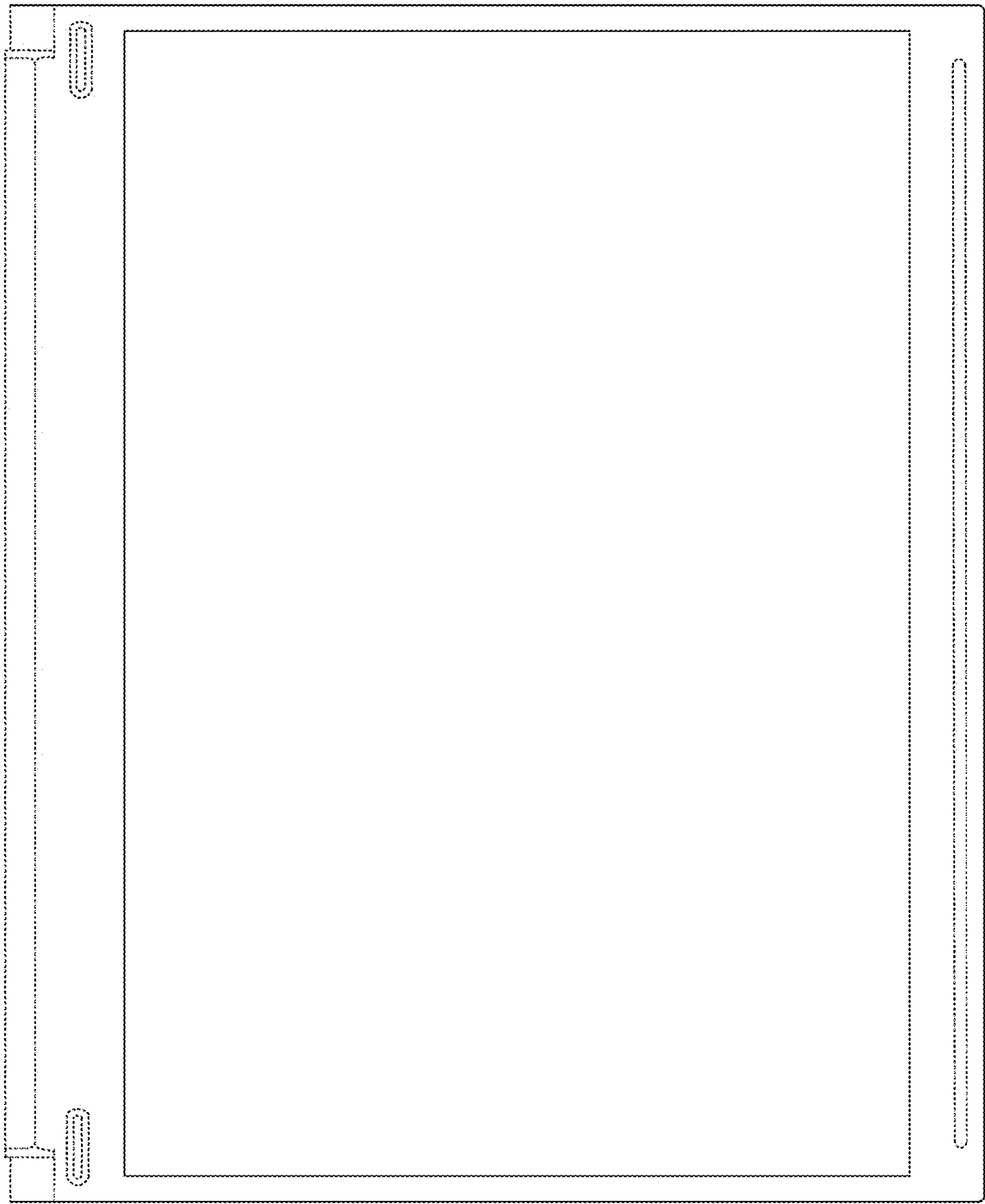


FIG. 5