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(12) **United States Design Patent** (10) **Patent No.:** **US D928,762 S**
Jannard et al. (45) **Date of Patent:** **** Aug. 24, 2021**

- (54) **ELECTRONIC DEVICE**
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- (**) Term: **15 Years**
- (21) Appl. No.: **29/692,924**
- (22) Filed: **May 29, 2019**

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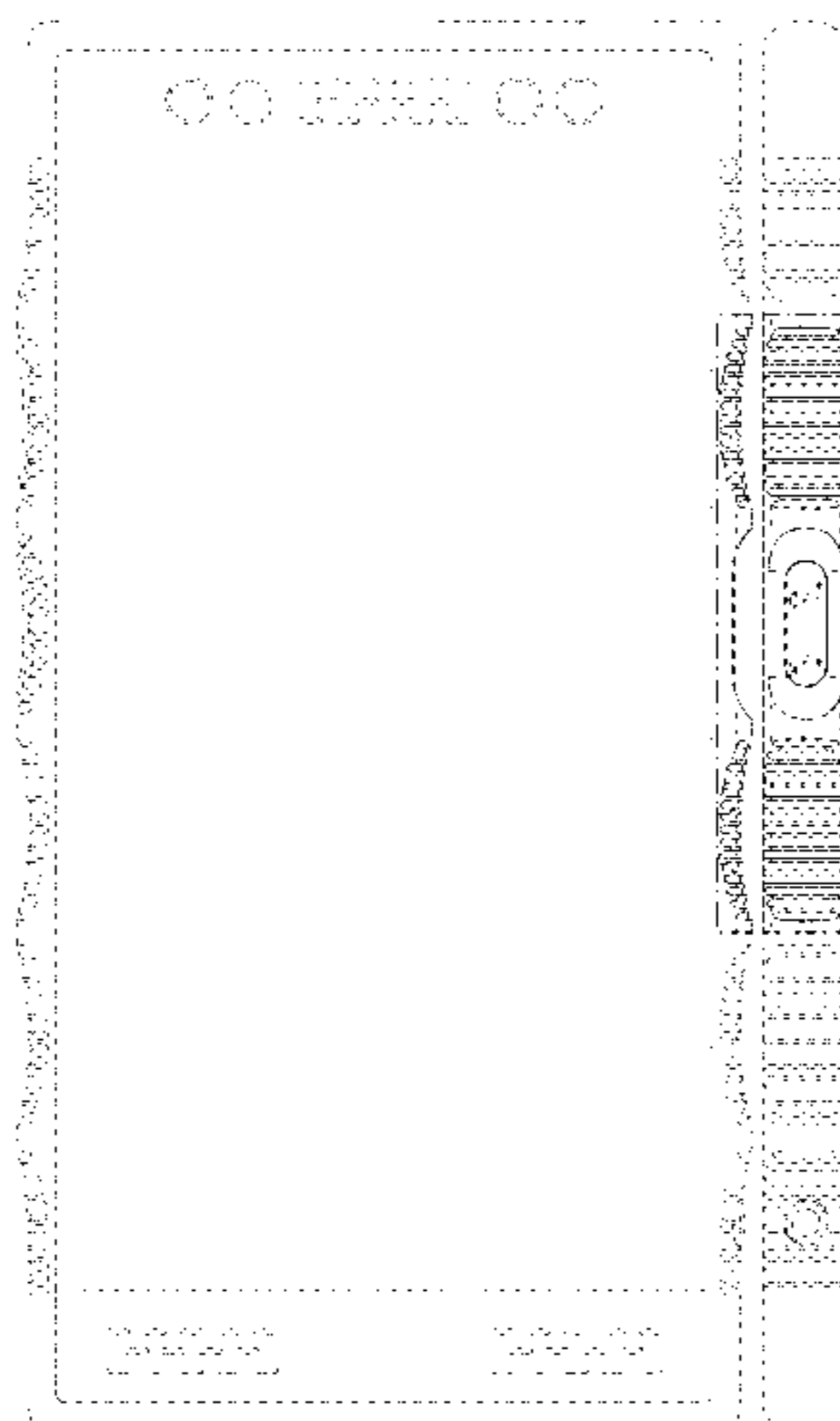
Related U.S. Application Data

- (62) Division of application No. 29/648,268, filed on May 18, 2018, now Pat. No. Des. 873,785.
- (51) **LOC (13) Cl.** **14-03**
- (52) **U.S. Cl.**
USPC **D14/248**
- (58) **Field of Classification Search**
USPC D14/138 G, 248, 341, 371, 374, 138 R, D14/138 AA, 138 AD, 138 AB, 138 AC, D14/138 C, 345, 432, 439
CPC H04M 1/0202; H04M 1/0235; H04M 1/0237; H04M 1/0239; H04M 1/0266; H04M 1/0268; H04M 1/027; H04M 1/0295; H04M 1/02; H04M 1/0279; H04M 1/0283
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an electronic device, as shown and described.

DESCRIPTION

FIG. 1 is a top, front, and left-side perspective view of an electronic device;

FIG. 2 is a bottom, rear, and right-side perspective view of the electronic device of FIG. 1;

FIG. 3 is a front elevational view of the electronic device of FIG. 1;

FIG. 4 is a rear elevational view of the electronic device of FIG. 1;

FIG. 5 is a right-side elevational view of the electronic device of FIG. 1;

FIG. 6 is a left-side elevational view of the electronic device of FIG. 1;

FIG. 7 is a top plan view of the electronic device of FIG. 1; and,

FIG. 8 is a bottom plan view of the electronic device of FIG. 1.

The dot-dash broken lines represent the bounds of the claimed design while all other broken lines are direct to environment and are for illustrative purposes only; the broken lines form no part of the claimed design.

1 Claim, 8 Drawing Sheets

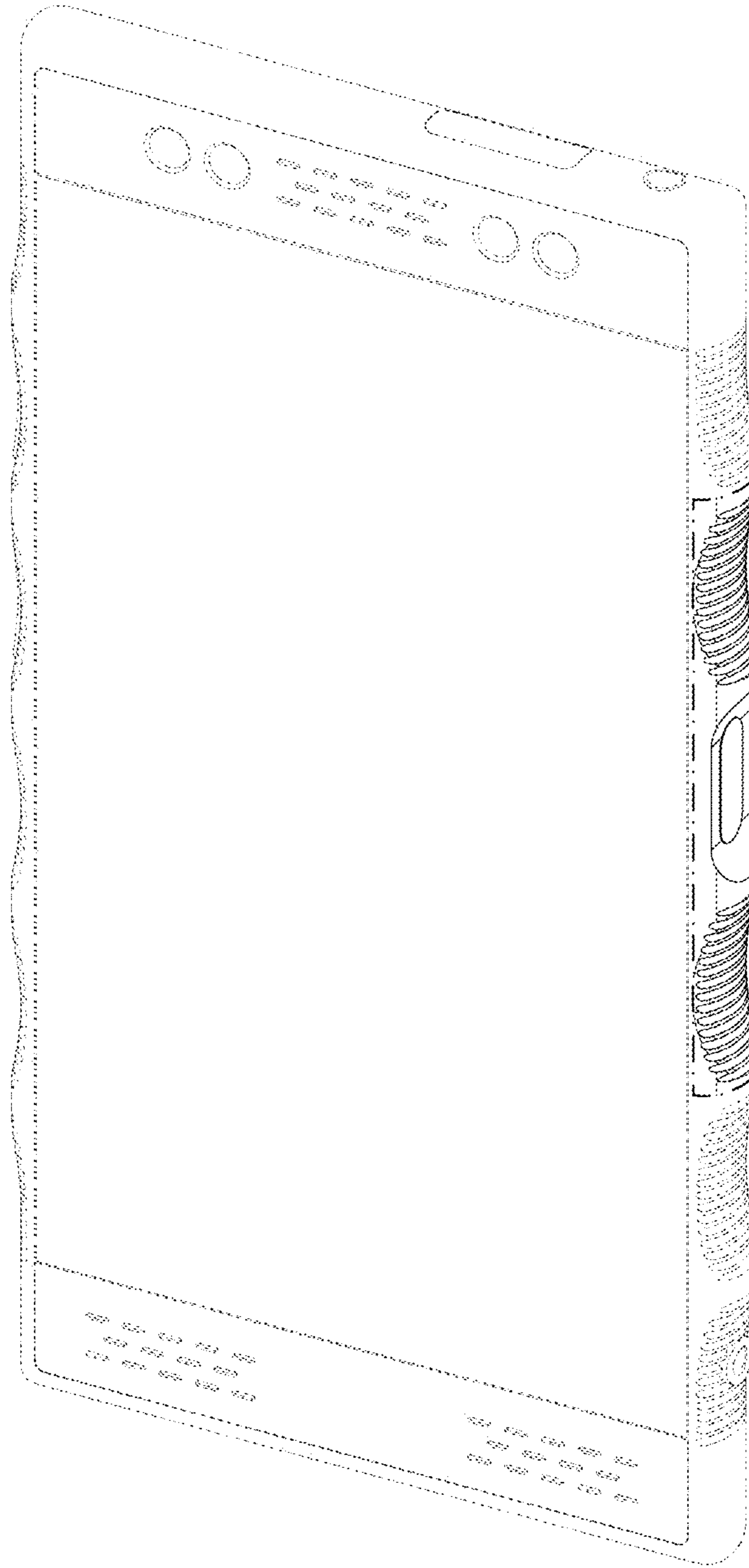


FIG. 1

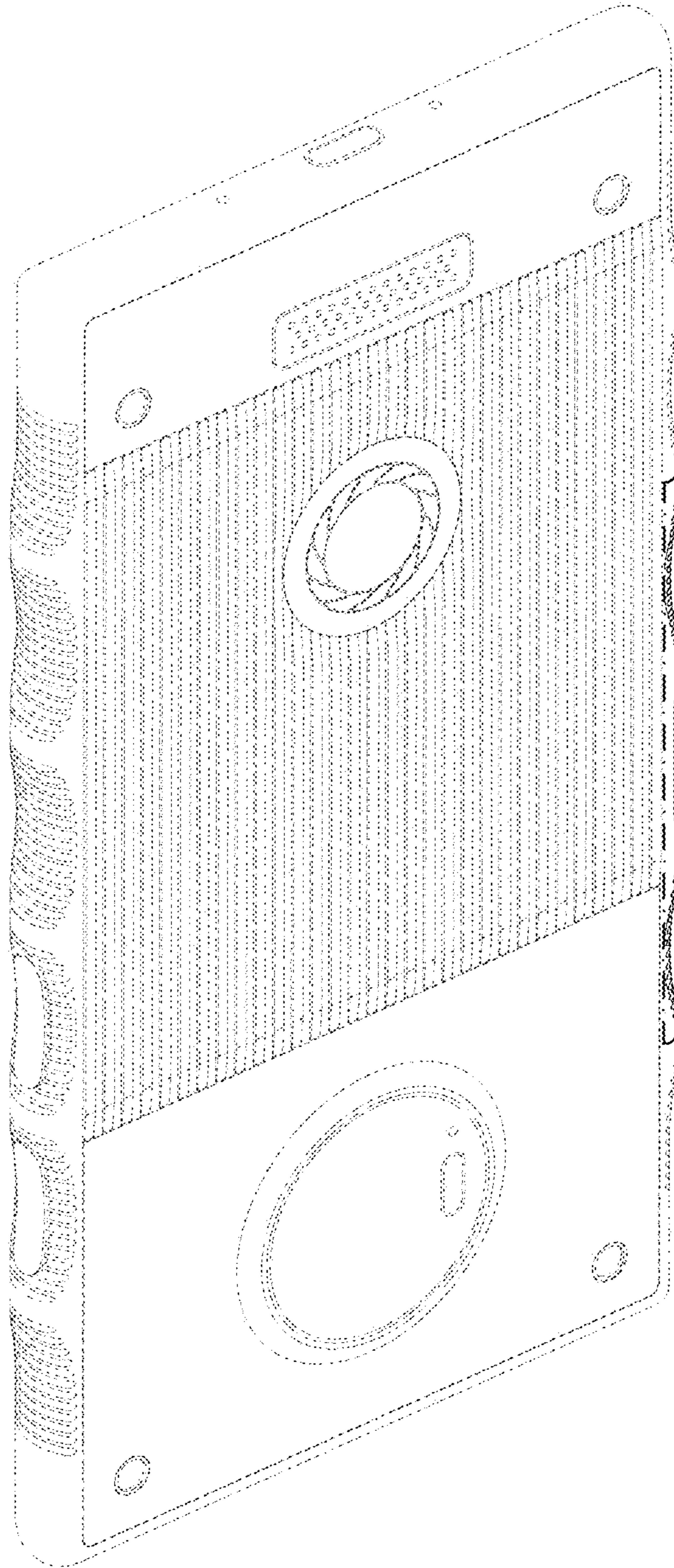


FIG. 2

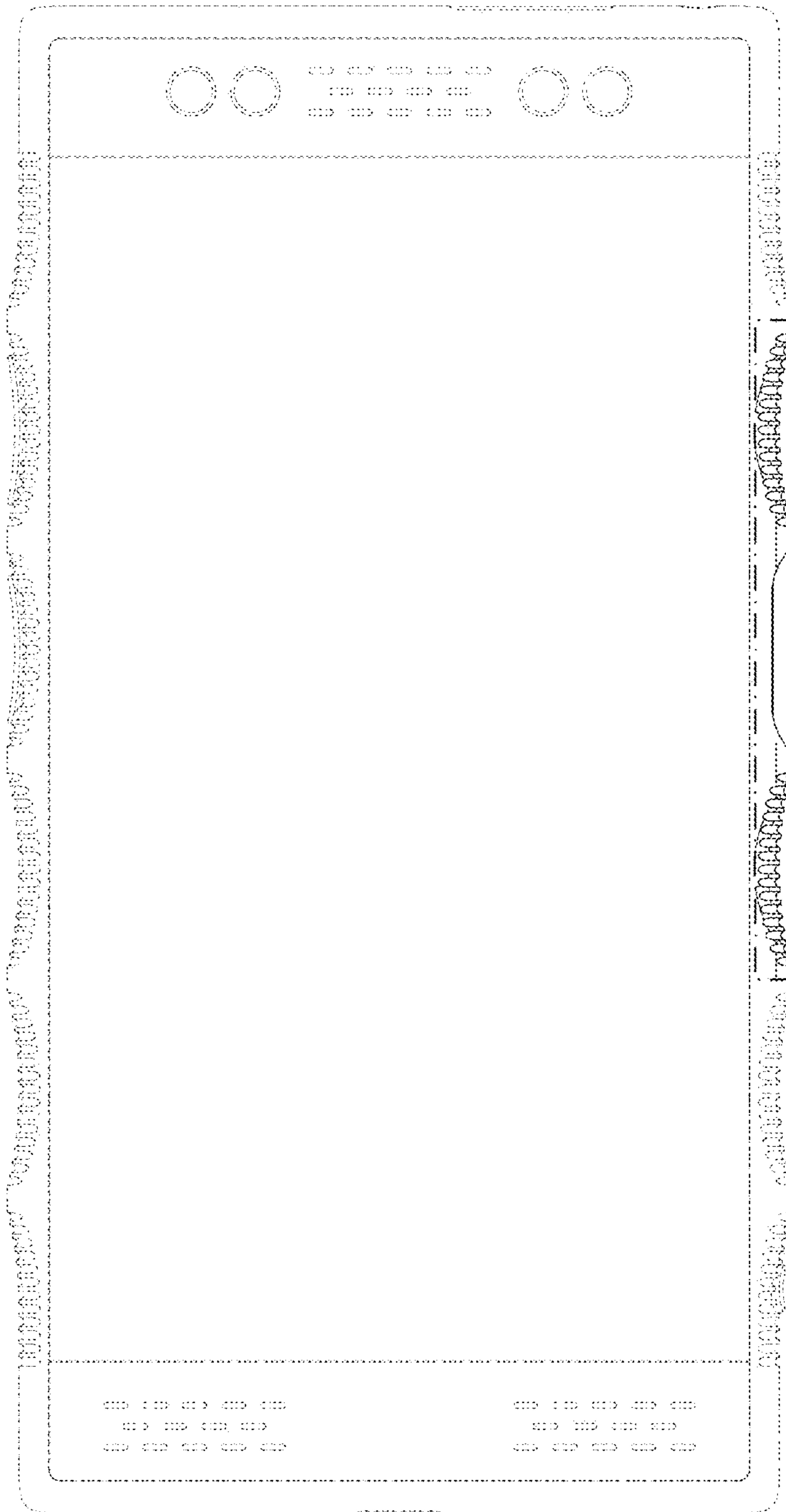


FIG. 3

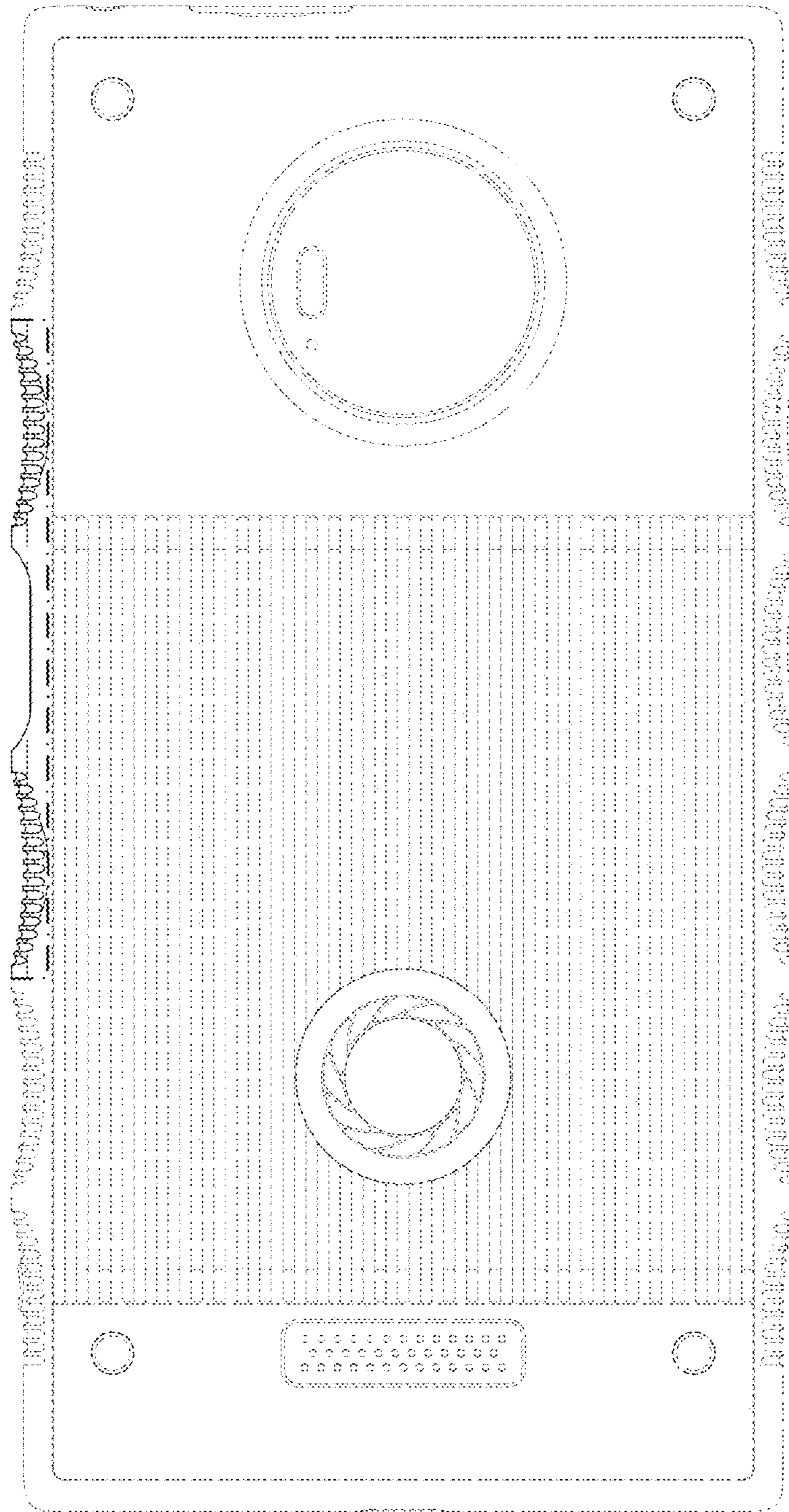


FIG. 4

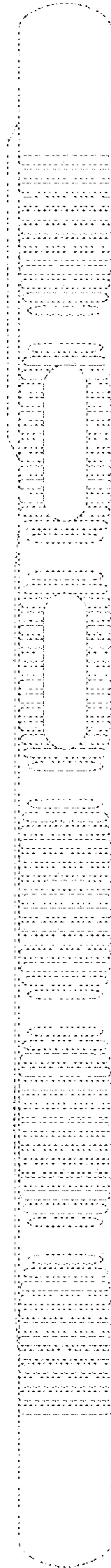


FIG. 5

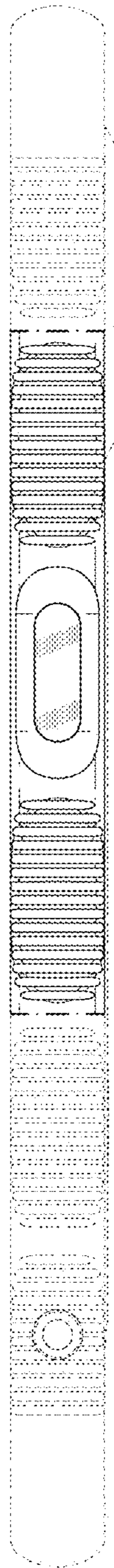


FIG. 6

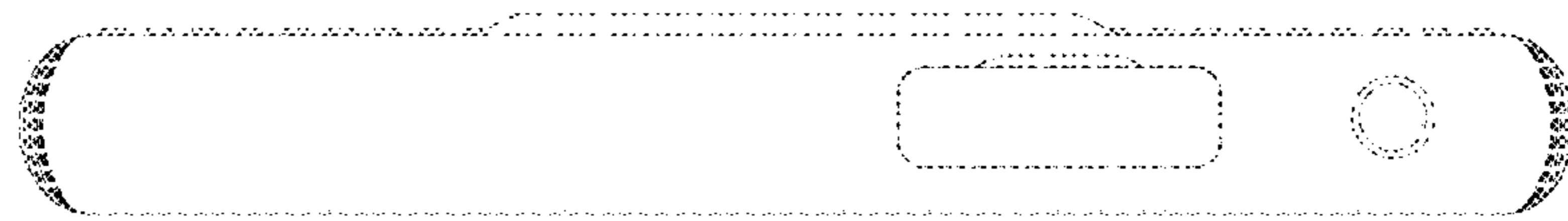


FIG. 7

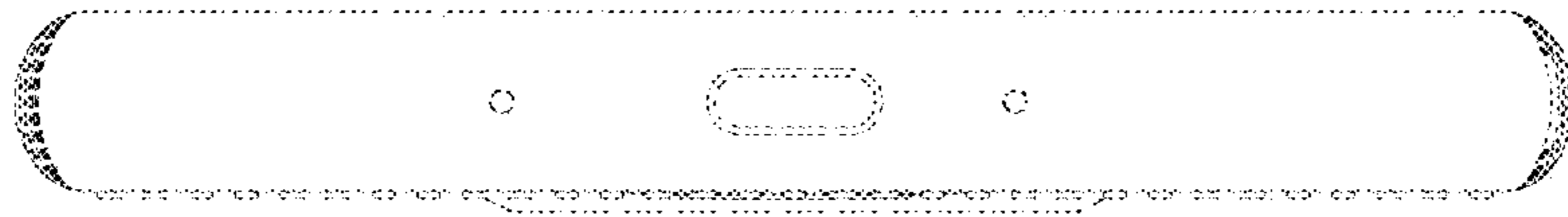


FIG. 8