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(12) **United States Design Patent** (10) **Patent No.:** **US D927,429 S**
Adams et al. (45) **Date of Patent:** **** Aug. 10, 2021**

(54) **PLURALITY OF CONTACT FIELDS FOR A PRINTED CIRCUIT BOARD**

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D875,057 S * 2/2020 Podubni D13/182

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(**) Term: **15 Years**

(21) Appl. No.: **29/683,842**

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(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/147**; D13/182

(58) **Field of Classification Search**
USPC D13/120, 121, 123, 147, 154, 179, 182,
D13/184, 199; D14/356, 432, 435
CPC H05K 2201/10287; H05K 2201/10295;
H05K 7/1084
See application file for complete search history.

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Attorney of Record

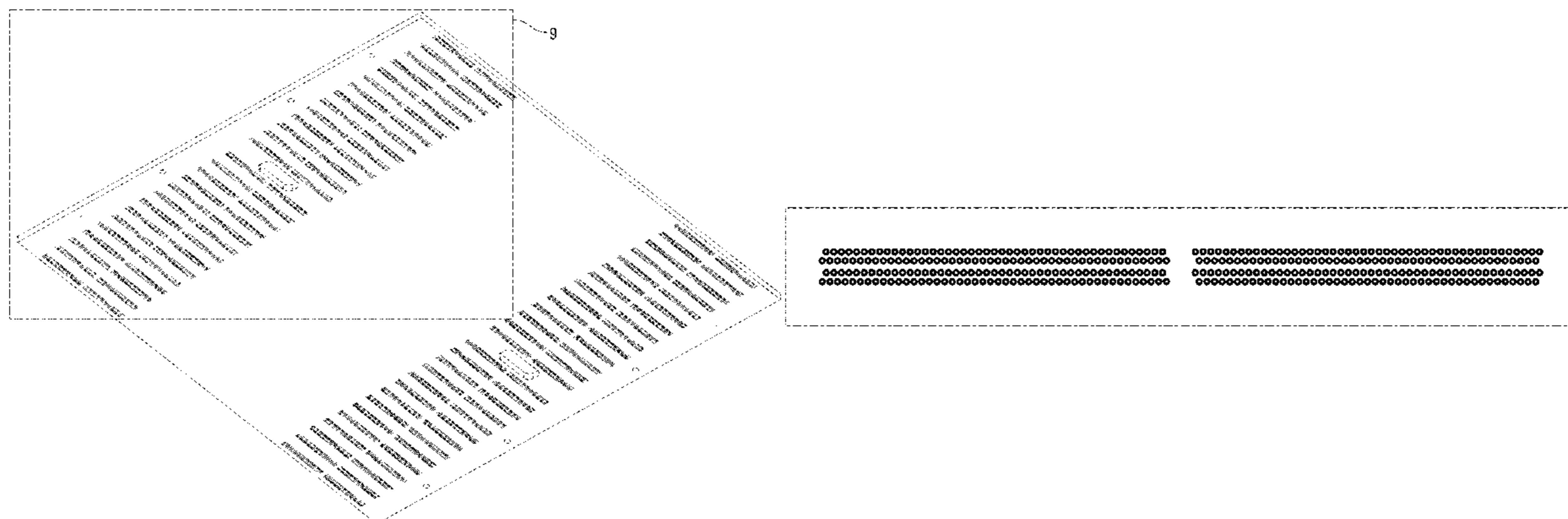
(57) **CLAIM**

The ornamental design for a plurality of contact fields for a printed circuit board, as shown and described.

DESCRIPTION

FIG. 1 is a top perspective view of a plurality of contact fields for a printed circuit board, showing our new design; FIG. 2 is a bottom perspective view thereof; FIG. 3 is a front elevation view thereof; FIG. 4 is a rear elevation view thereof; FIG. 5 is a right elevation view thereof; FIG. 6 is a left elevation view thereof; FIG. 7 is a top plan view thereof; FIG. 8 is a bottom plan view thereof; FIG. 9 is a bottom perspective view of thereof, shown enlarged; and, FIG. 10 is a bottom elevation thereof, shown enlarged. The equal-length broken lines depict environmental subject matter that forms no part of the claimed design.

1 Claim, 8 Drawing Sheets



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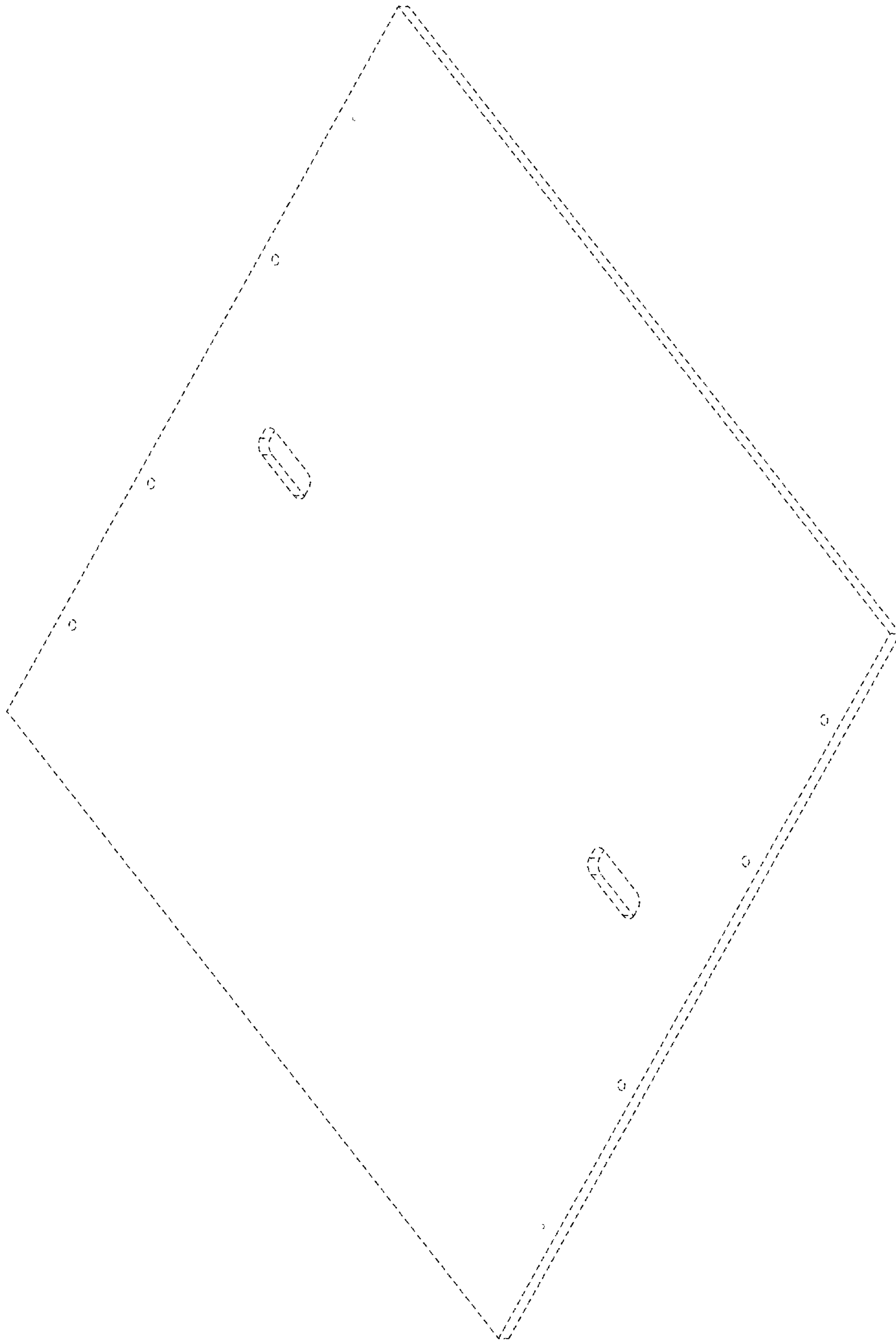


FIG. 1

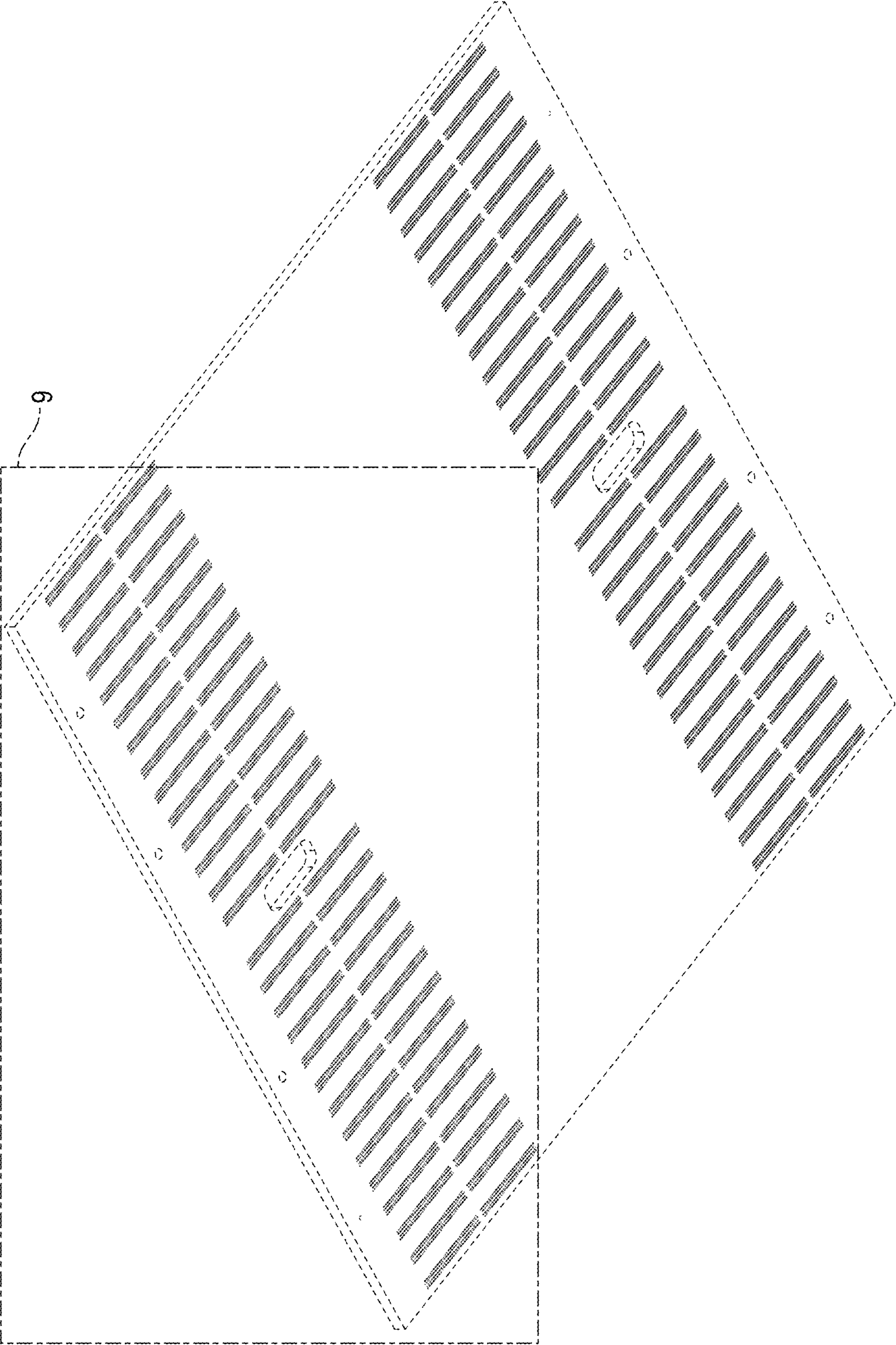


FIG. 2

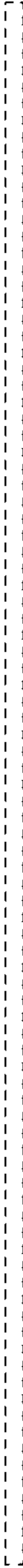


FIG. 3

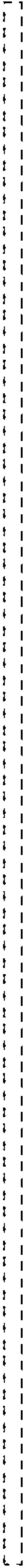


FIG. 4

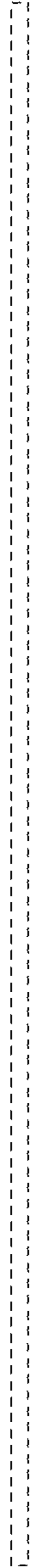


FIG. 5

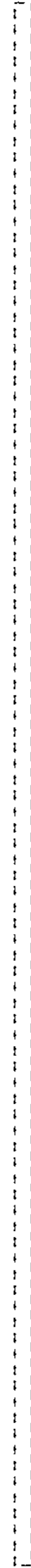
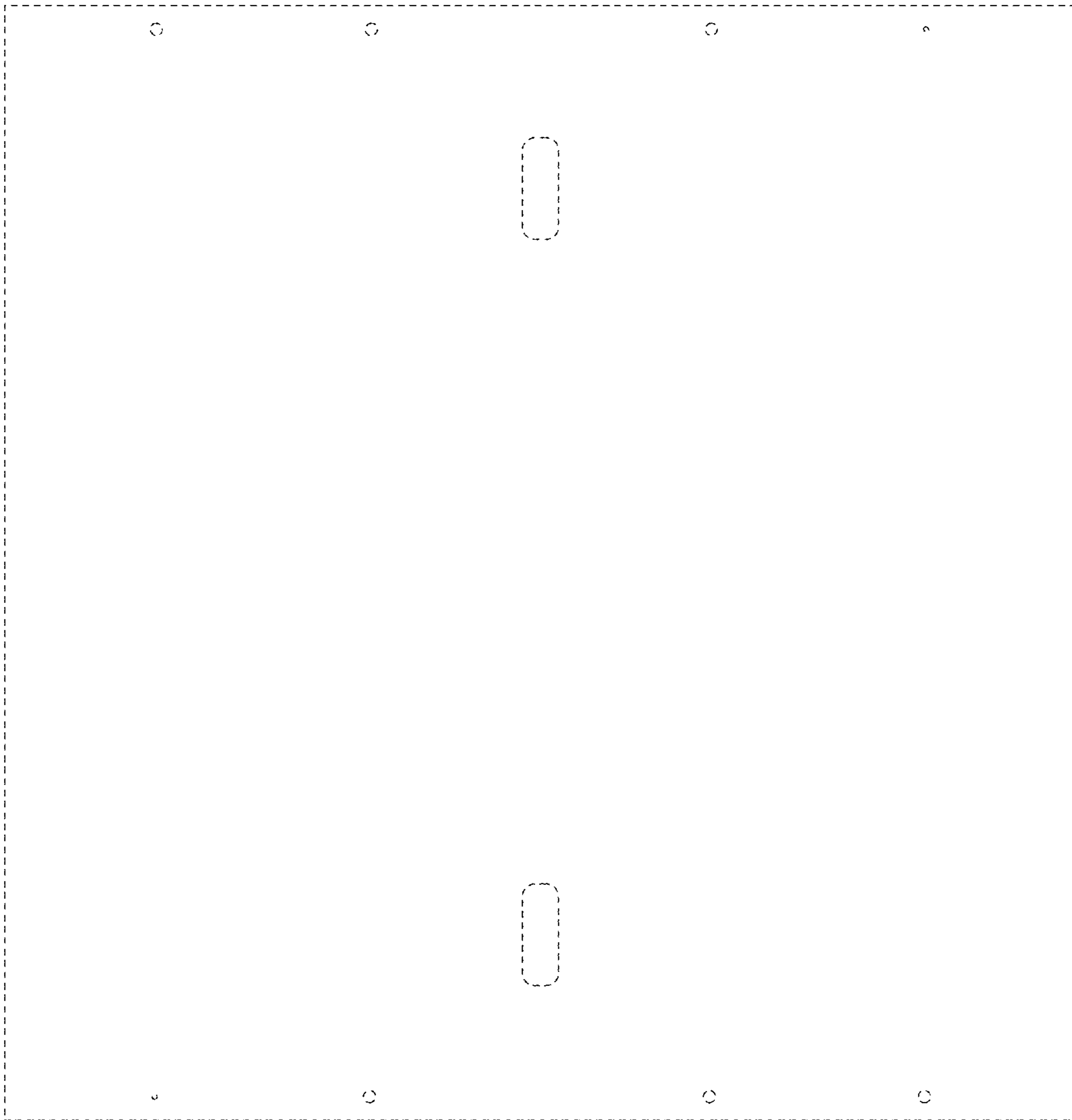


FIG. 6

FIG. 7



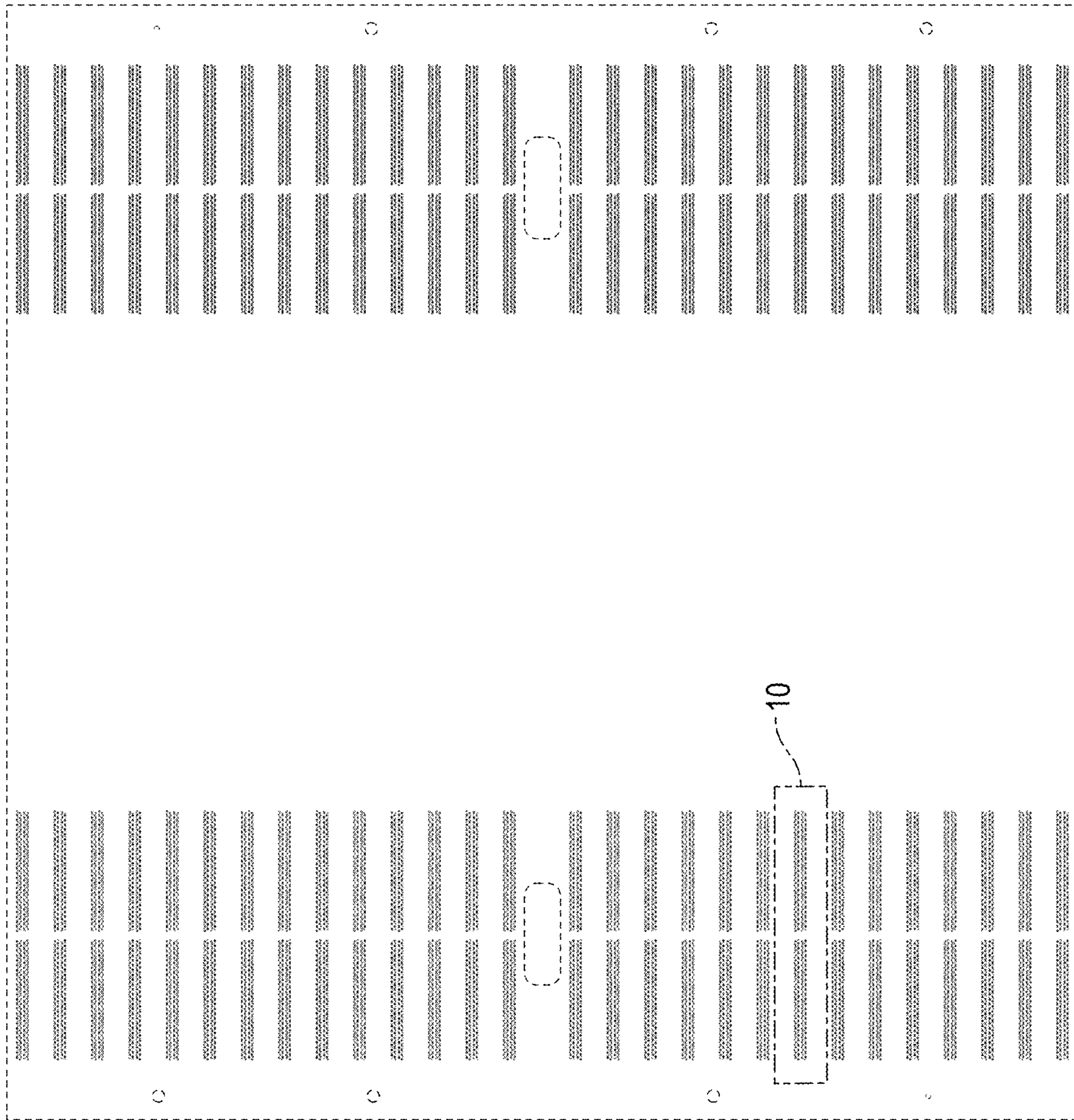


FIG. 8

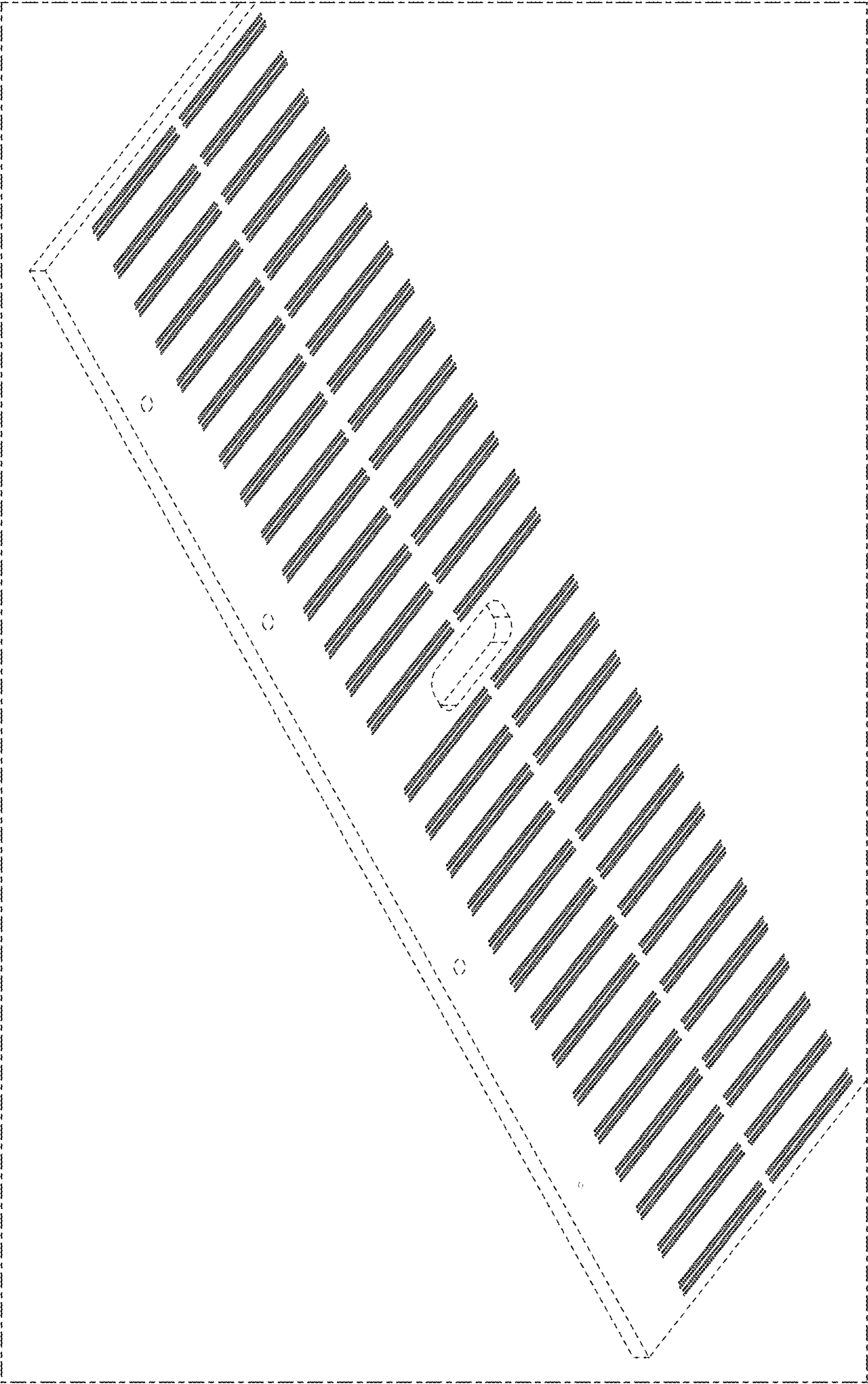


FIG. 9

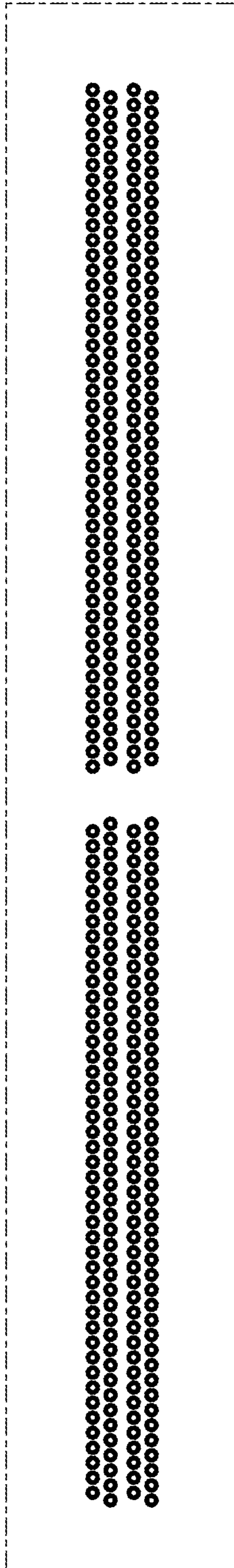


FIG. 10