



US00D927233S

(12) **United States Design Patent** (10) **Patent No.:** **US D927,233 S**
Lee (45) **Date of Patent:** **** Aug. 10, 2021**

(54) **STAND FOR ELECTRONIC DEVICES**

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(**) Term: **15 Years**

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Primary Examiner — Steven J Czyz

(51) **LOC (13) Cl.** **20-02**

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(52) **U.S. Cl.**

USPC **D6/682**; D13/108

(58) **Field of Classification Search**

USPC D6/672, 673, 682, 682.1, 682.3;
D13/107, 108, 118, 199; D14/432, 251,
D14/253

CPC .. H02J 50/00; H02J 50/05; H02J 50/10; H02J
50/12; H02J 50/15; H02J 50/40; H02J
50/80; H02J 7/005; H02J 7/025; H02J
7/0042; H02J 7/0044; H02J 7/0045

See application file for complete search history.

(57) **CLAIM**

The ornamental design for a stand for electronic devices, as shown and described.

DESCRIPTION

(56) **References Cited**

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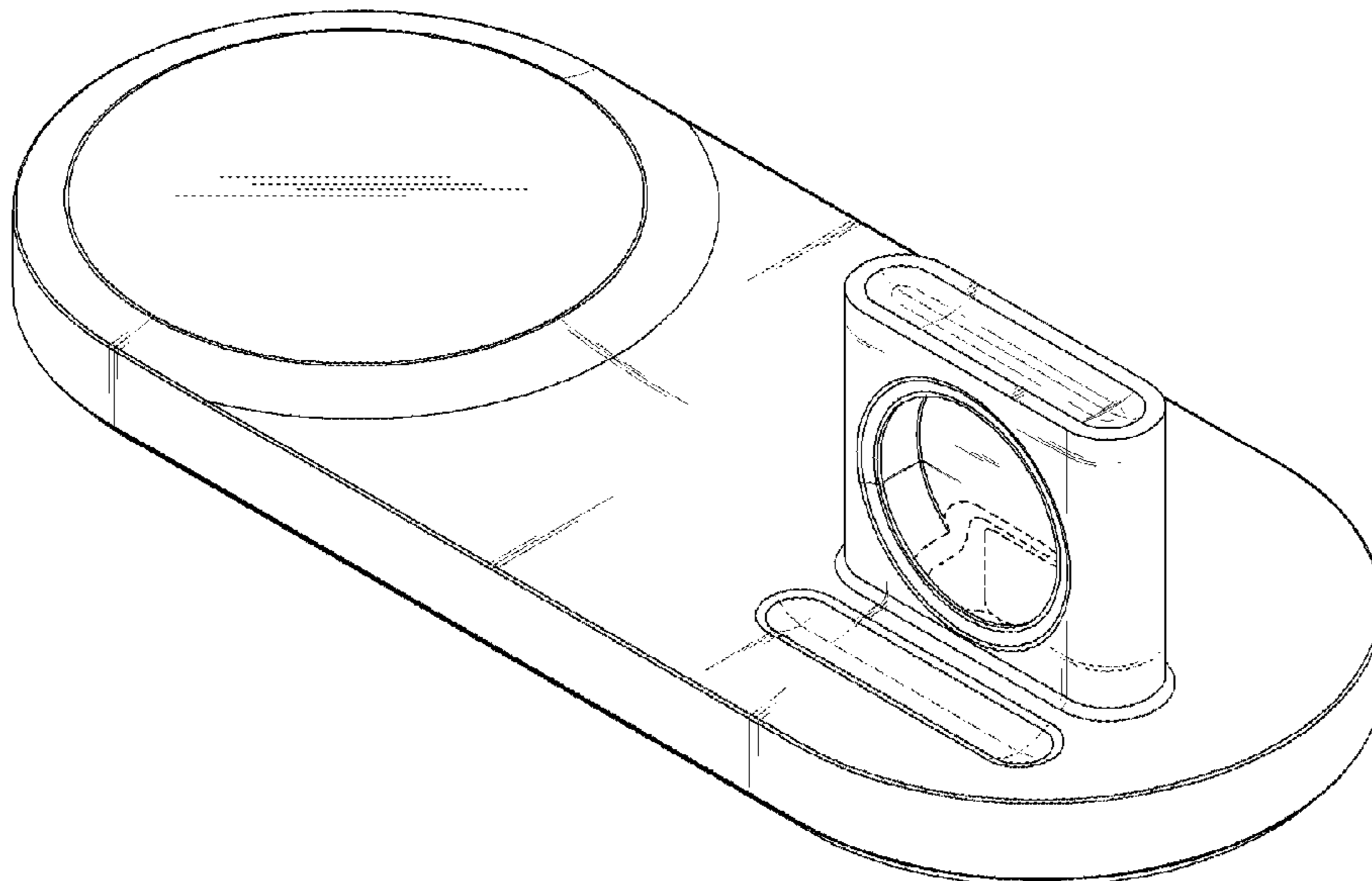
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FIG. 1 is a front, top, and right-side perspective view of a stand for electronic devices showing my new design; FIG. 2 is a front view thereof; FIG. 3 is a back view thereof; FIG. 4 is a left-side view thereof; FIG. 5 is a right-side view thereof; FIG. 6 is a top view thereof; FIG. 7 is a bottom view thereof; FIG. 8 is a back, top, and right-side perspective view thereof; FIG. 9 is a front, bottom, and left-side perspective view thereof; and, FIG. 10 is a front, top, and right-side perspective view thereof in use.

The broken lines shown in FIGS. 1, 2, 7, and 9 depict parts of the stand for electronic devices and form no part of the claimed design. The broken lines shown in FIG. 10 depict environment and form no part of the claimed design.

1 Claim, 7 Drawing Sheets



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FIG. 1

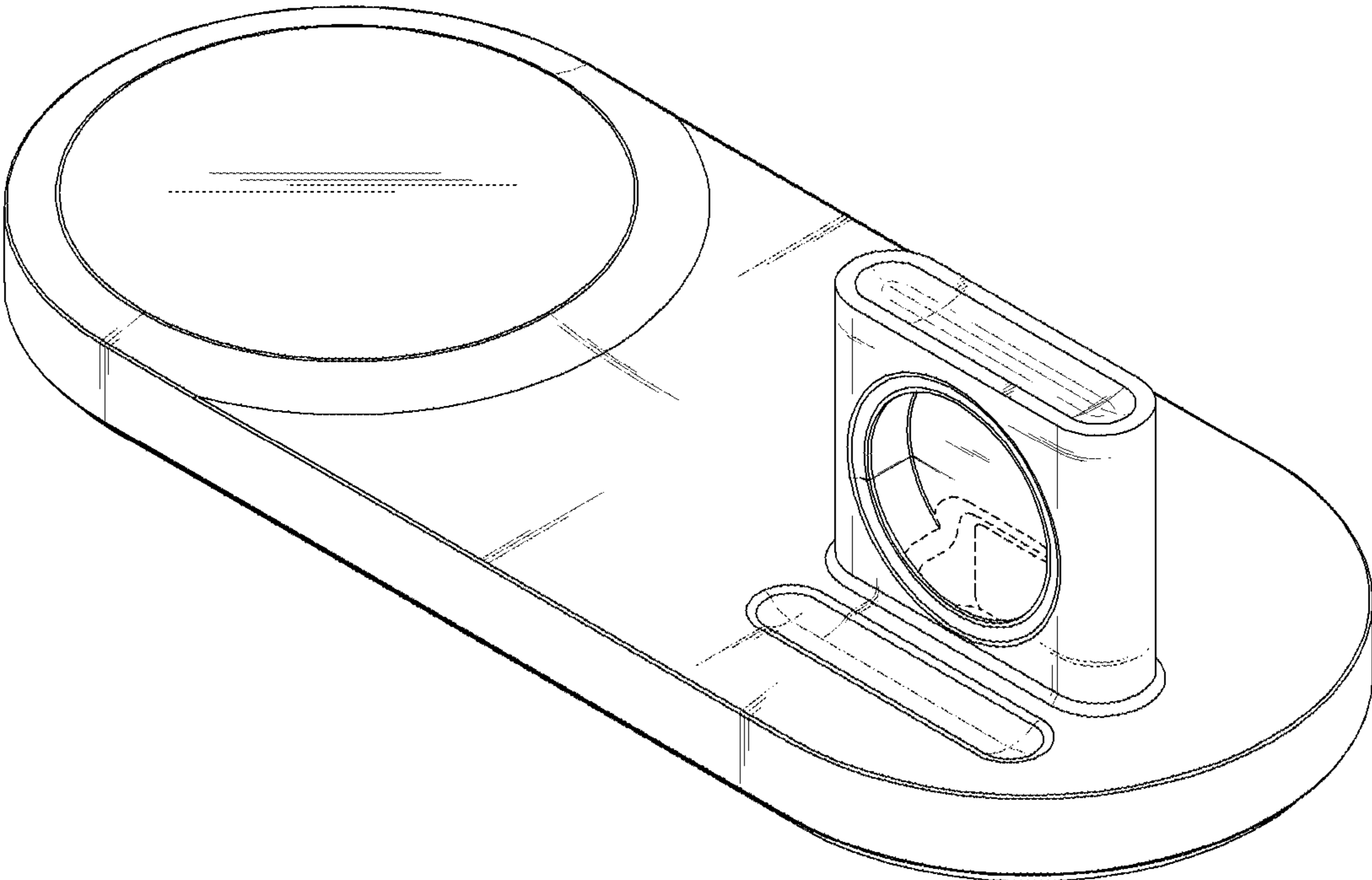


FIG. 2

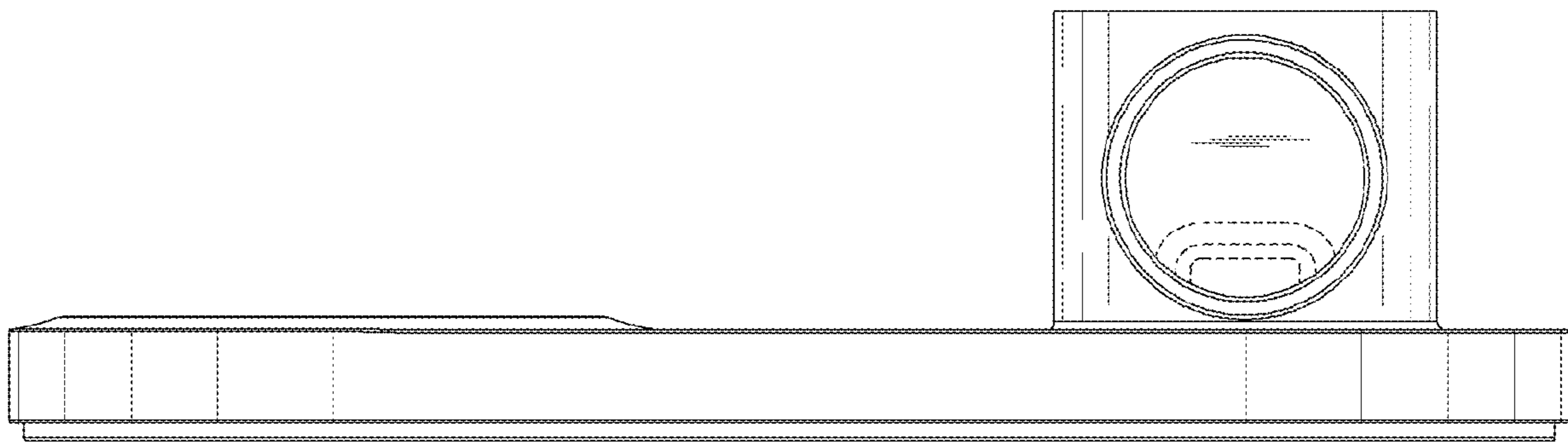


FIG. 3

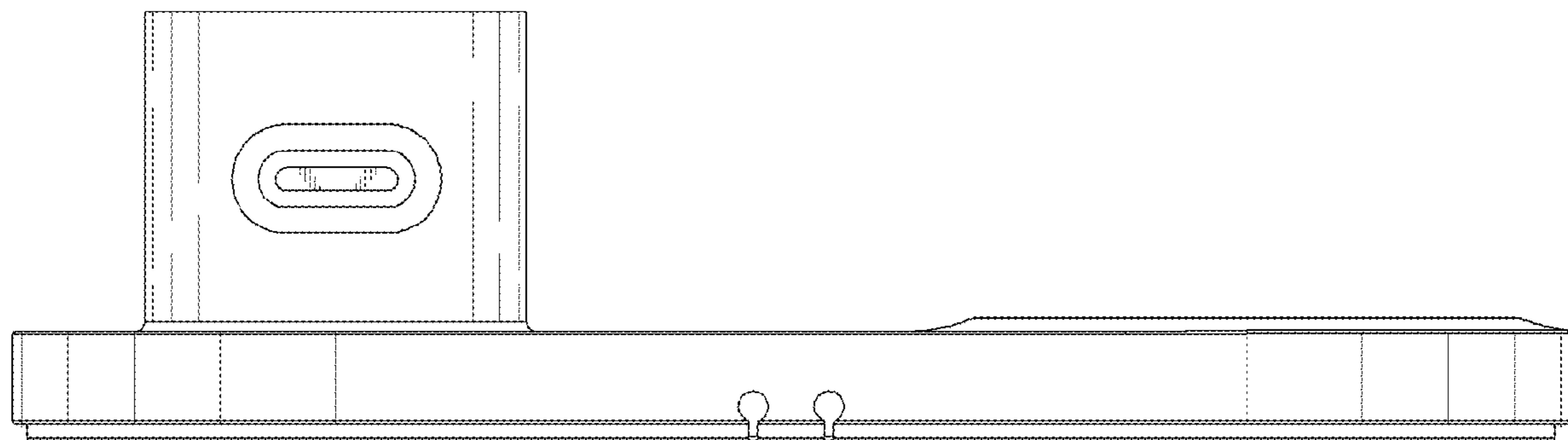


FIG. 4

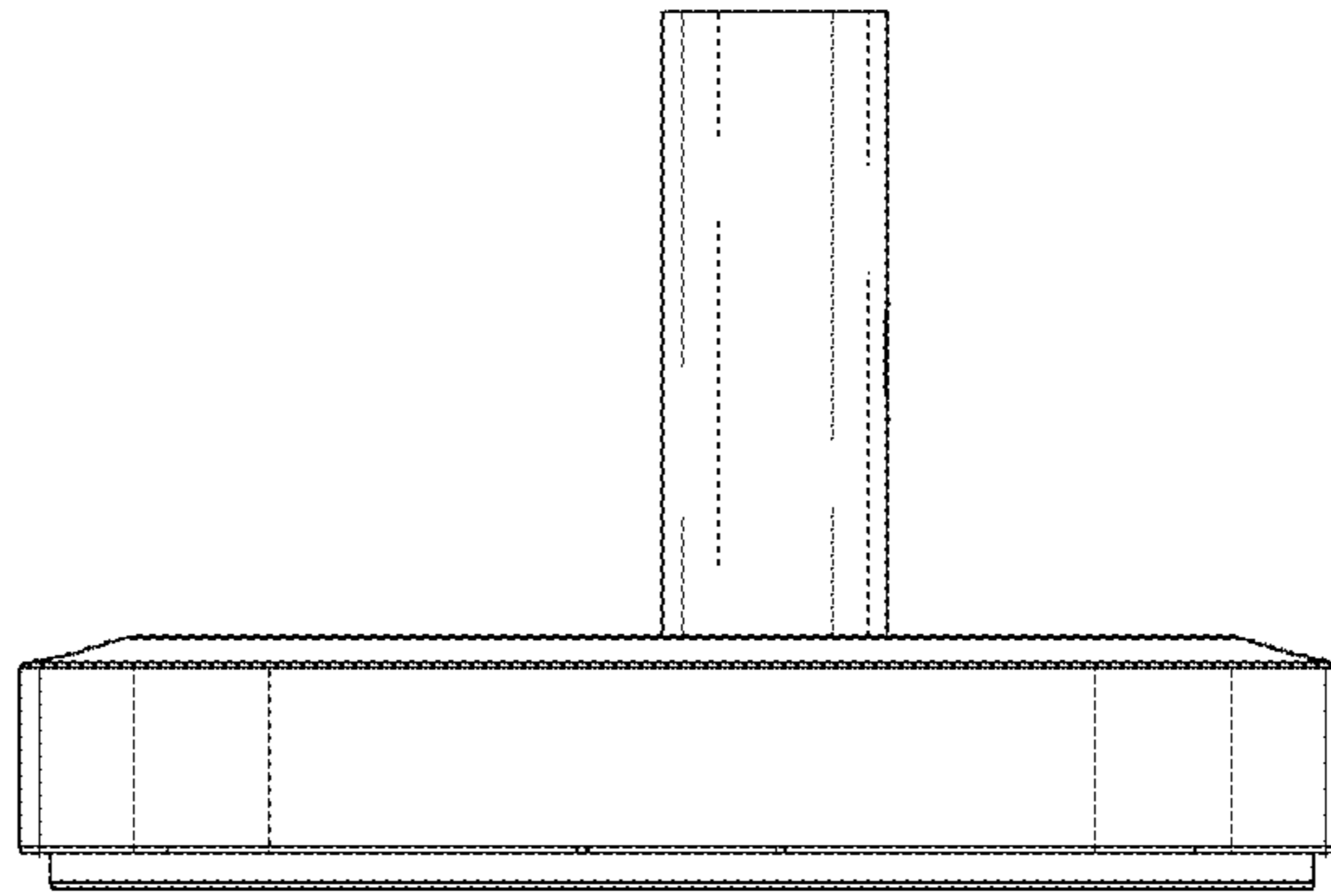


FIG. 5

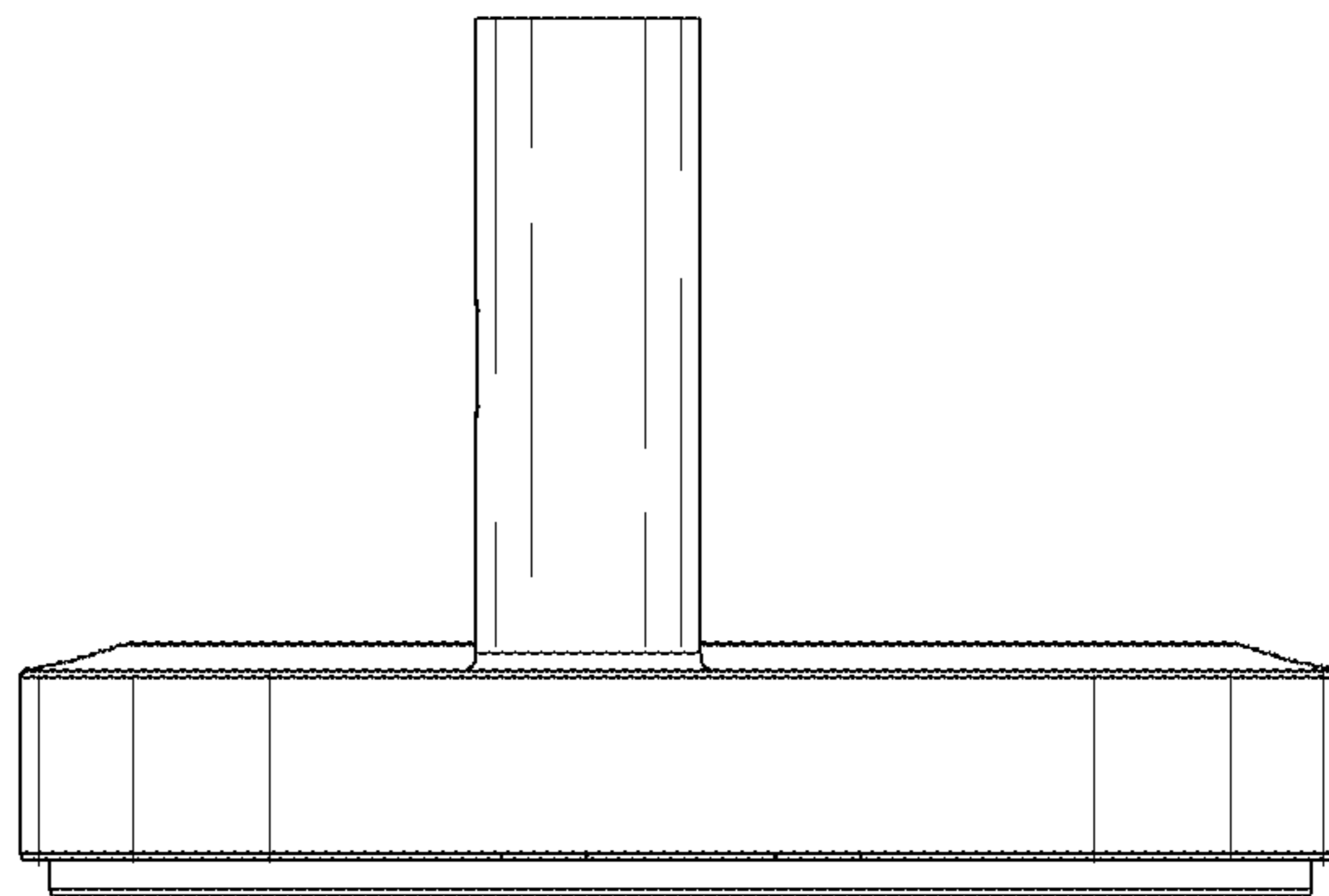


FIG. 6

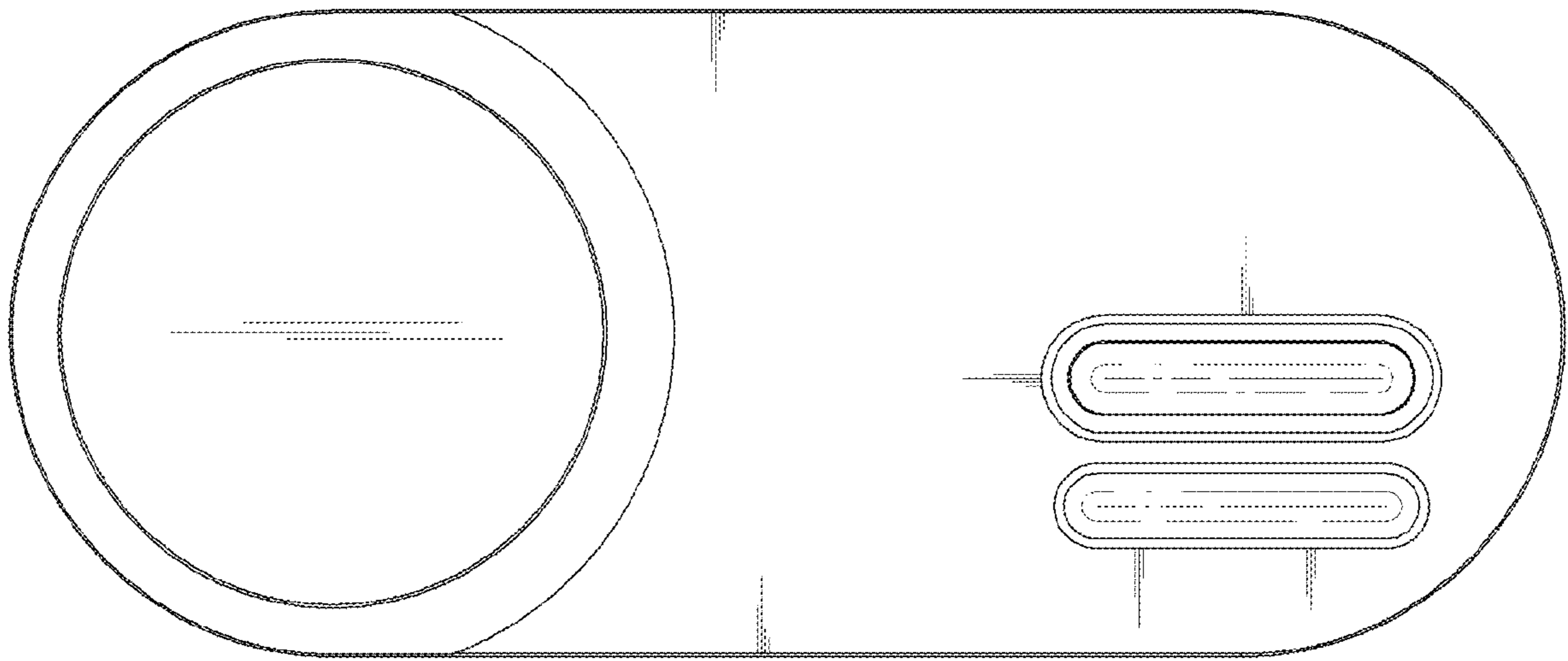


FIG. 7

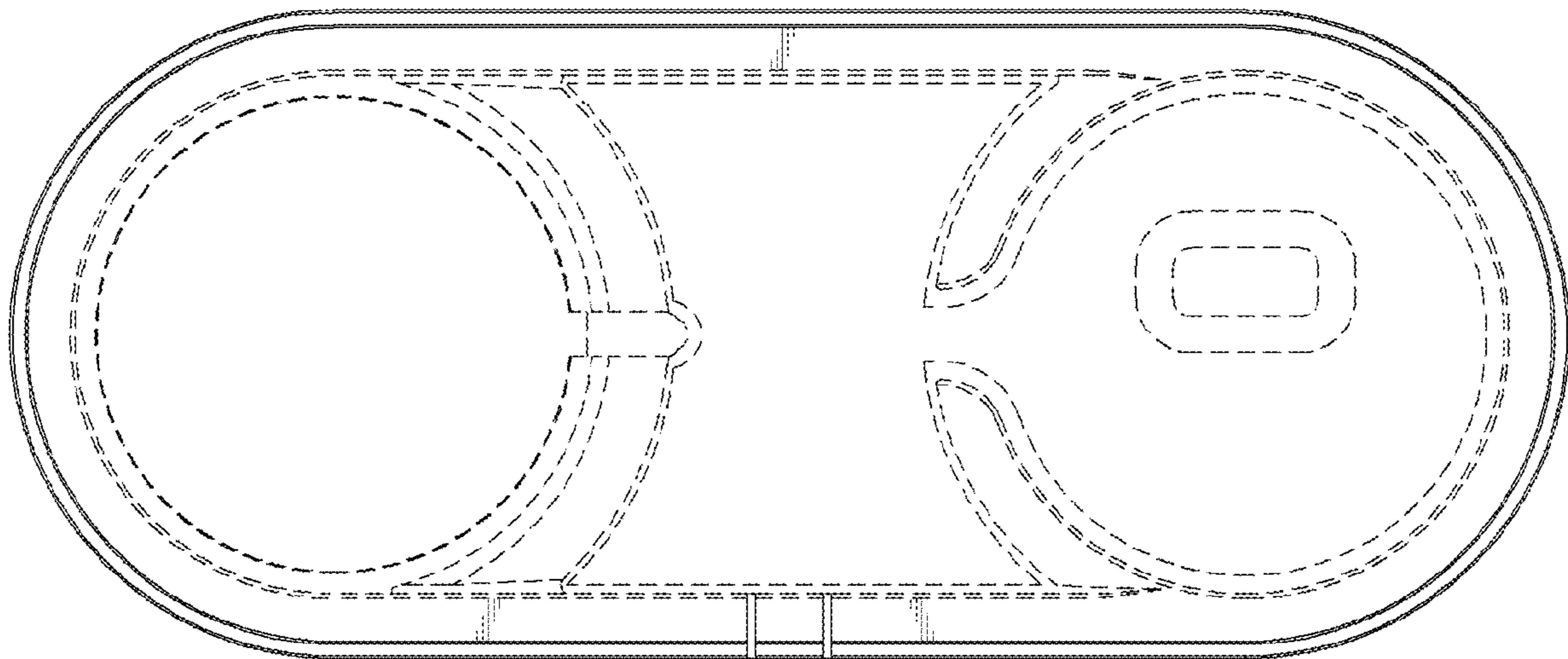


FIG. 8

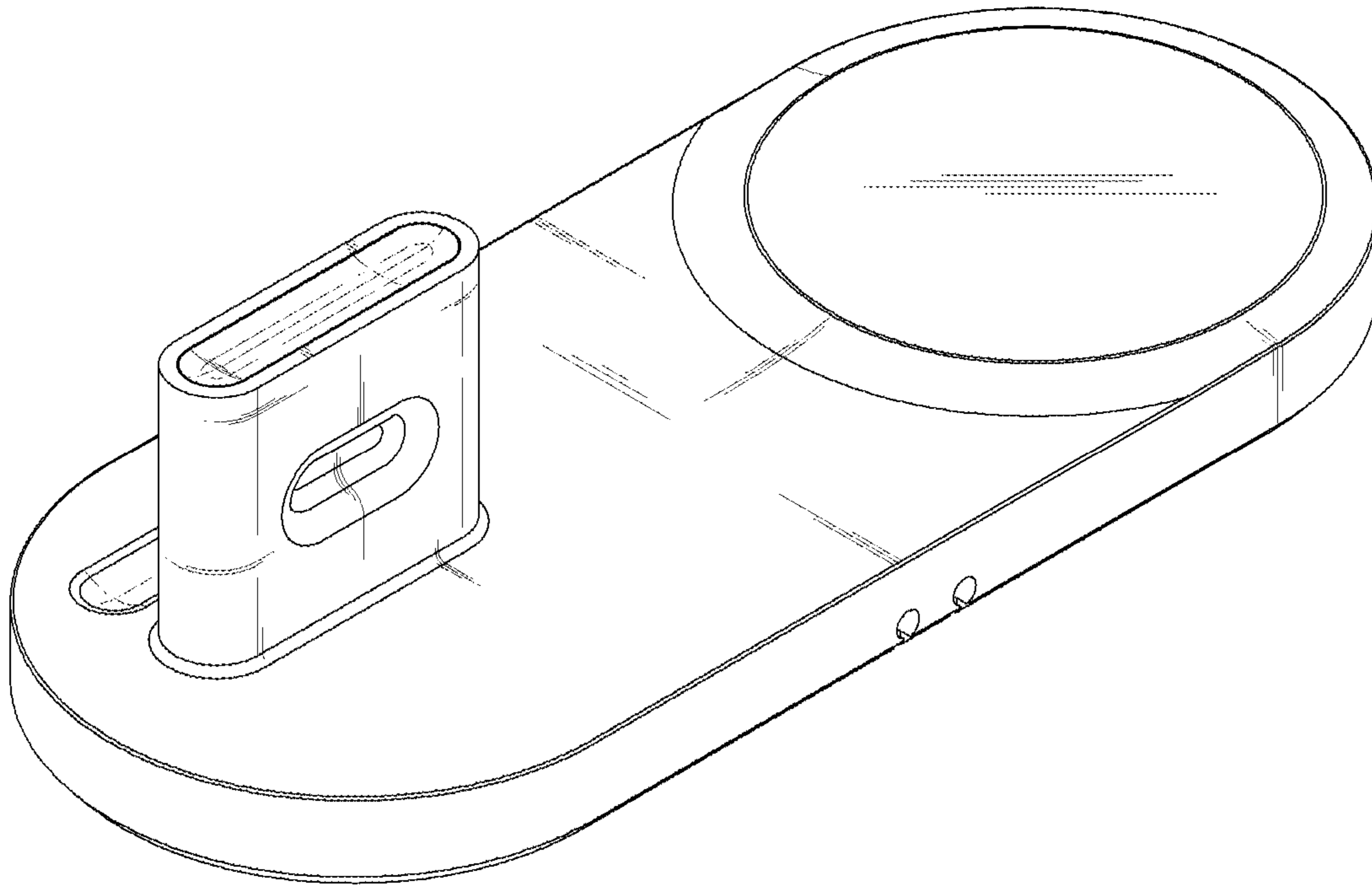


FIG. 9

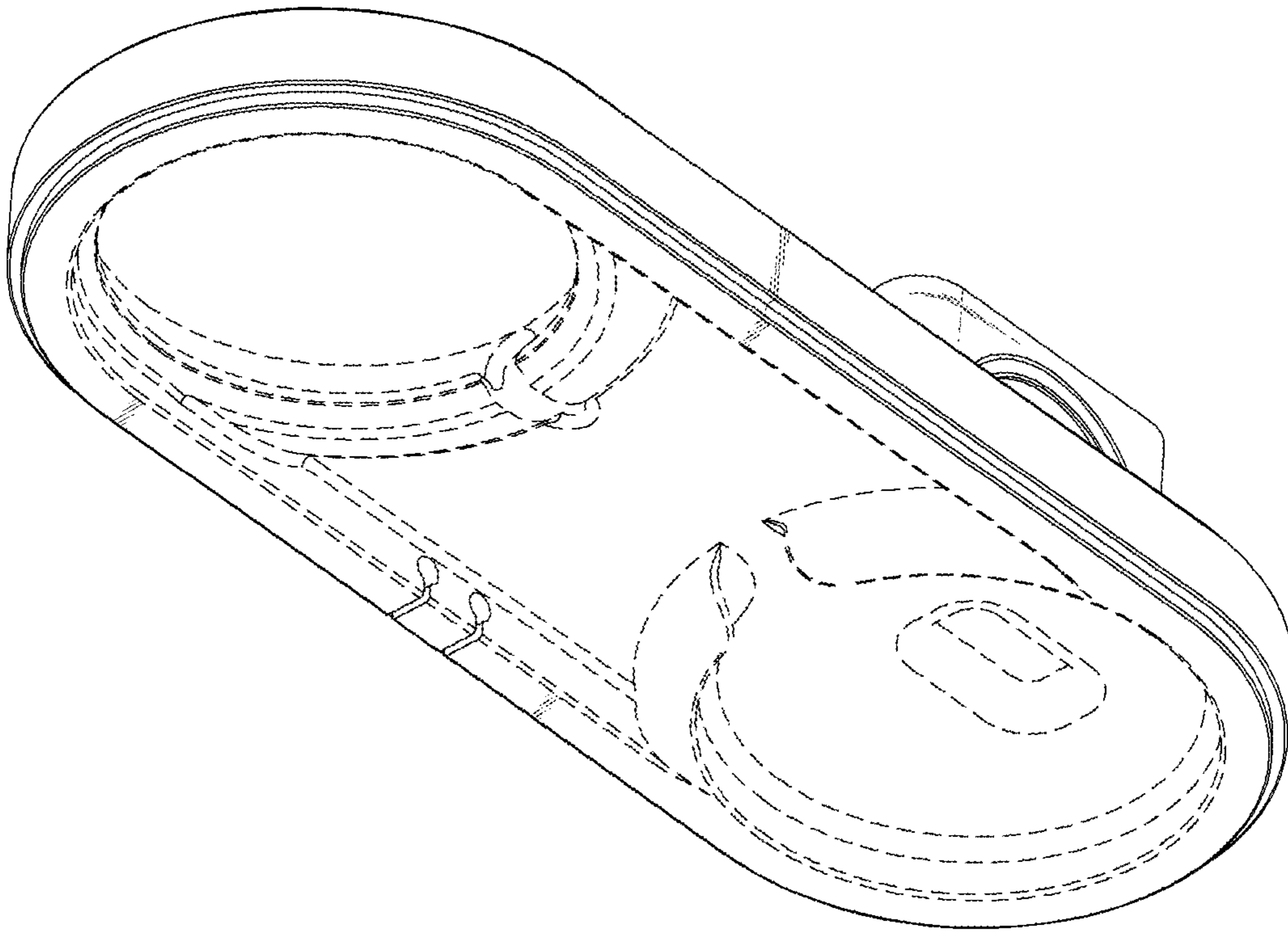


FIG. 10

