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(12) **United States Design Patent**  
**Kagaya et al.**

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(54) **INLET LINER FOR SUBSTRATE PROCESSING APPARATUS**  
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(73) Assignee: **KOKUSAI ELECTRIC CORPORATION**, Tokyo (JP)

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(21) Appl. No.: **29/693,715**  
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(30) **Foreign Application Priority Data**  
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(57) **CLAIM**

(51) **LOC (13) Cl.** ..... **13-03**  
(52) **U.S. Cl.**  
USPC ..... **D13/182**

The ornamental design for an inlet liner for substrate processing apparatus, as shown and described.

(58) **Field of Classification Search**  
USPC ..... D13/182; 118/50, 715, 728, 500, 501, 118/504, 428, 505, 506, 723 R, 729, 719, 118/733, 309; 204/298.01, 298.11; 156/345.51, 916; D15/138  
CPC . C23C 16/00; C23C 16/4401; C23C 16/0245; C23C 14/564; C23F 1/02; C23F 1/04; C23F 1/38; B65B 1/04; B65B 3/04  
See application file for complete search history.

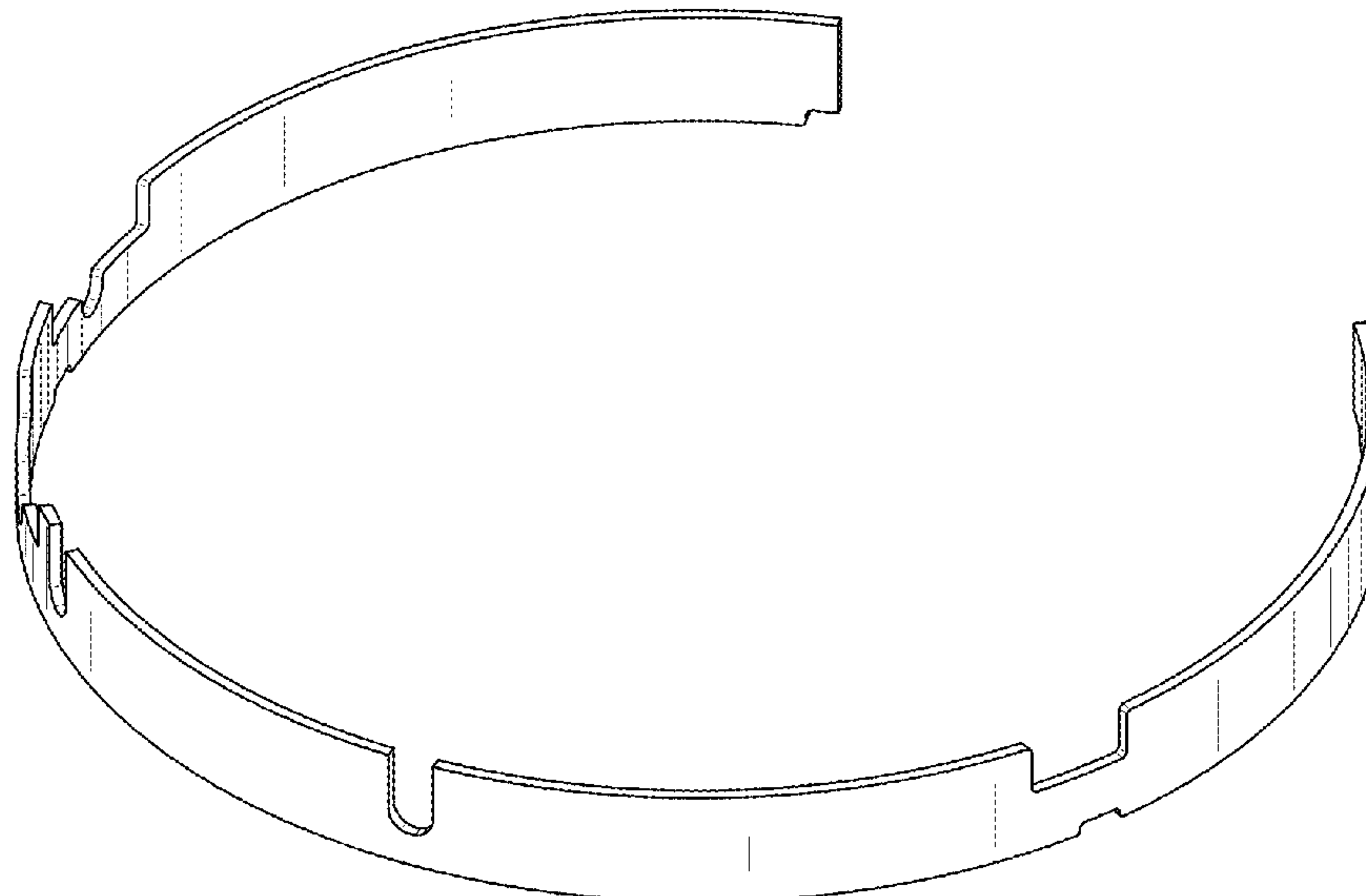
**DESCRIPTION**

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FIG. 1 is a front, top and right side perspective view of an inlet liner for substrate processing apparatus showing our new design;  
FIG. 2 is a front elevational view thereof;  
FIG. 3 is a rear elevational view thereof;  
FIG. 4 is a right side elevational view thereof;  
FIG. 5 is a left side elevational view thereof;  
FIG. 6 is a top plan view thereof;  
FIG. 7 is a bottom plan view thereof; and,  
FIG. 8 is a cross-sectional view take along line 8-8 in FIG. 6.  
The broken line arrow shows the association of the two sides of the cross sectional view in FIG. 8 and forms no part of the claimed design.

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**1 Claim, 3 Drawing Sheets**



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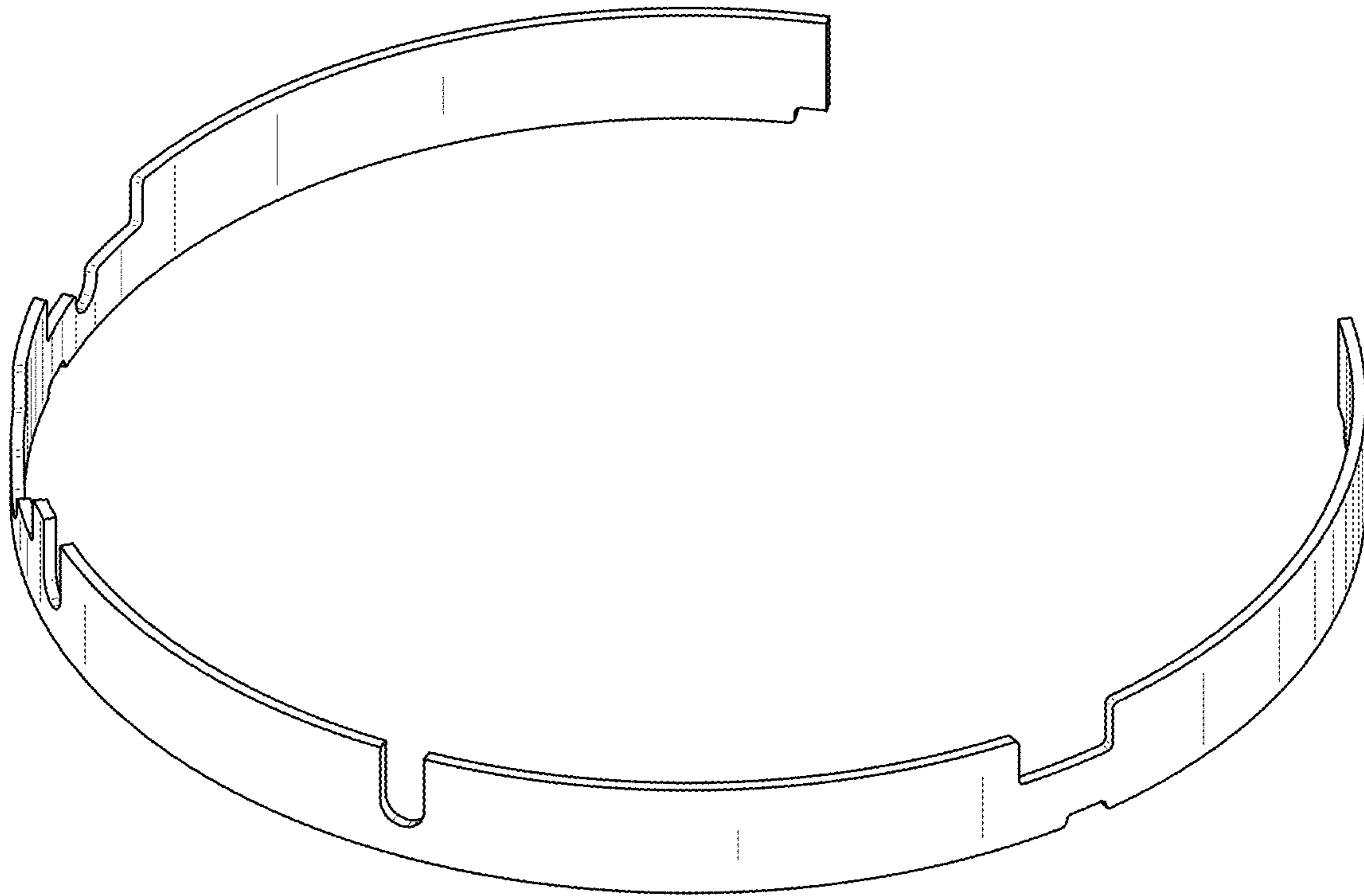
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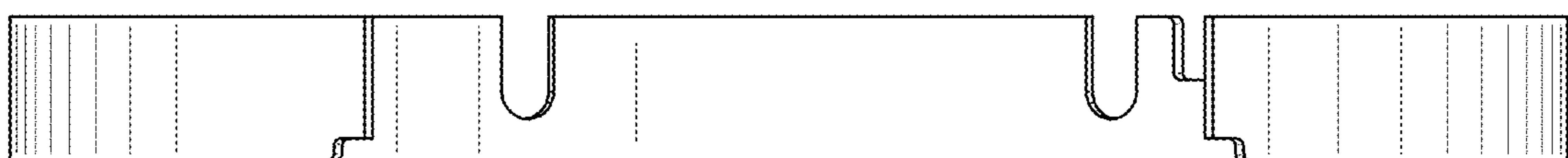
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*FIG. 1*



*FIG. 2*



*FIG. 3*

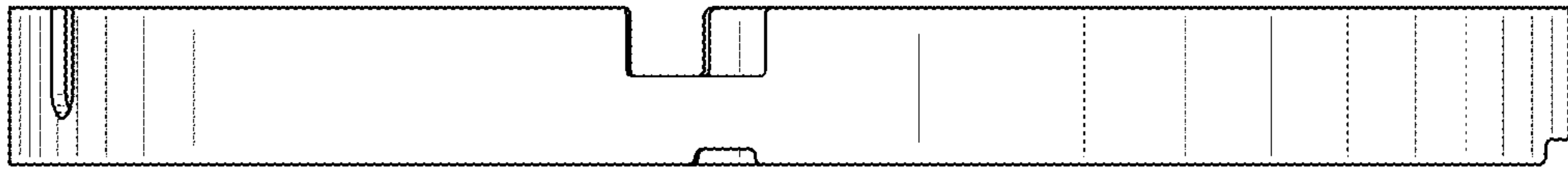


FIG. 4

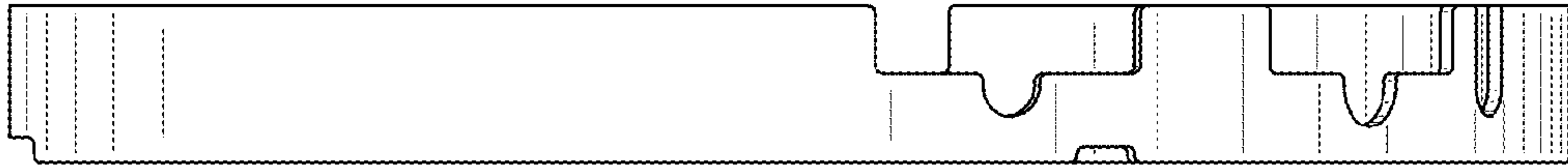


FIG. 5

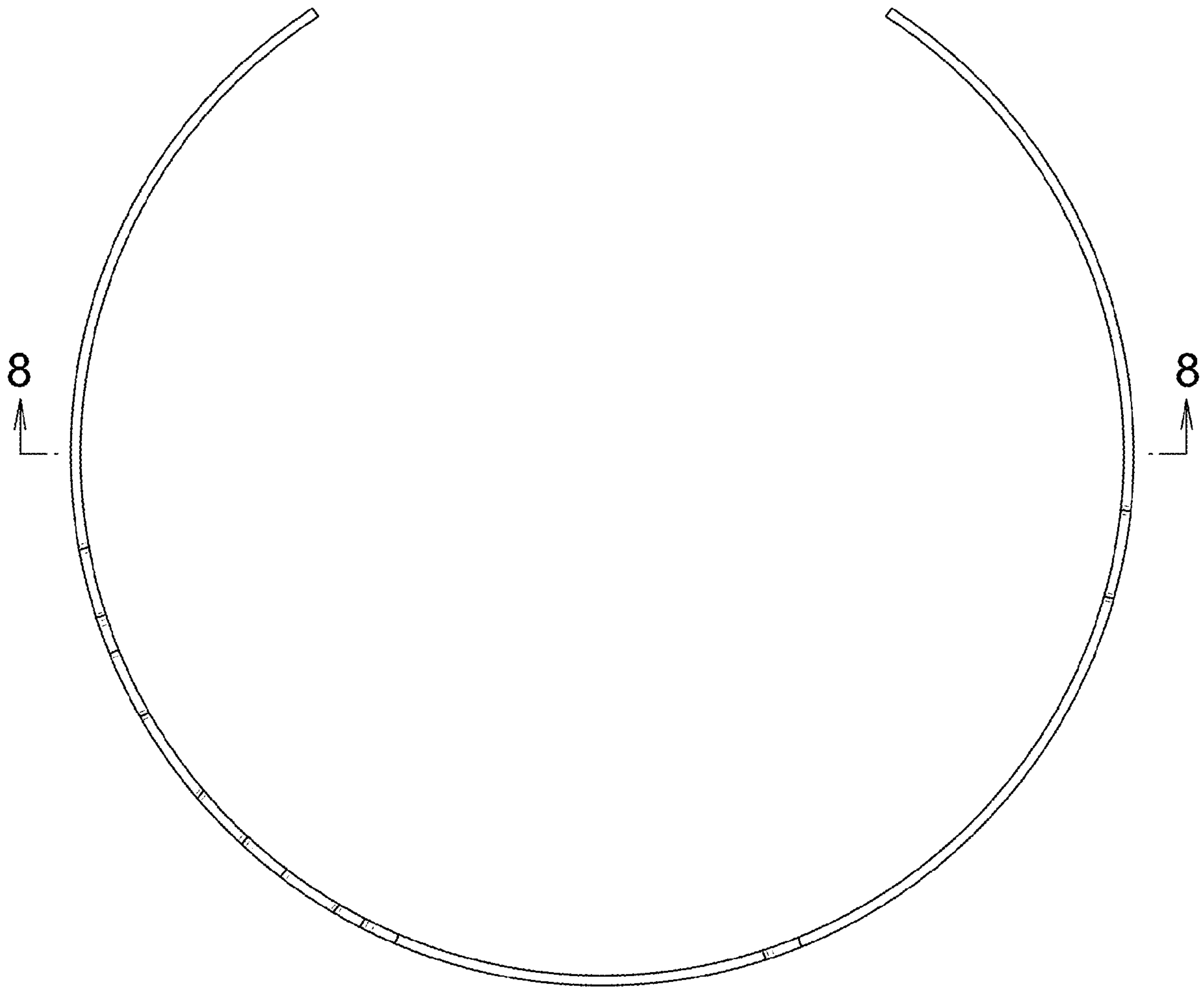
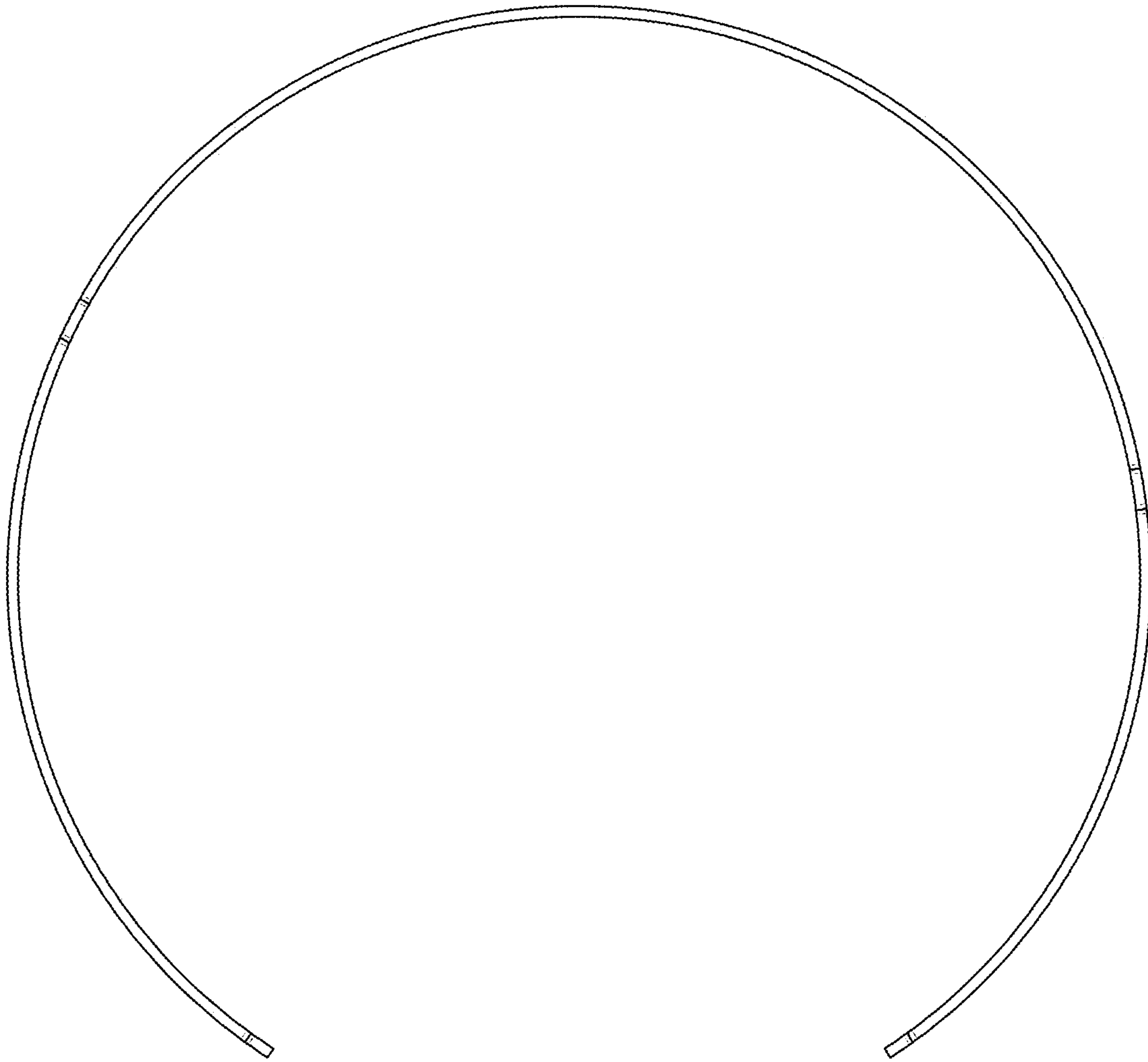


FIG. 6



*FIG. 7*



*FIG. 8*