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(12) **United States Design Patent**
Tamamura

(10) **Patent No.: US D924,917 S**
(45) **Date of Patent: ** Jul. 13, 2021**

(54) **DISPLAY PANEL OR SCREEN OR PORTION THEREOF WITH GRAPHICAL USER INTERFACE**

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(73) Assignee: **SONY CORPORATION**, Tokyo (JP)

(**) Term: **15 Years**

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Related U.S. Application Data

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(30) **Foreign Application Priority Data**

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Feb. 16, 2018 (JP) D2018-003226

(51) **LOC (13) Cl.** **14-04**
(52) **U.S. Cl.**
USPC **D14/486**
(58) **Field of Classification Search**
USPC D14/485–495
CPC G06F 3/048–04897
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D395,291	S *	6/1998	Mason	D14/490
D403,674	S *	1/1999	Arora	D14/490
D451,520	S	12/2001	Fukuda	
D454,355	S	3/2002	Yoshioka	
D473,566	S *	4/2003	Platz	D14/489

(Continued)

OTHER PUBLICATIONS

We today released "Neural Network Console—Cloud" open beta, by Yoshiyuki Kobayashi, dated Nov. 10, 2017, support.dl.sony.com [online]. Retrieved Jan. 16, 2021 from internet <URL:https://support.dl.sony.com/blogs/news/we-today-released-neural-network-console-cloud-open-beta/> (Year: 2017).*

(Continued)

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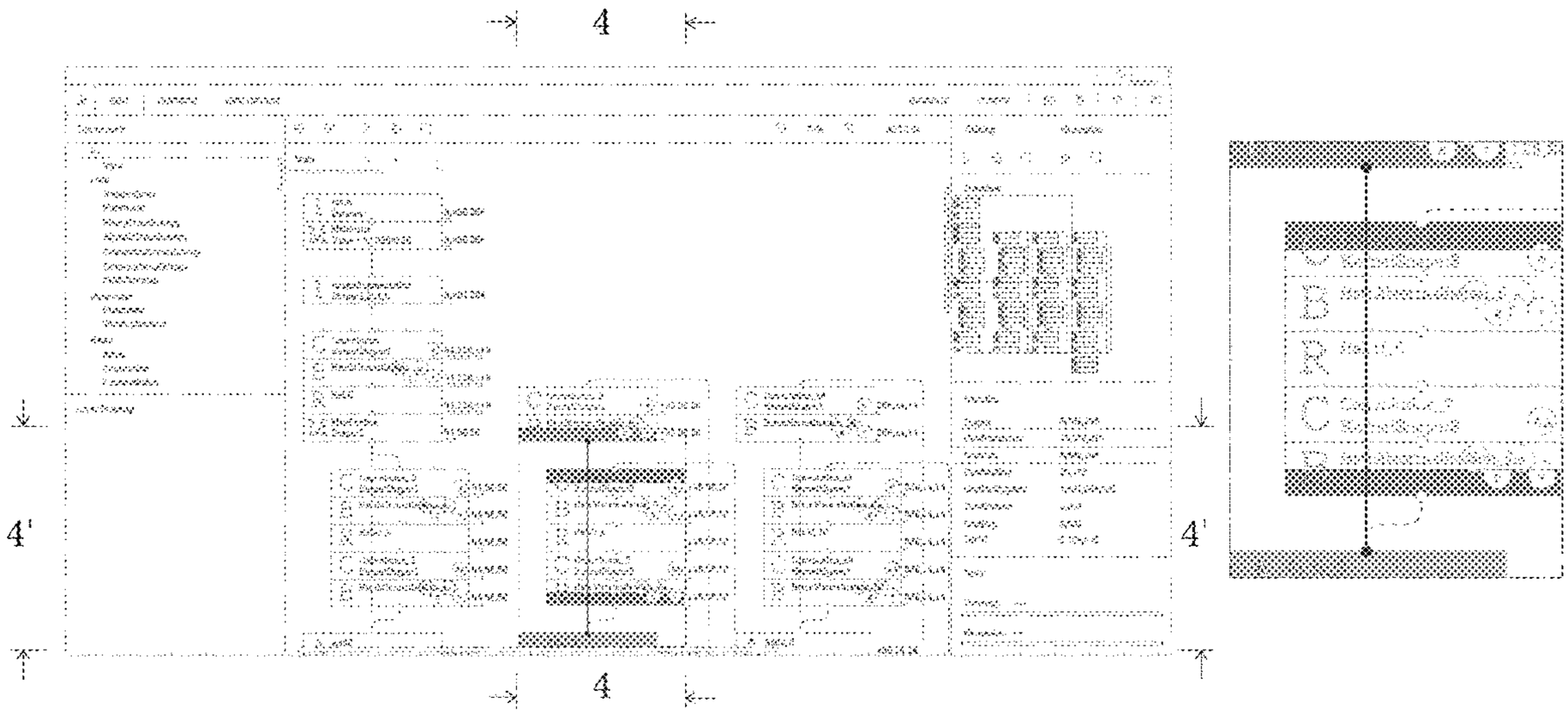
(57) **CLAIM**

The ornamental design for a display panel or screen or portion thereof with graphical user interface, as shown and described.

DESCRIPTION

FIG. **1** is a front view of a first embodiment of a display panel or screen or portion thereof with graphical user interface showing my new design;
FIG. **2** is a second embodiment thereof; and
FIG. **3** is a third embodiment thereof.
FIG. **4** is the claimed portion of FIG. **1** enlarged along the lines **4-4** and **4'-4'** for clarity of illustration;
FIG. **5** is the claimed portion of FIG. **2** enlarged along the lines **5-5** and **5'-5'** for clarity of illustration; and,
FIG. **6** is the claimed portion of FIG. **3** enlarged along the lines **6-6** and **6'-6'** for clarity of illustration.
The outermost even-dash broken lines in FIGS. **1-3** and FIGS. **4-6** illustrate a display panel or screen or portion thereof. The remaining even-dash broken lines in all views, including all text and numerals, illustrate portions of the graphical user interface. The dot-dash broken lines define the boundaries of the claimed design. All of the lines outside of the dot-dash broken line in FIGS. **1-3** and FIGS. **4-6** are illustrated in broken lines. None of the broken lines form part of the claimed design.

1 Claim, 4 Drawing Sheets



(56) **References Cited**

U.S. PATENT DOCUMENTS

D484,142	S	*	12/2003	Bungert	D14/489
D668,669	S	*	10/2012	Vance	D14/487
D681,656	S	*	5/2013	Lawrence	D14/486
D693,829	S	*	11/2013	Edwards	D14/485
D694,764	S	*	12/2013	Talbot	D14/485
D694,768	S	*	12/2013	Edwards	D14/485
D695,751	S	*	12/2013	Edwards	D14/485
D695,752	S	*	12/2013	Edwards	D14/485
D695,753	S	*	12/2013	Edwards	D14/485
D708,193	S		7/2014	Agnew et al.	
D710,863	S		8/2014	Agnew	
D711,898	S		8/2014	Edman	
D727,246	S		4/2015	Sachtleben et al.	
D735,732	S	*	8/2015	Nezhad	D14/485
D743,977	S		11/2015	dela Cruz et al.	
D751,106	S	*	3/2016	Chetan	D14/486
D760,272	S	*	6/2016	Li	D14/487
D765,122	S	*	8/2016	Walters	H04L 67/1046 D14/486
D767,585	S		9/2016	Qu	
D769,293	S	*	10/2016	Colbeck	D14/486
D770,470	S		11/2016	Jin	
D771,109	S		11/2016	Broughton et al.	
D779,526	S	*	2/2017	Volovik	D14/486
D805,526	S	*	12/2017	Ternoey	D14/485
D810,094	S		2/2018	Matsuda	
D813,882	S	*	3/2018	Shivaji-Rao	D14/485
D821,413	S		6/2018	Zukerman et al.	
D824,922	S	*	8/2018	McGovern	D14/485
D826,268	S		8/2018	Cabot	
D828,391	S		9/2018	Cabot	
D833,463	S	*	11/2018	Shivaji-Rao	D14/485
D835,636	S		12/2018	Yao	
D840,412	S		2/2019	Donini et al.	
D841,687	S	*	2/2019	Muller	D14/488
D845,966	S	*	4/2019	Shivaji-Rao	D14/485
D857,721	S	*	8/2019	Sanz	D14/486
D881,912	S	*	4/2020	Tamamura	D14/486
D887,432	S	*	6/2020	White	D14/486
D894,914	S	*	9/2020	Bickel	D14/485
D895,654	S	*	9/2020	Wills	D14/486
D902,944	S	*	11/2020	Garland	D14/485
D912,077	S	*	3/2021	Digioia	D14/486
2008/0120574	A1	*	5/2008	Heredia	G06Q 10/06 715/835
2014/0282177	A1	*	9/2014	Wang	G16B 45/00 715/771
2017/0242551	A1	*	8/2017	Friedland	G06F 3/0481
2019/0050925	A1		2/2019	Hodge et al.	

OTHER PUBLICATIONS

Sony Neural Network Console, dated Sep. 18, 2018, sony.co.jp [online]. Retrieved Jun. 23, 2019 from internet <URL:https://www.sony.co.jp/SonyInfo/News/Press/201809/18-076/index.html> (Year: 2018).

* cited by examiner

FIG.1

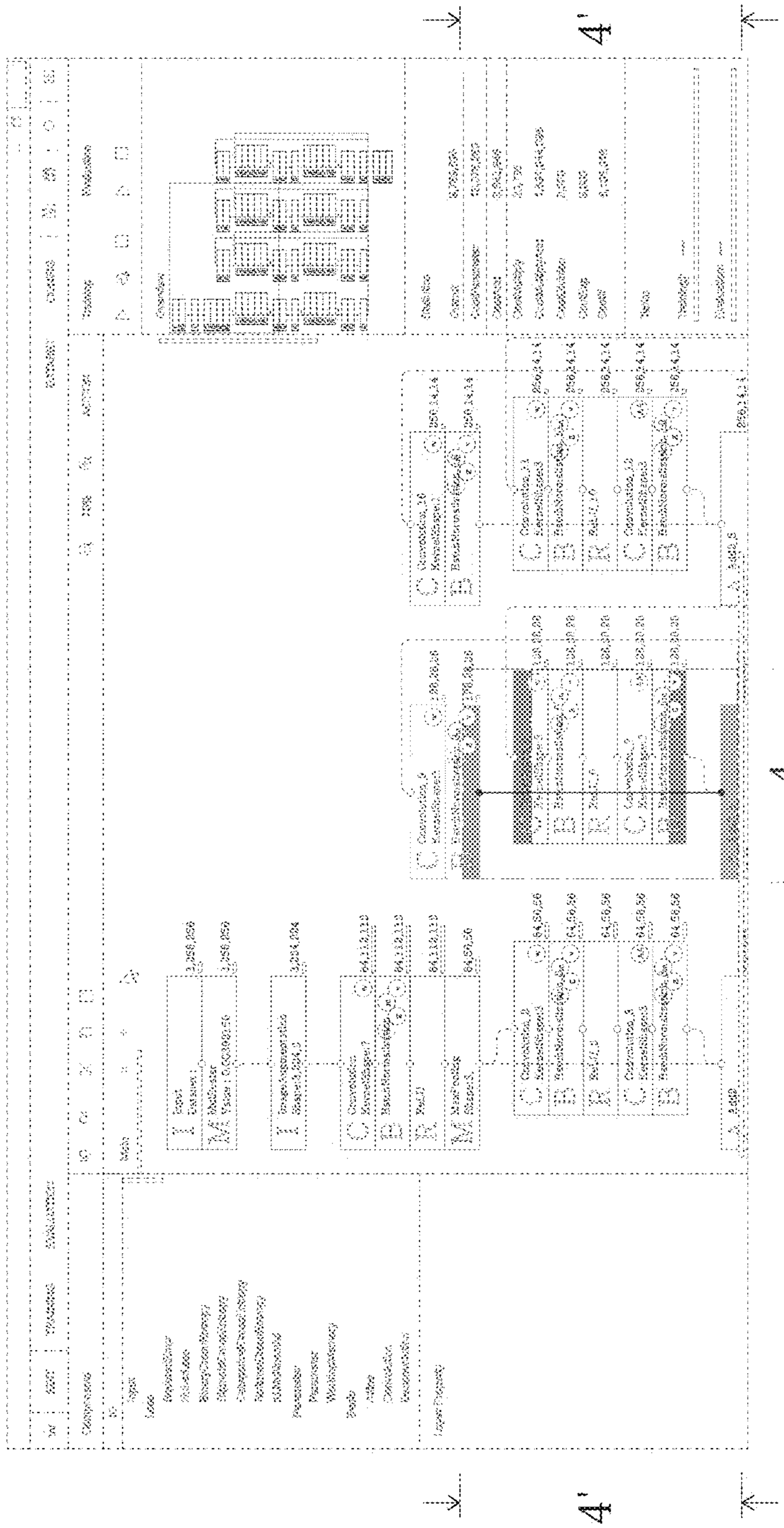


FIG.2

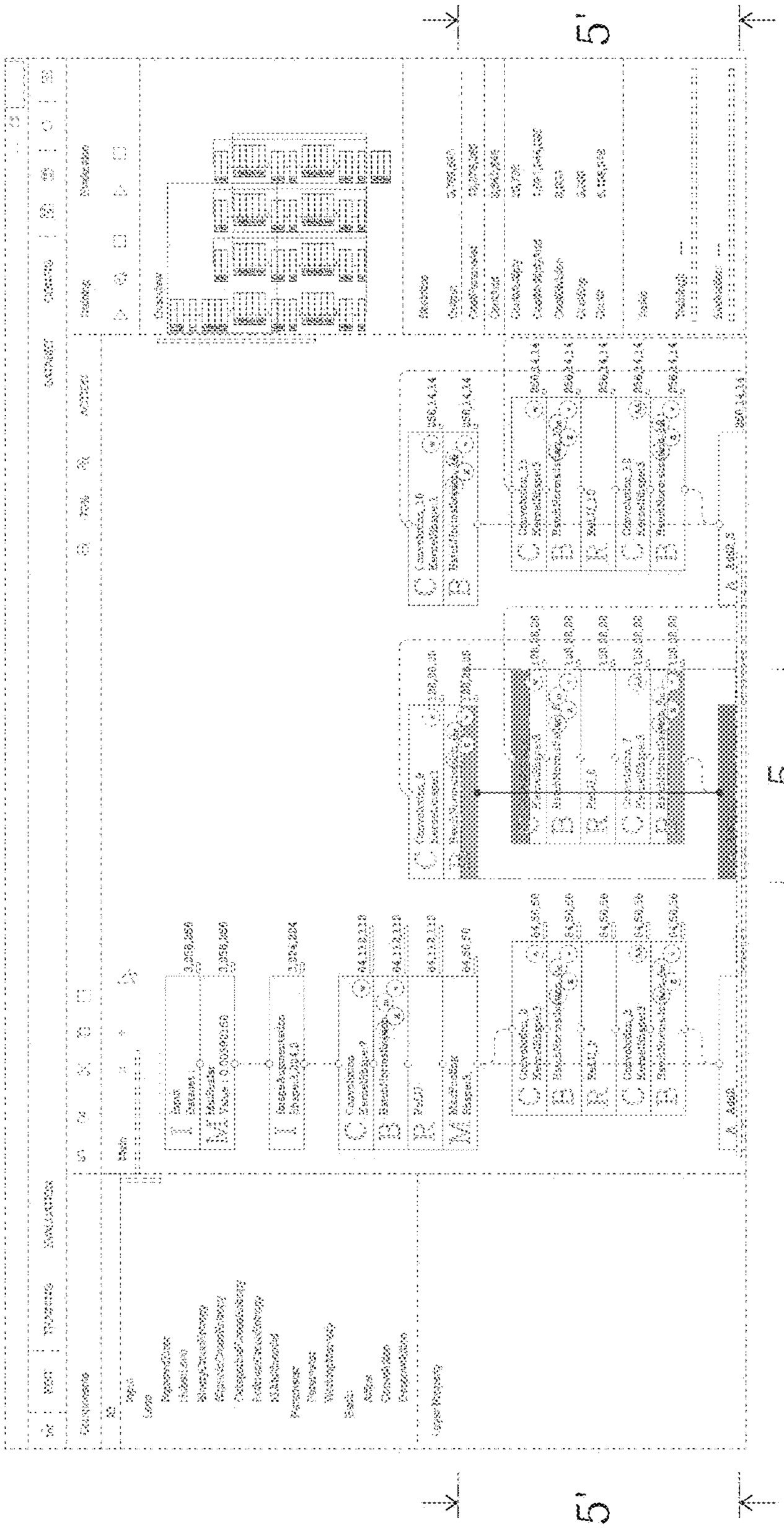


FIG.3

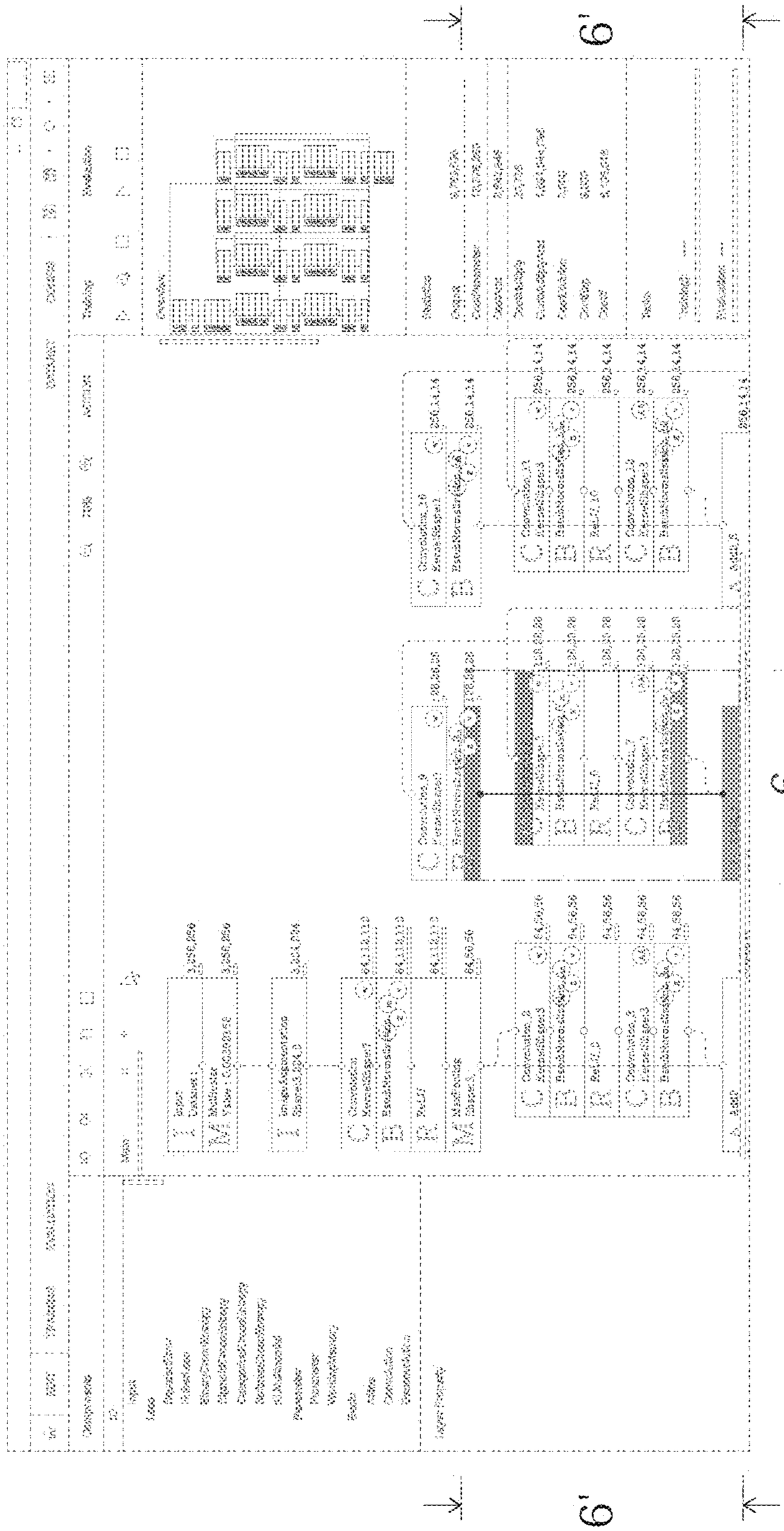


FIG.4

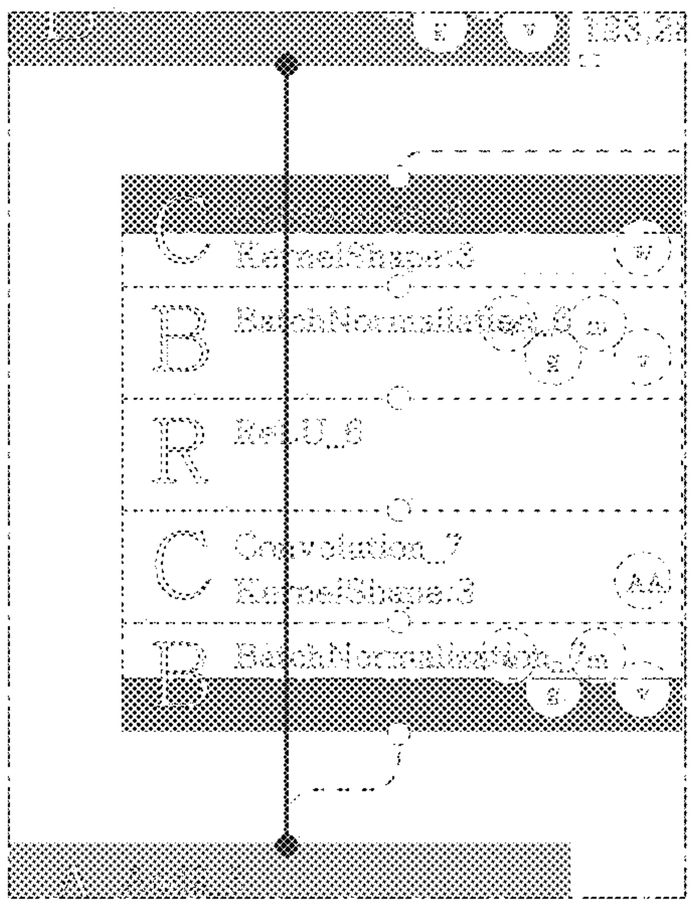


FIG.5

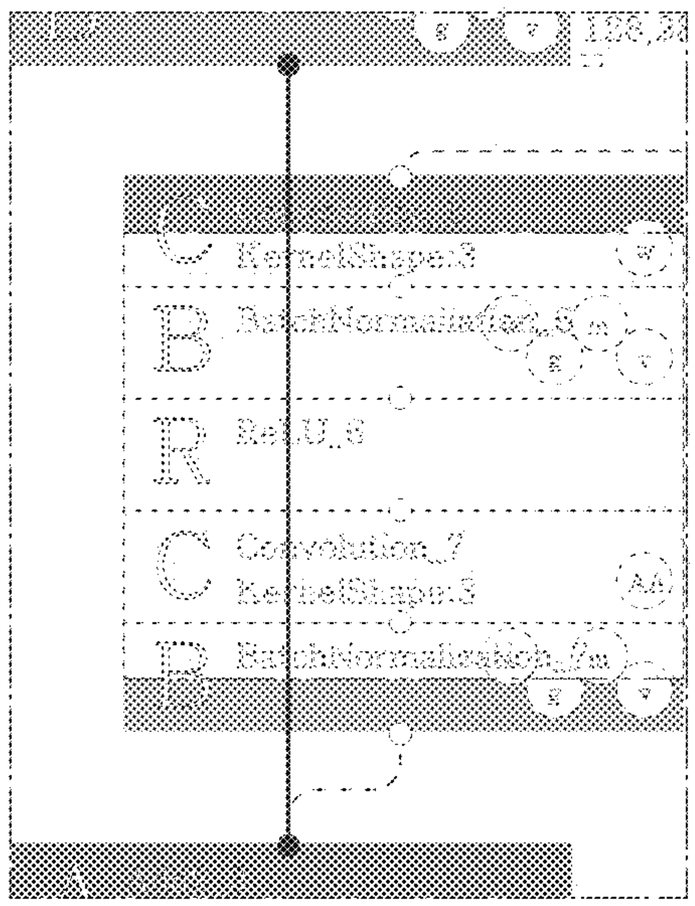


FIG.6