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(12) **United States Design Patent**  
**Li et al.**

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- (54) **GATEWAY CONTROLLER**
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- (73) Assignee: **HONEYWELL INTERNATIONAL INC.**, Charlotte, NC (US)
- (\*\*) Term: **15 Years**
- (21) Appl. No.: **29/700,121**
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- (51) **LOC (13) Cl.** ..... **14-03**
- (52) **U.S. Cl.**  
USPC ..... **D14/242**
- (58) **Field of Classification Search**  
USPC .... D14/240, 242, 357, 358, 140–140.9, 155, D14/125, 137, 139, 243, 348, 349, 351, D14/354, 355, 433  
CPC ..... H04L 12/00; H03K 17/00; H04W 88/00; H04W 88/005; H04W 88/02; H04W 88/08; H04W 88/085; H04W 88/10; H04W 88/12; H04W 88/14; H04B 1/38  
See application file for complete search history.

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(Continued)

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(57) **CLAIM**

The ornamental design for a gateway controller, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of a first embodiment of a gateway controller;  
 FIG. 2 is a front elevation view of the gateway controller of FIG. 1;  
 FIG. 3 is a left side view of the gateway controller of FIG. 1;  
 FIG. 4 is a right side view of the gateway controller of FIG. 1;  
 FIG. 5 is a top plan view of the gateway controller of FIG. 1;  
 FIG. 6 is a bottom plan view of the gateway controller of FIG. 1;  
 FIG. 7 is a perspective view of a second embodiment of a gateway controller; and,  
 FIG. 8 is a front elevation view of the gateway controller of FIG. 7.

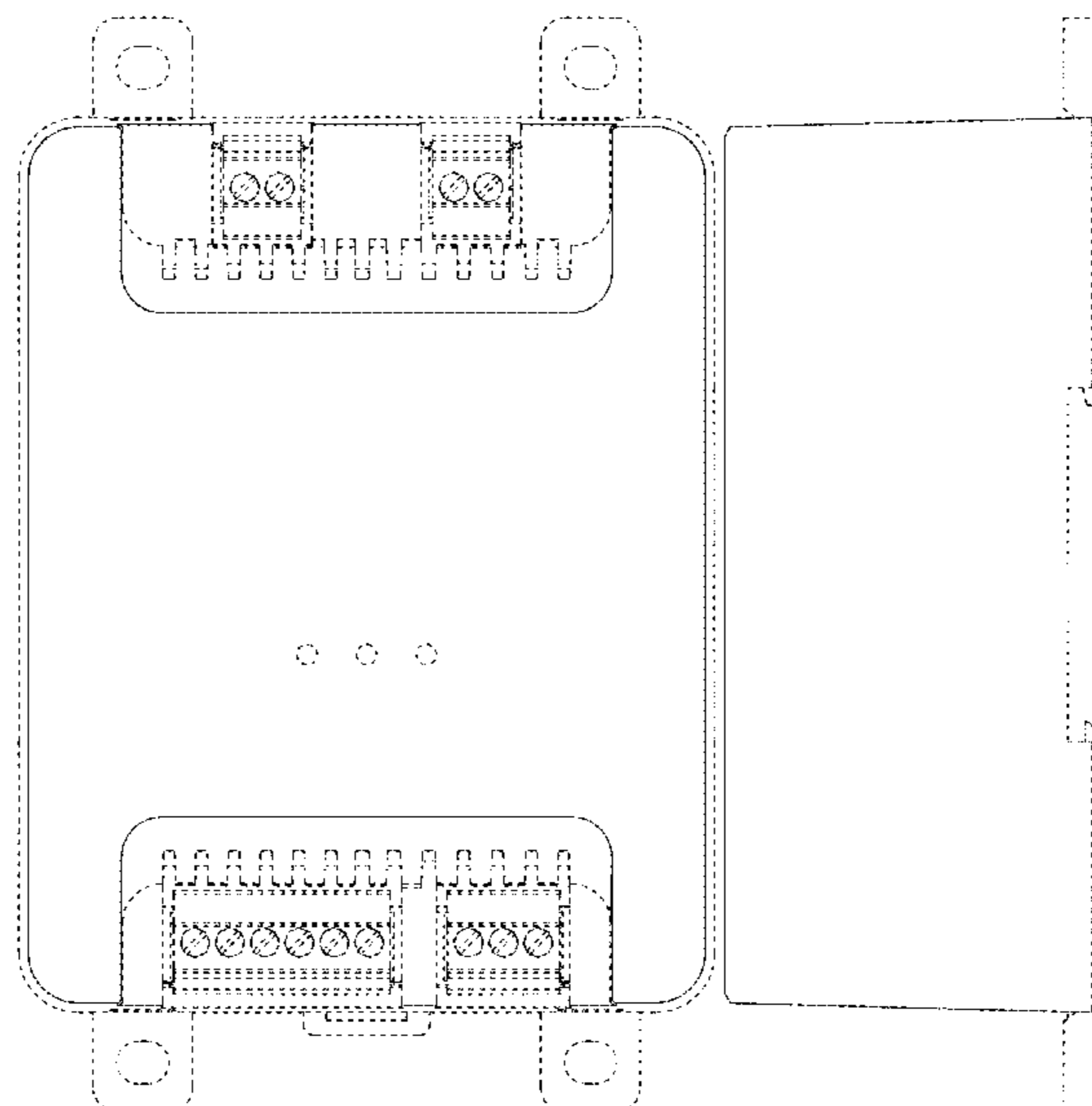
The broken lines in the figures are included for the purpose of illustrating portions of the gateway controller that form no part of the claimed design.

**1 Claim, 8 Drawing Sheets**

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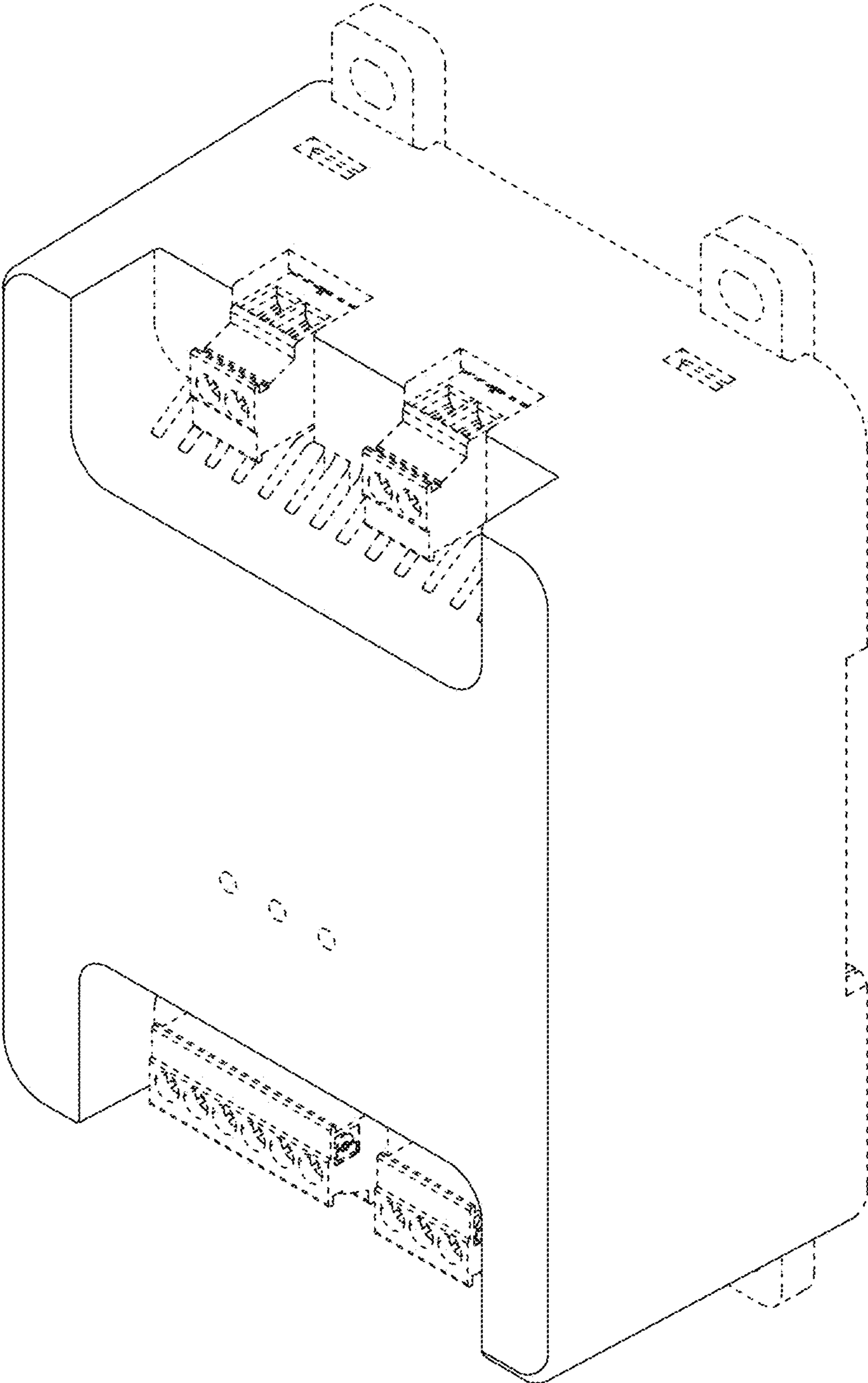


FIG. 1

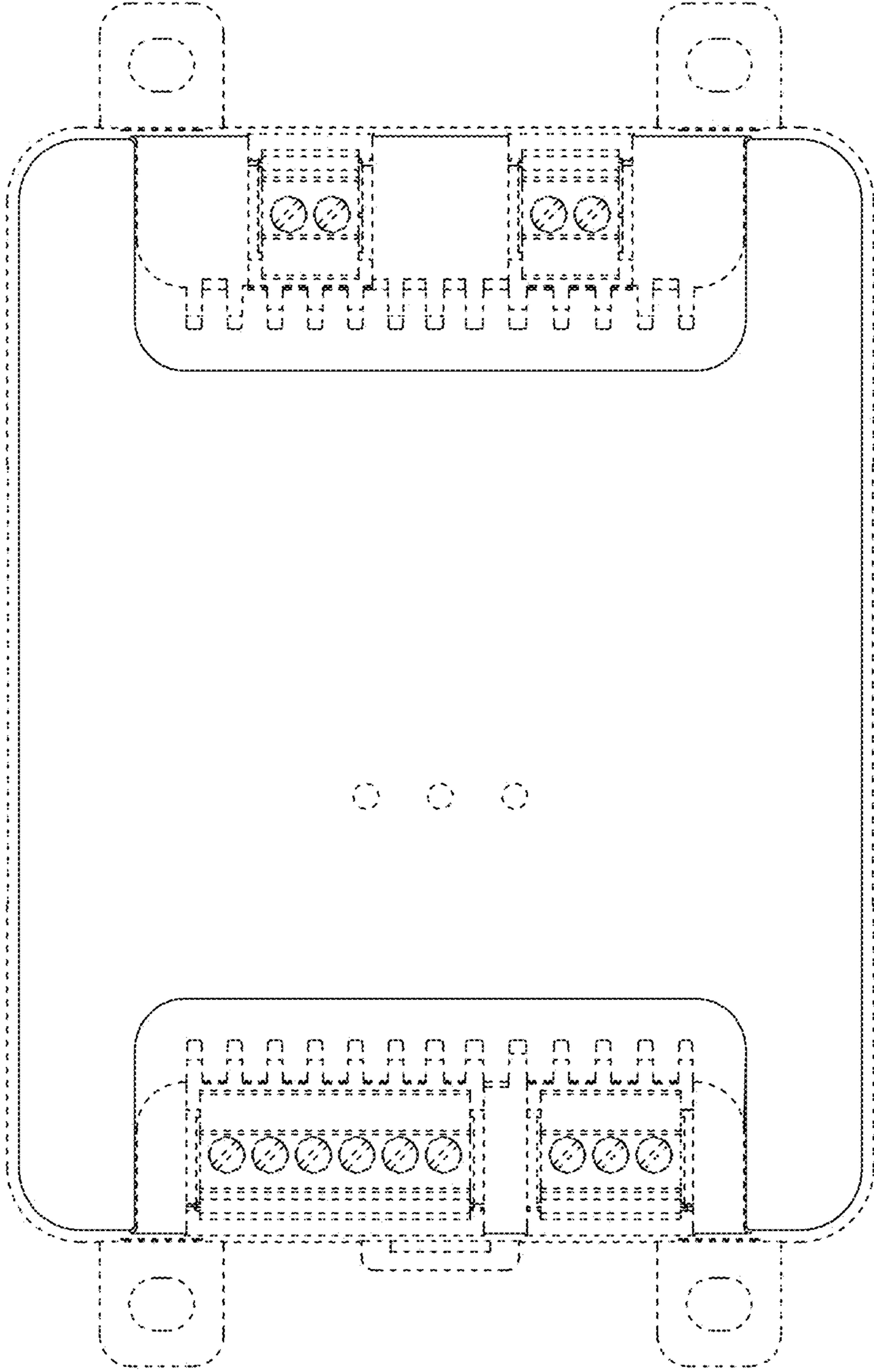


FIG. 2

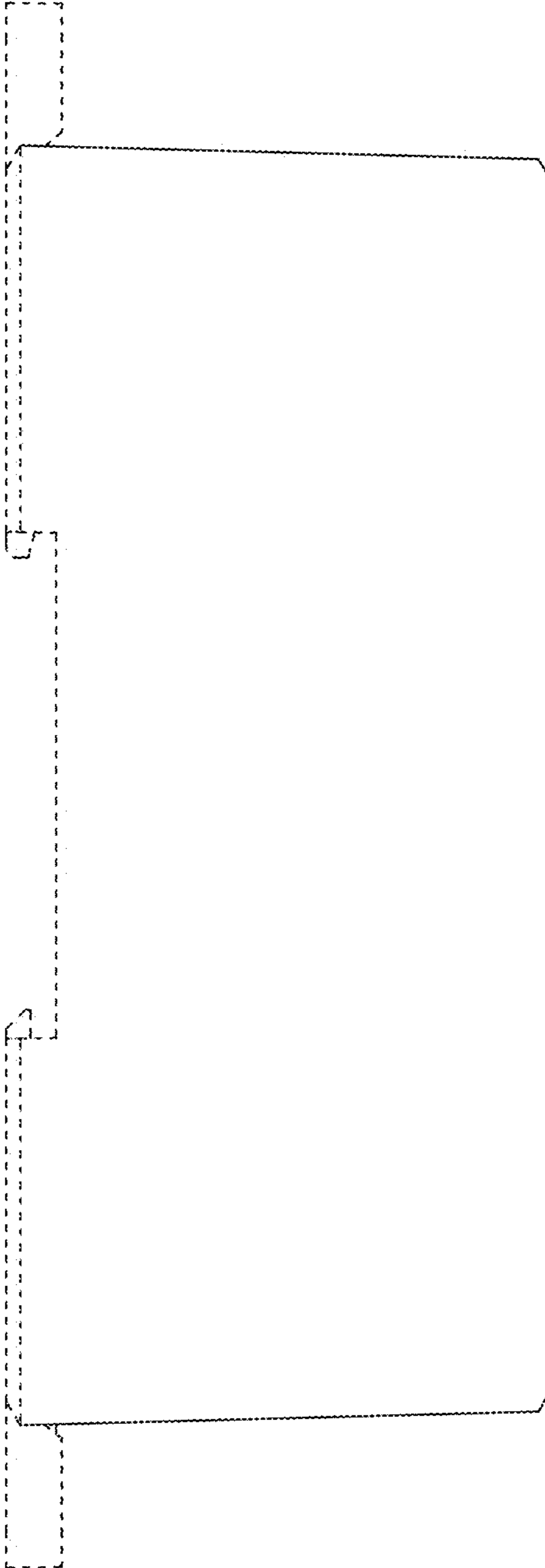


FIG. 3

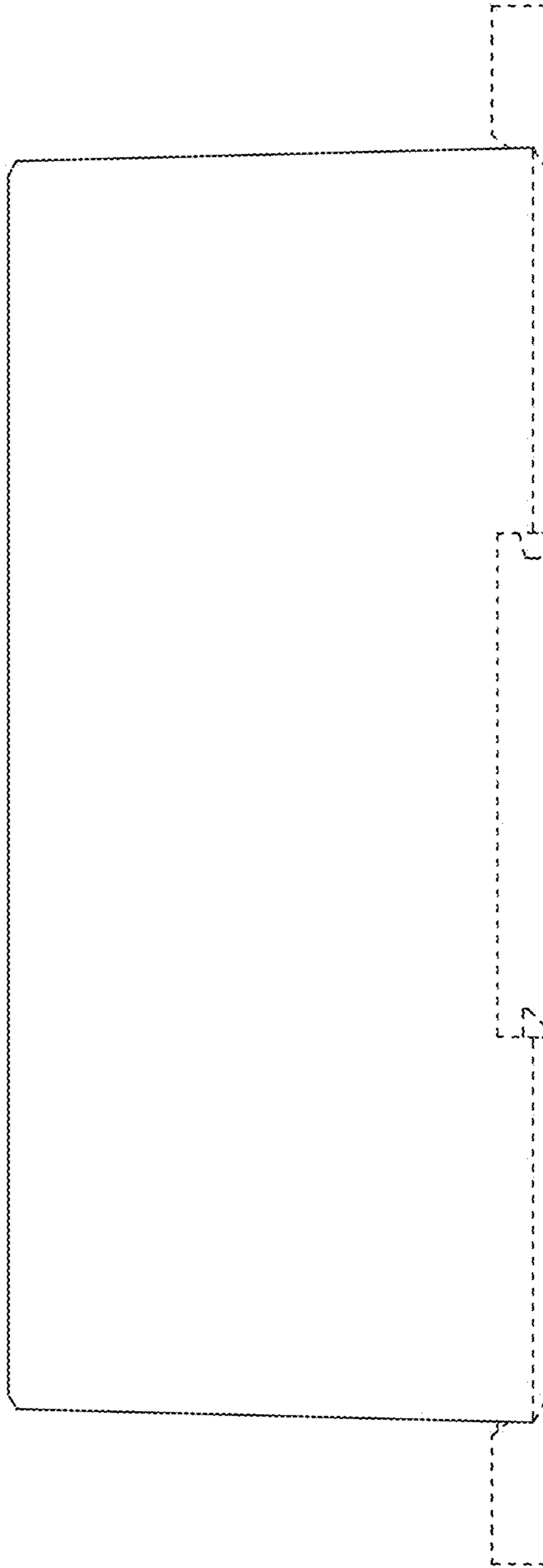


FIG. 4

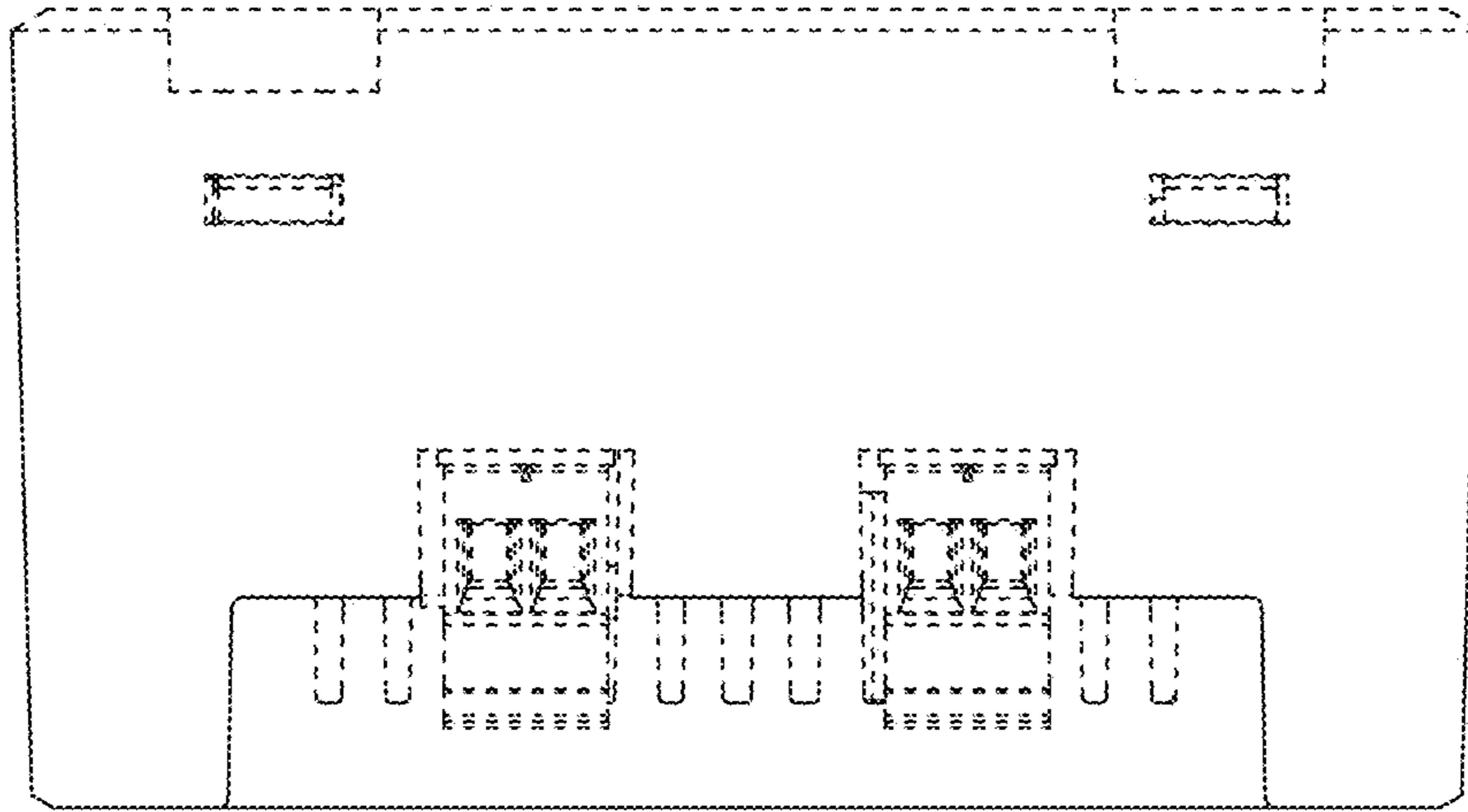


FIG. 5

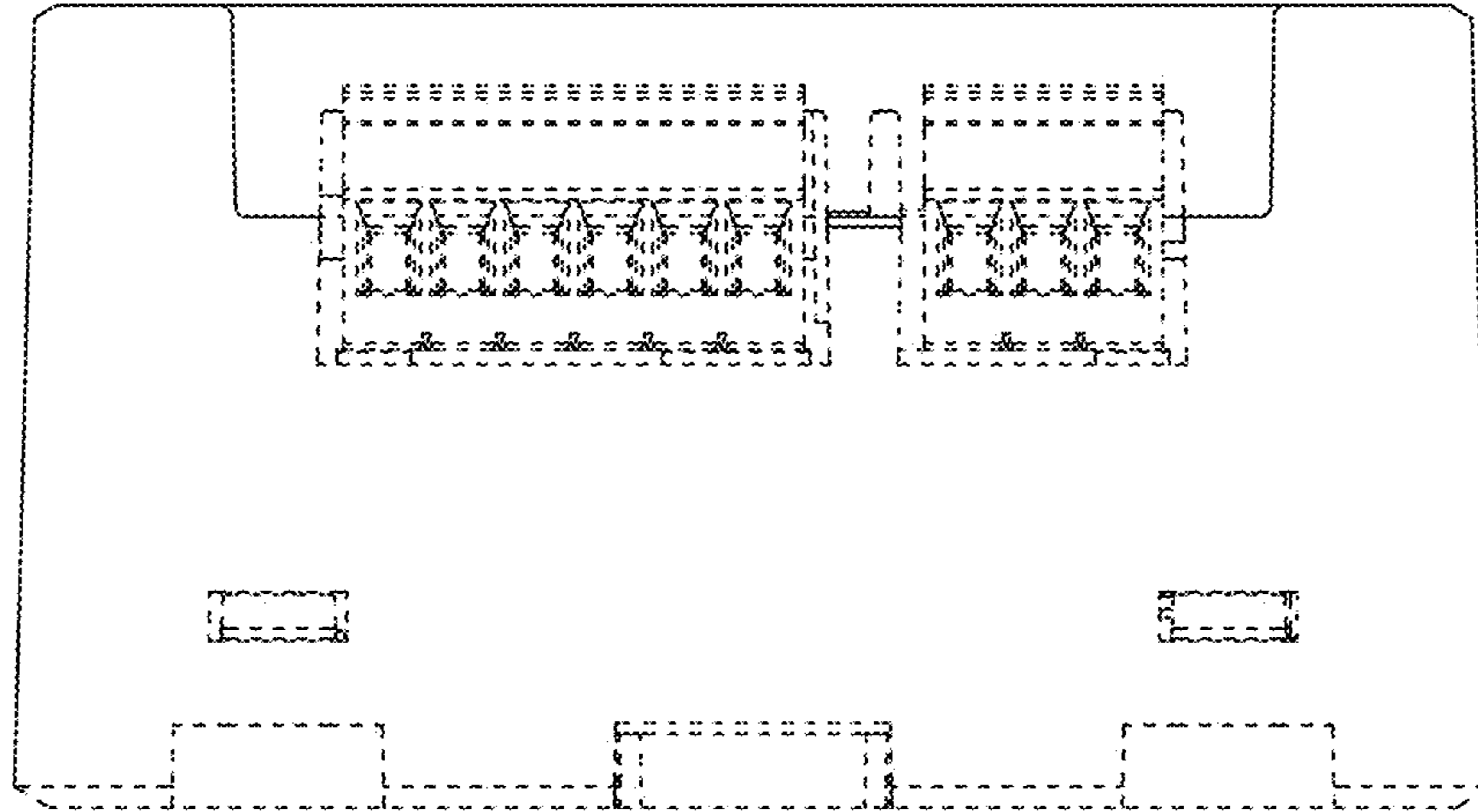


FIG. 6



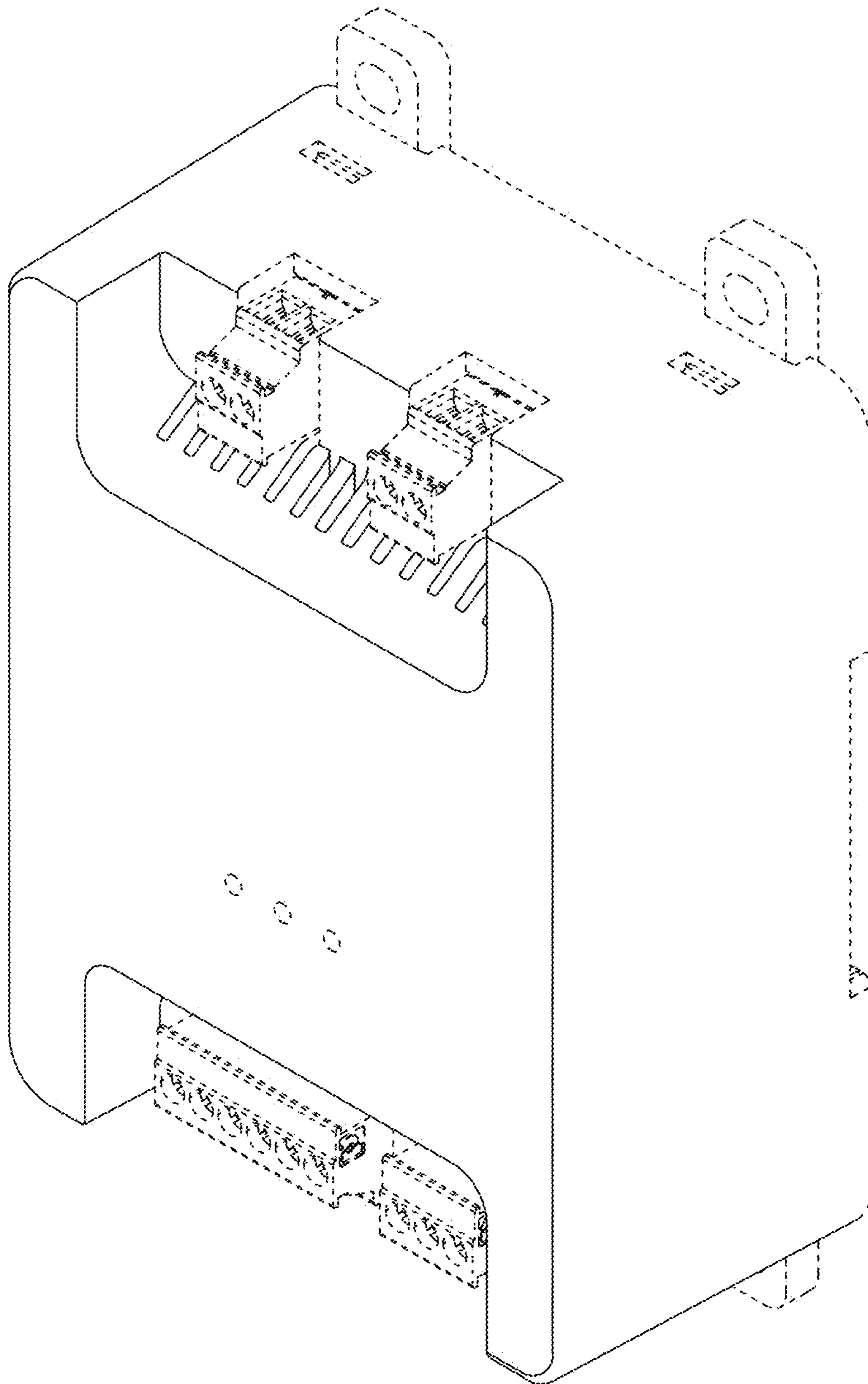


FIG. 7

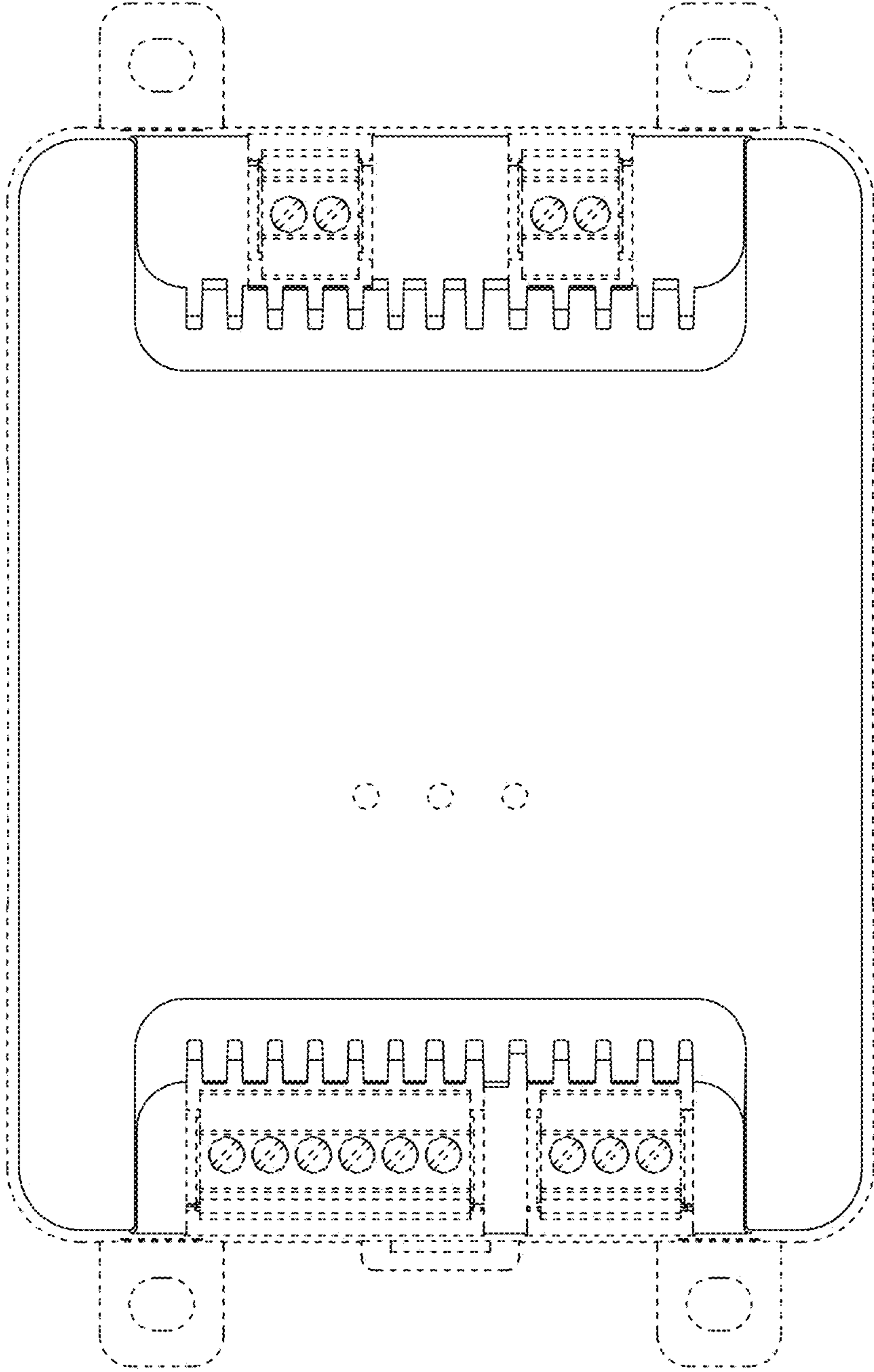


FIG. 8