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(12) **United States Design Patent**
Bryant et al.

(10) **Patent No.:** **US D921,328 S**
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- (54) **WAFER BAR**
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- (73) Assignee: **General Mills, Inc.**, Minneapolis, MN (US)
- (**) Term: **15 Years**
- (21) Appl. No.: **29/670,883**
- (22) Filed: **Nov. 20, 2018**
- (51) **LOC (13) Cl.** **01-01**
- (52) **U.S. Cl.**
USPC **D1/128; D1/101**
- (58) **Field of Classification Search**
USPC D1/100–130, 199; 426/94, 104, 138, 426/144, 391, 549, 808, 143, 559, 560; D7/388, 359, 409; D6/606; D25/138, D25/38.1, 100, 114; 119/710; D9/456; 428/182; 521/79; 72/301
CPC A21D 13/0029; A21D 13/0074; A21D 13/0022; A21D 13/0006; A23L 1/1641; A23L 1/005
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for wafer bar, as shown and described.

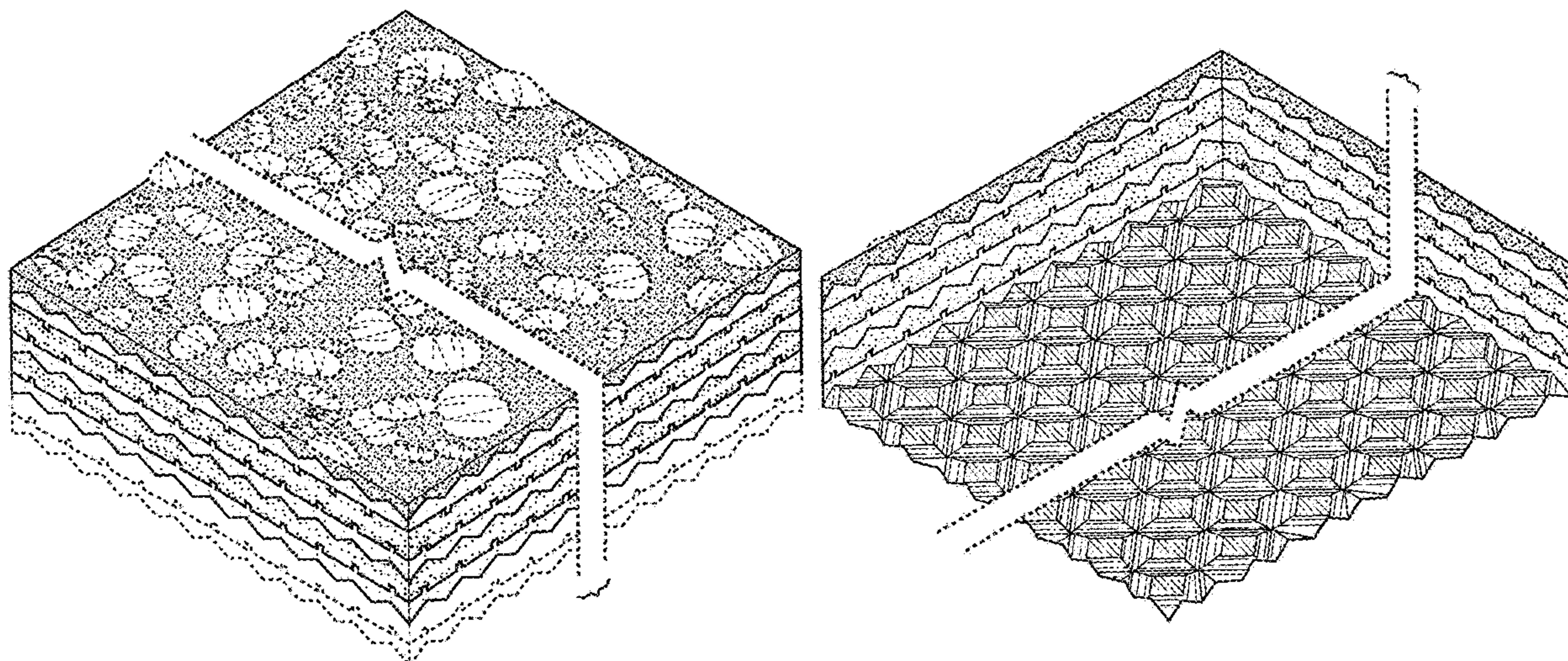
DESCRIPTION

FIG. 1 is an upper perspective view of a wafer bar according to our design;
 FIG. 2 is a front elevational view of the wafer bar;
 FIG. 3 is a rear elevational view of the wafer bar;
 FIG. 4 is a left side view of the wafer bar;
 FIG. 5 is a right side view the wafer bar;
 FIG. 6 is a top plan view of the wafer bar;
 FIG. 7 is a bottom plan view of the wafer bar; and,
 FIG. 8 is a lower perspective view of the wafer bar.

The broken lines depicting extra wafer layers in FIG. 1 are environment, while the remaining broken lines in the drawings illustrate portions of the wafer bar that form no part of the claimed design. The break lines shown in the middle portion of the wafer bar in FIGS. 1, 4-7 and 8 indicate that the appearance of any portion of the article between the break lines forms no part of the claimed design.

The differing degrees of stippled shading and the line shading in FIGS. 1-8 represent a contrasting appearance.

1 Claim, 5 Drawing Sheets



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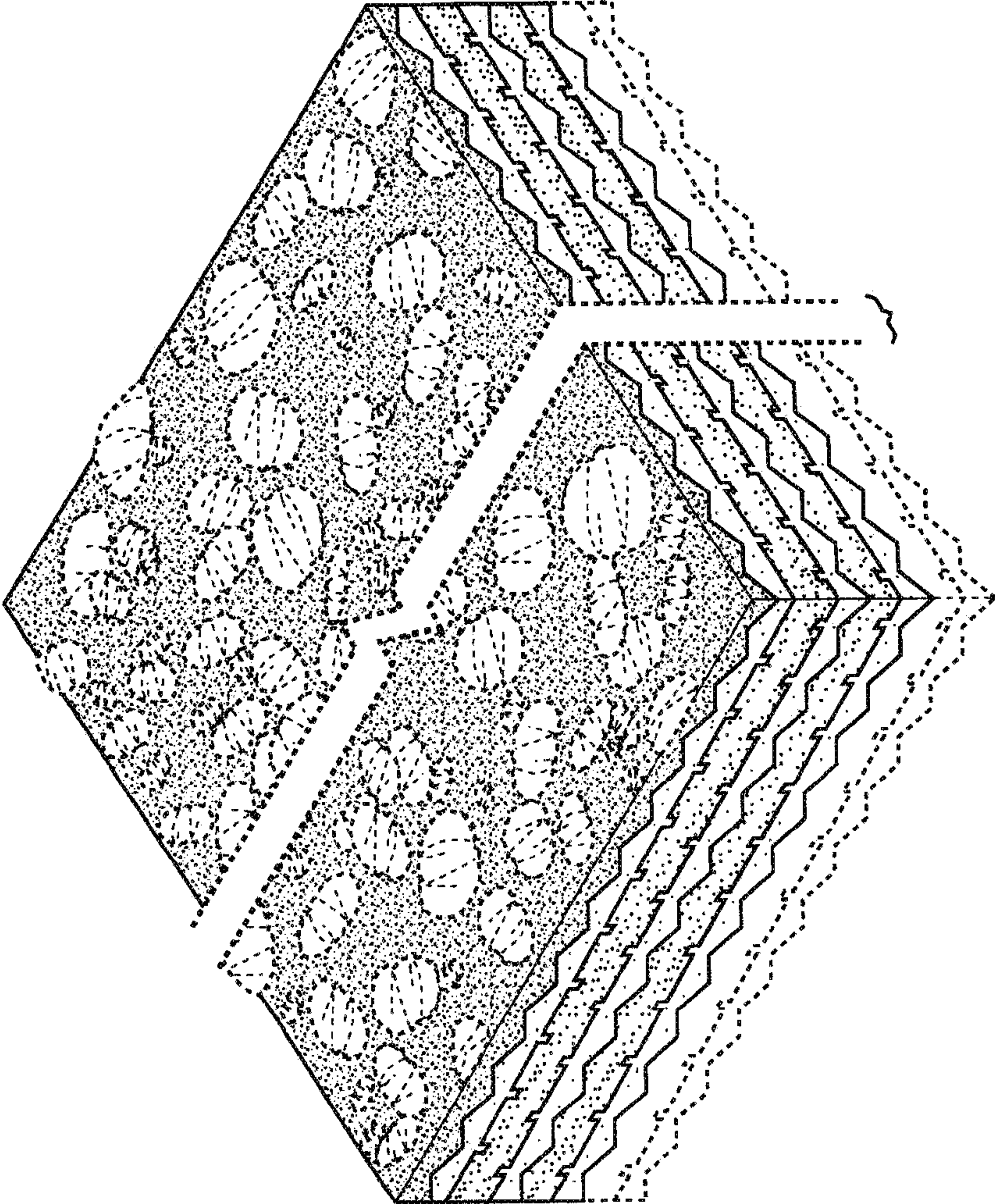


FIG. 1

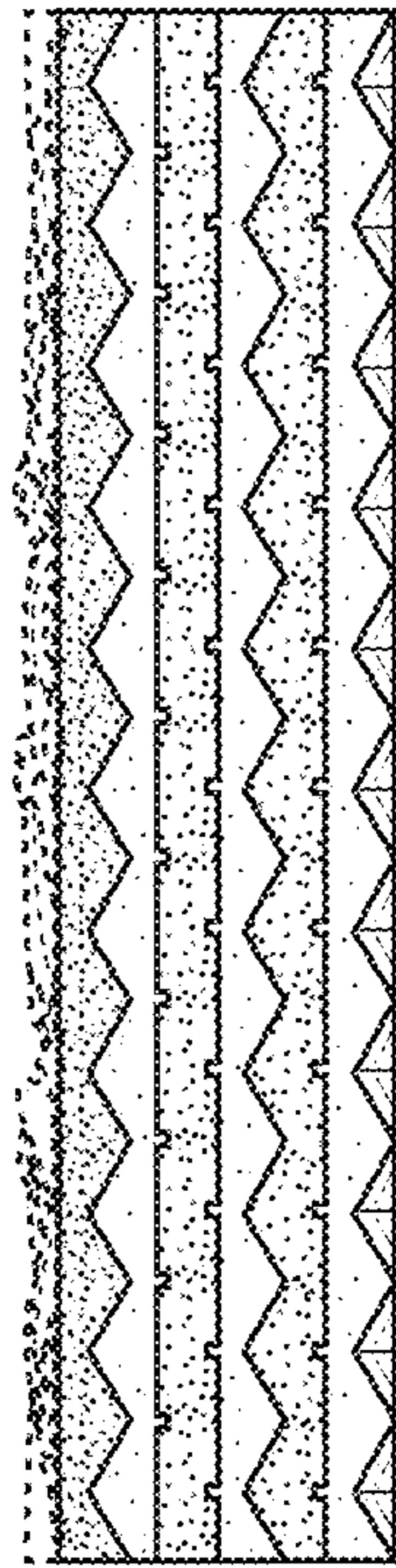


FIG. 2

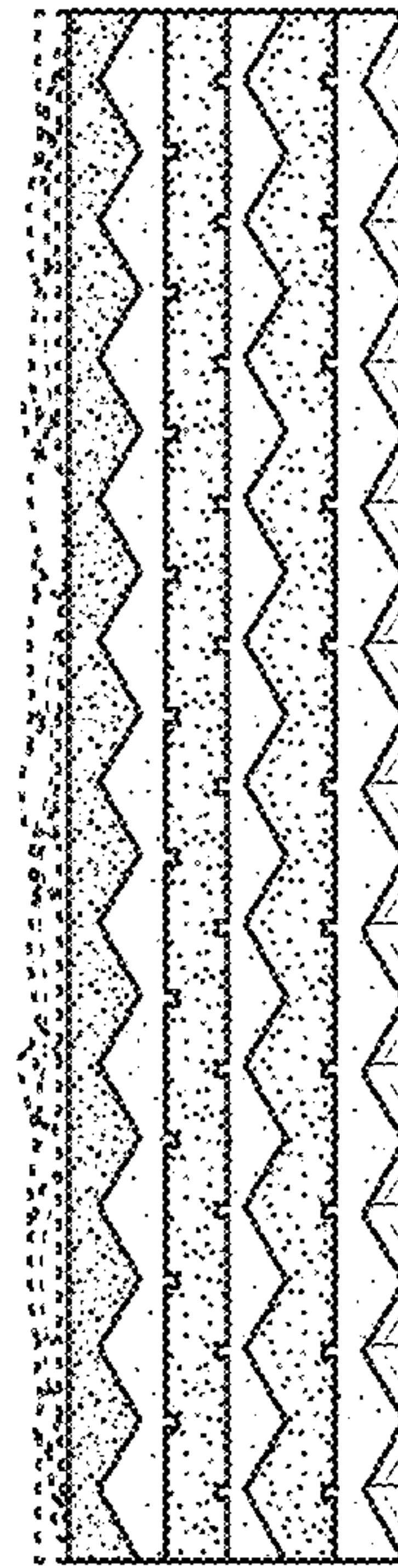


FIG. 3

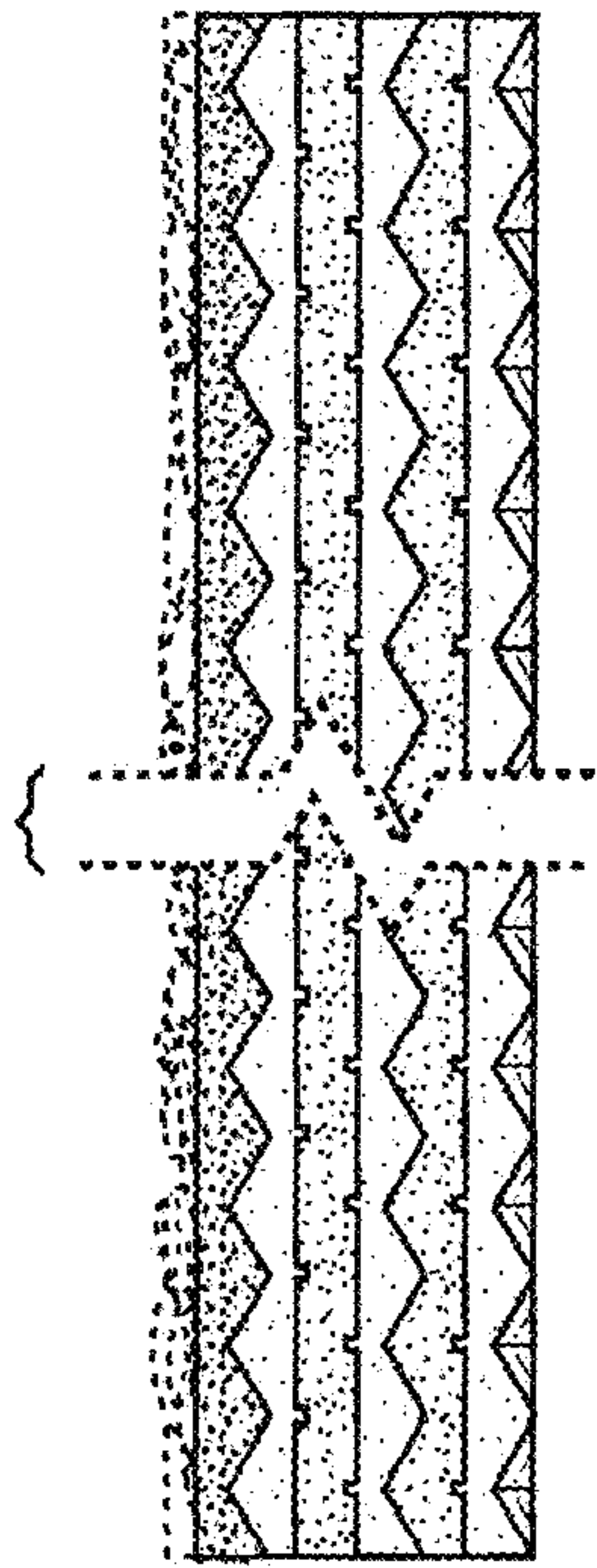


FIG. 4

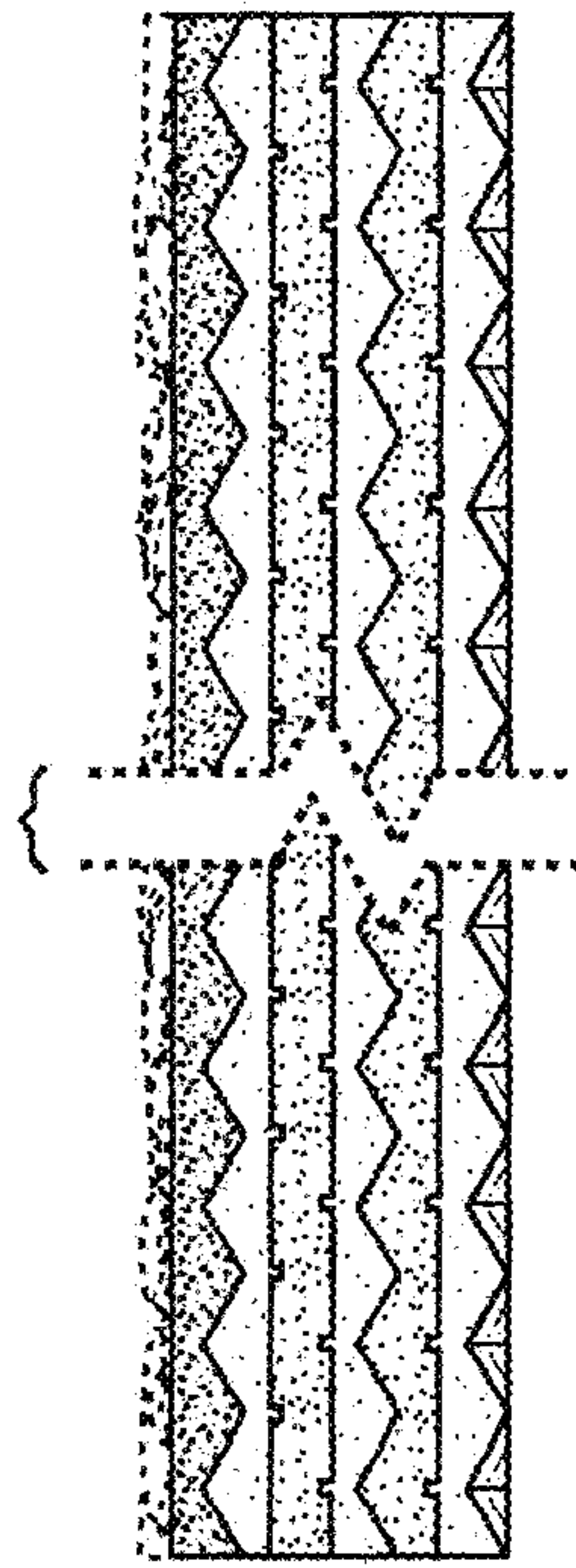


FIG. 5

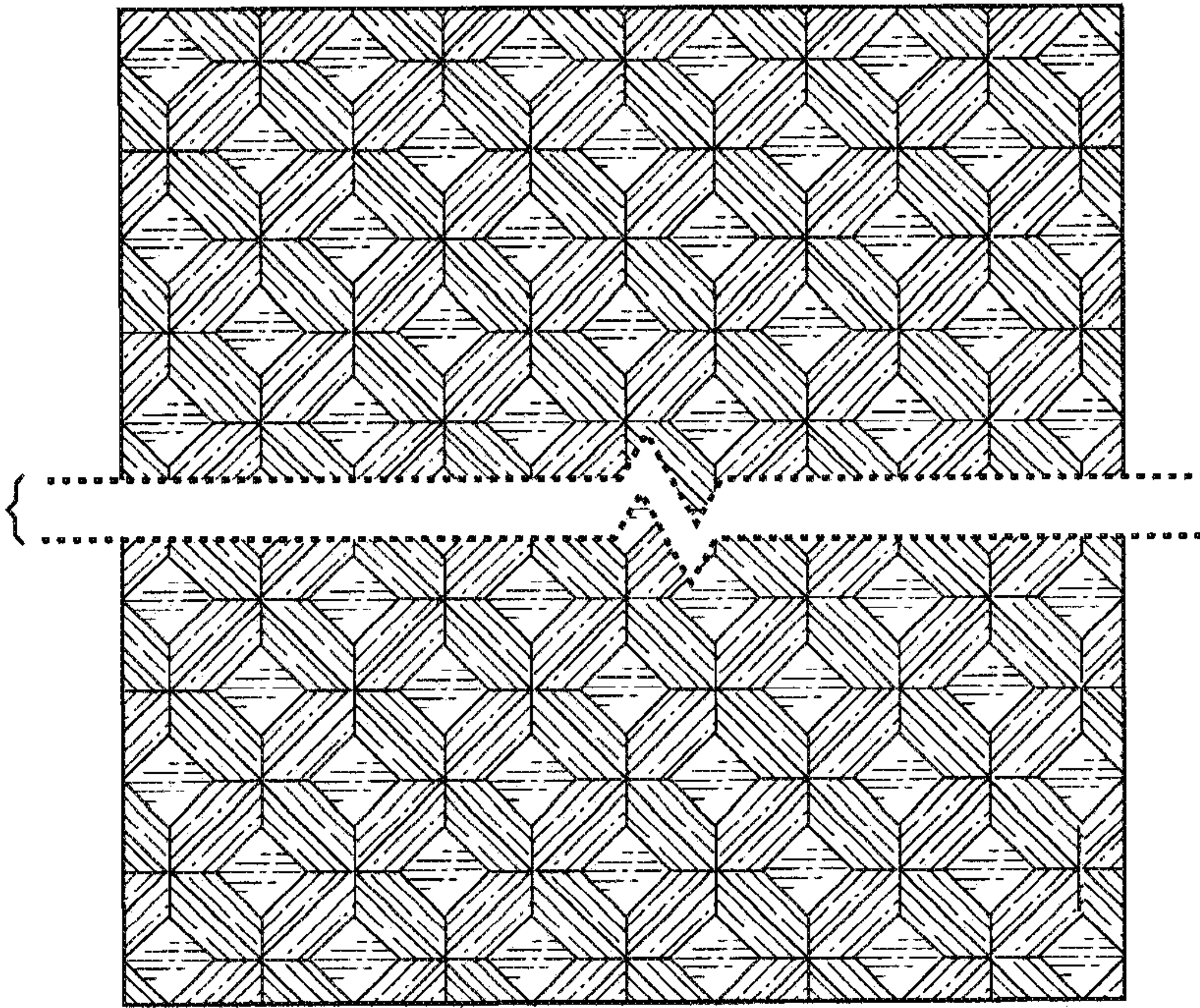


FIG. 7

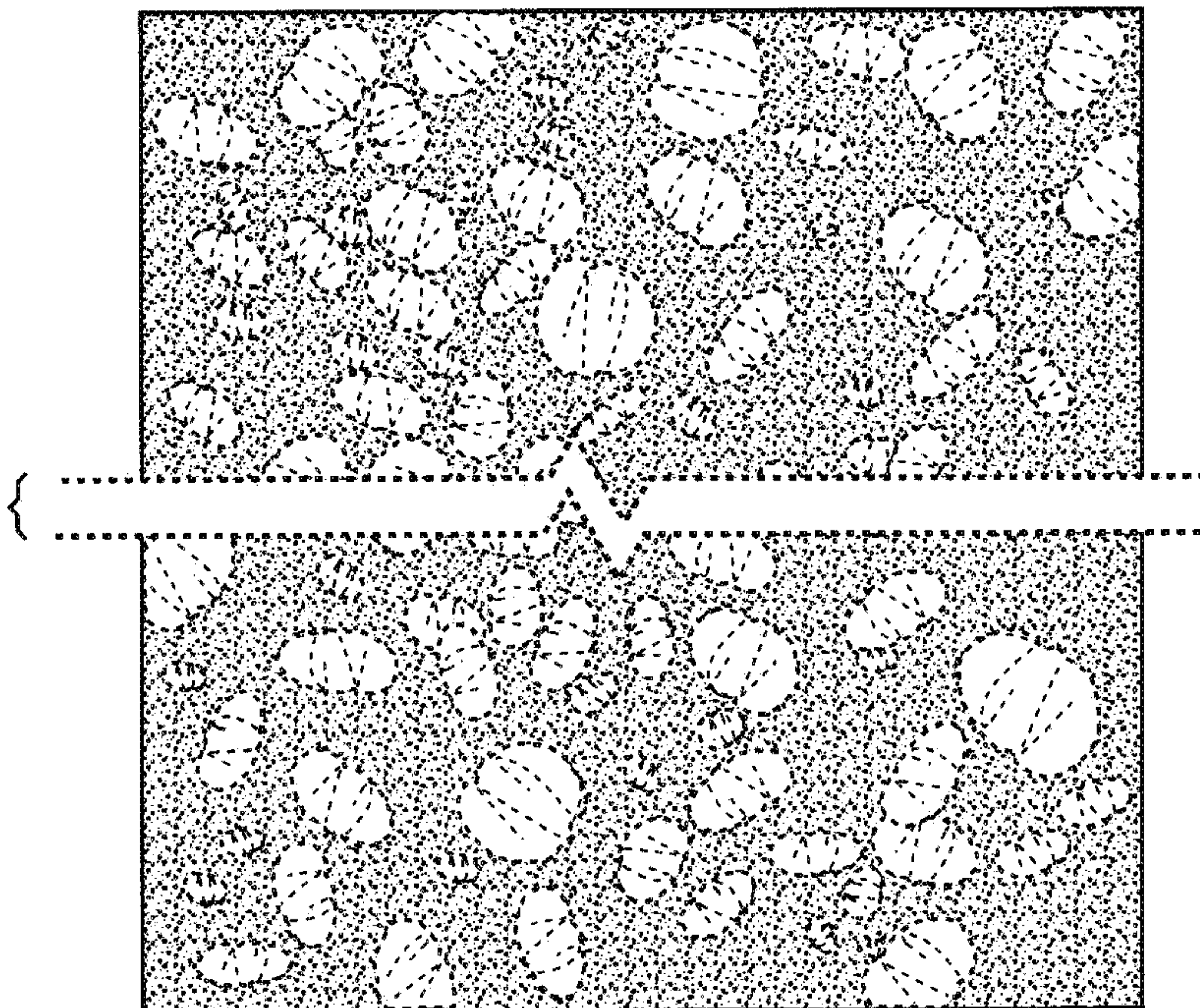


FIG. 6

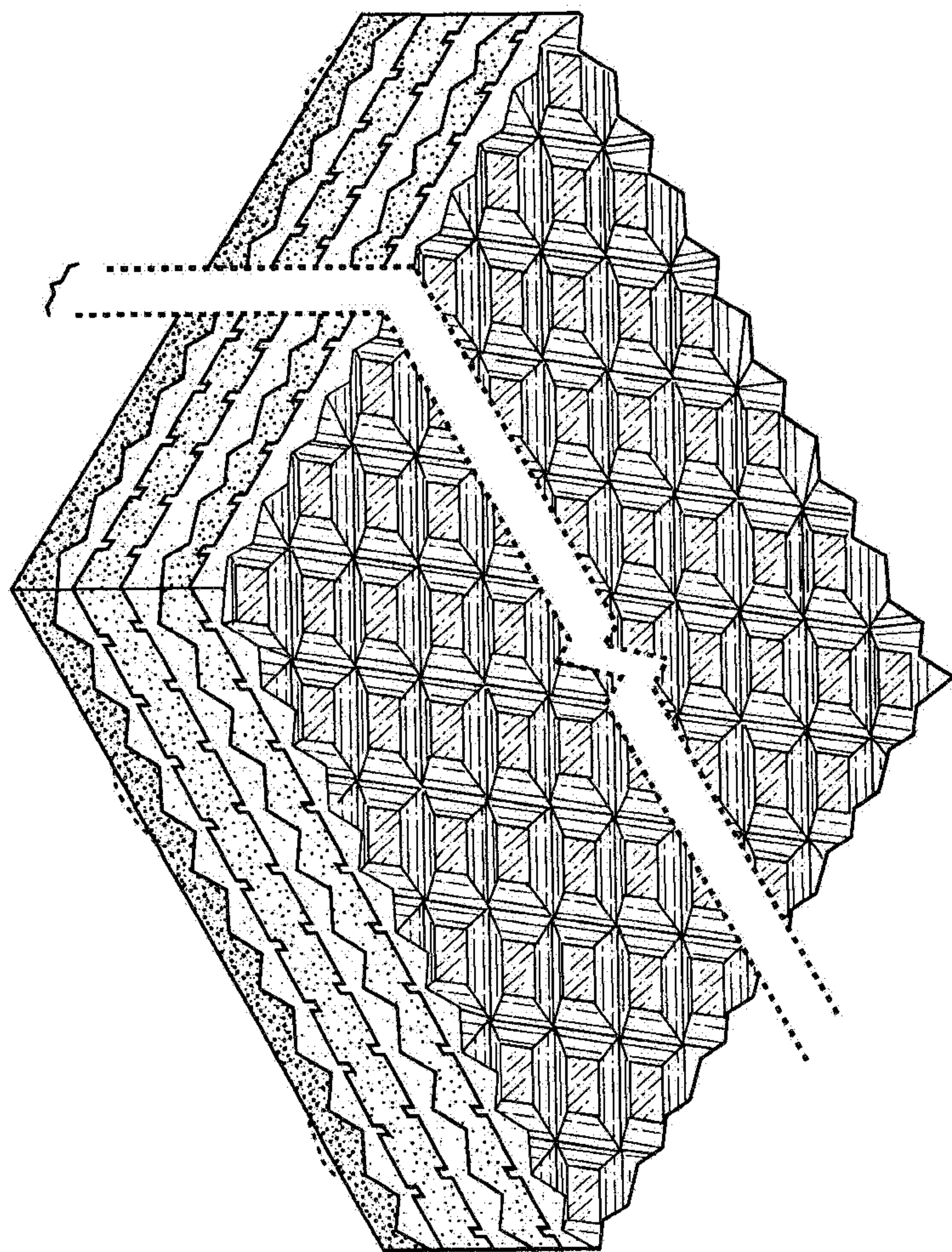


FIG. 8