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(12) **United States Design Patent**
Sakamura et al.

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(45) **Date of Patent:** **** Jun. 1, 2021**

(54) **SIGNAL INPUT AND OUTPUT CONVERTER**

(71) Applicant: **SMC CORPORATION**, Tokyo (JP)

(72) Inventors: **Naoki Sakamura**, Nagareyama (JP);
Takanori Saito, Tsukubamirai (JP)

(73) Assignee: **SMC CORPORATION**, Tokyo (JP)

(**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (13) Cl.** **13-02**

(52) **U.S. Cl.**
USPC **D13/110; D13/123**

(58) **Field of Classification Search**
USPC D13/110, 123, 146, 147; D14/355, 357,
D14/358, 433, 434
CPC G05F 3/04; G05F 3/06; G05F 3/08; G05F
3/10; H02M 7/003; H02M 7/06; H02M
2001/0045; H02M 2001/0067; H02M
2001/007; H02M 2001/0083; H02M
2001/009; H02P 27/04; H02P 27/047;
H02P 27/06

See application file for complete search history.

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Primary Examiner — Selina Sikder

(74) *Attorney, Agent, or Firm* — Birch, Stewart, Kolasch
& Birch LLP

(57) **CLAIM**

The ornamental design for a signal input and output con-
verter, as shown and described.

DESCRIPTION

FIG. 1 is a front, top and left side perspective view of a
signal input and output converter showing our new design;
FIG. 2 is a front, top and right side perspective view thereof;
FIG. 3 is a rear, bottom and right side perspective view
thereof;
FIG. 4 is a rear, bottom and left side perspective view
thereof;
FIG. 5 is a front view thereof;
FIG. 6 is a rear view thereof;
FIG. 7 is a top plan view thereof;
FIG. 8 is a bottom plan view thereof;
FIG. 9 is a left side view thereof; and,
FIG. 10 is a right side view thereof.
The broken lines depict portions of the signal input and
output converter that form no part of the claimed design.

1 Claim, 10 Drawing Sheets

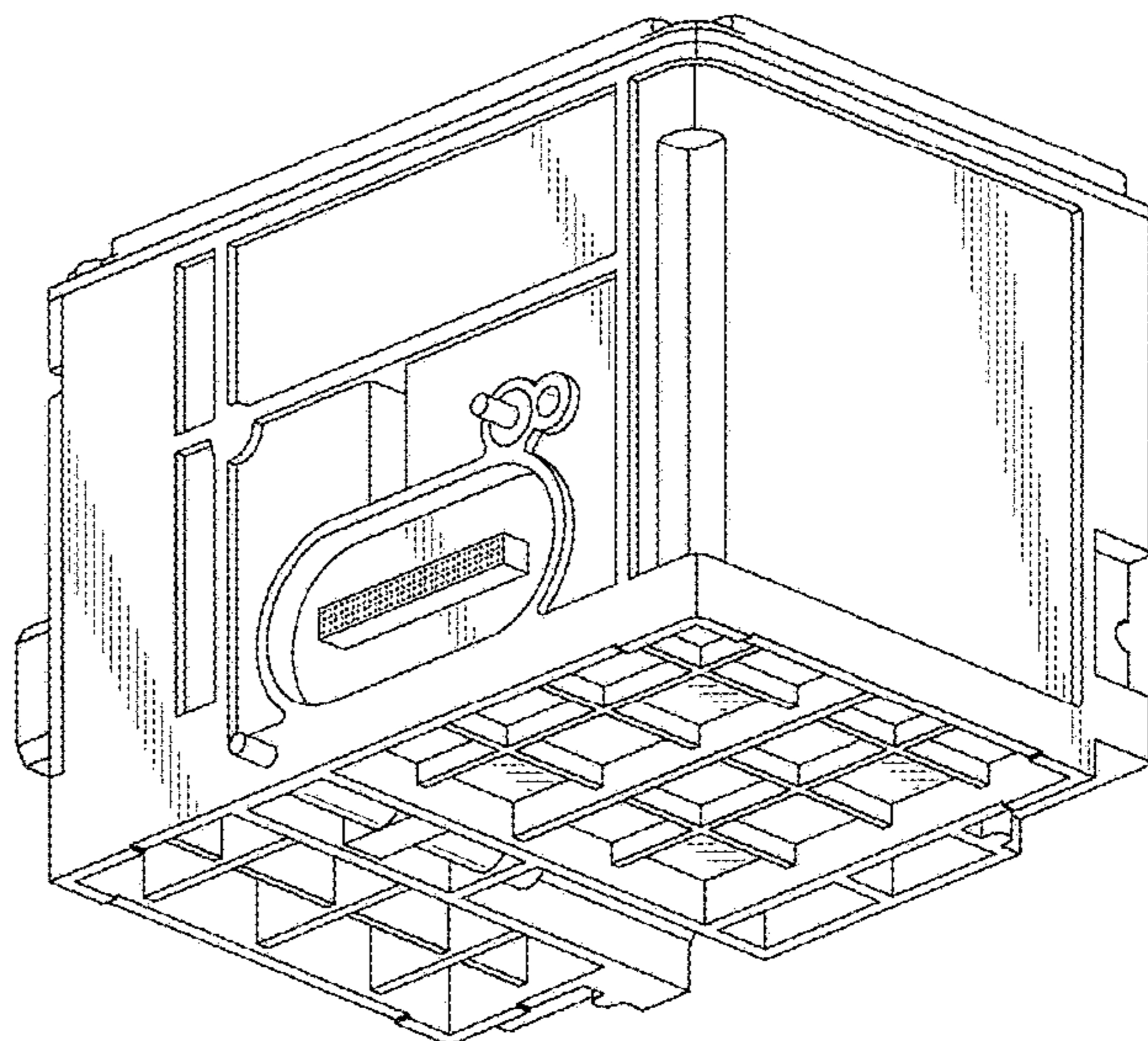
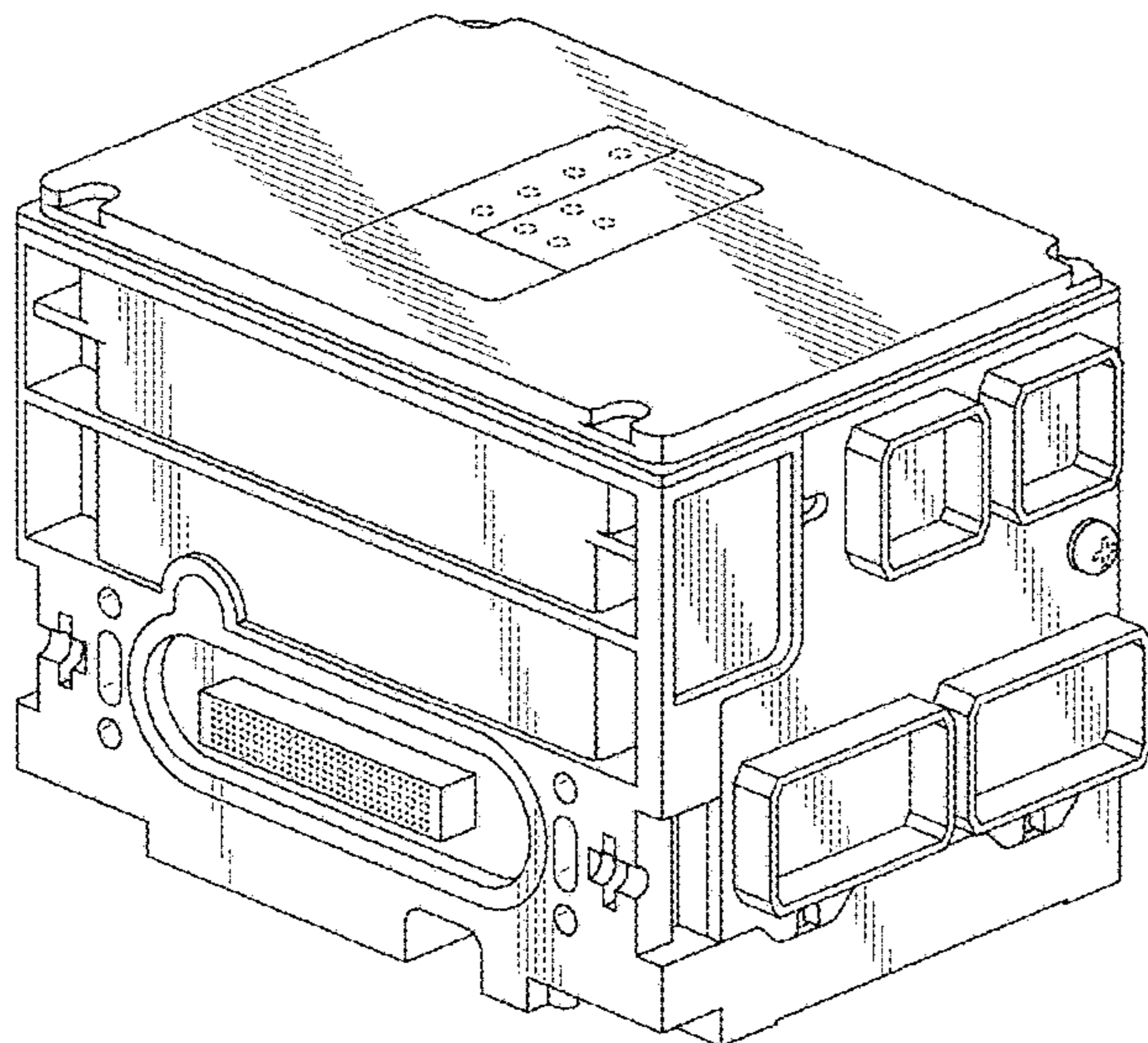


FIG. 1

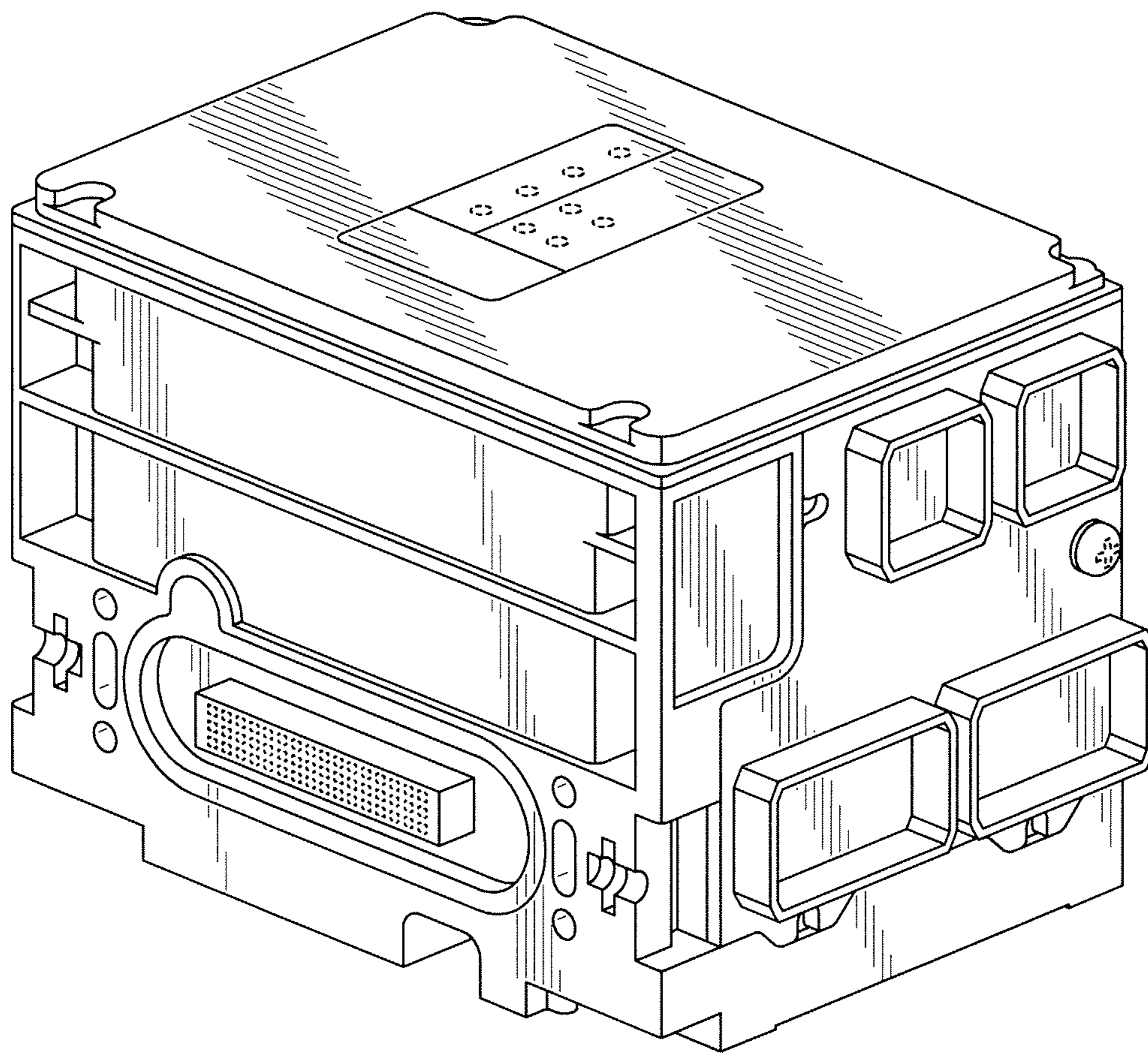


FIG. 2

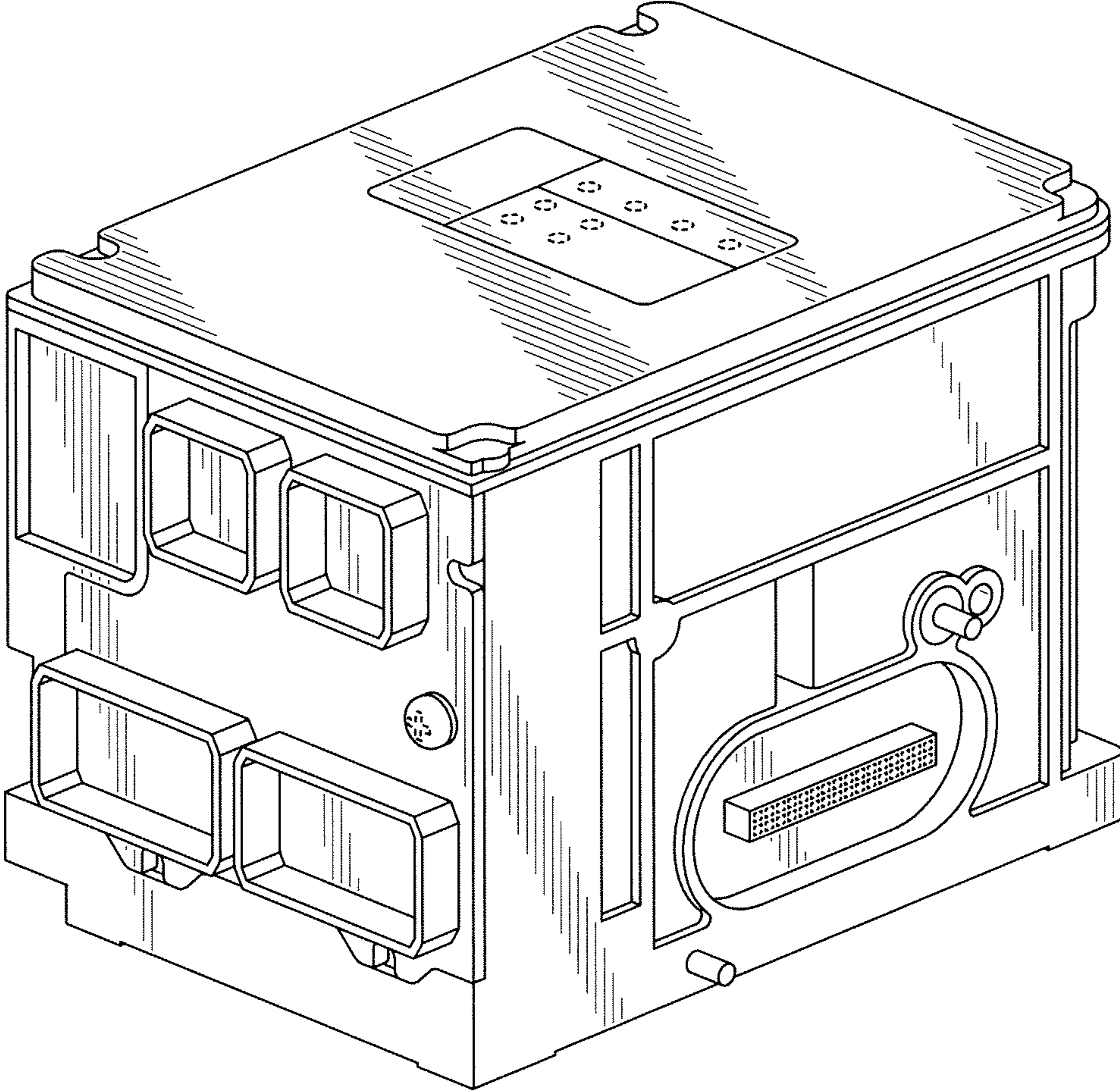


FIG. 3

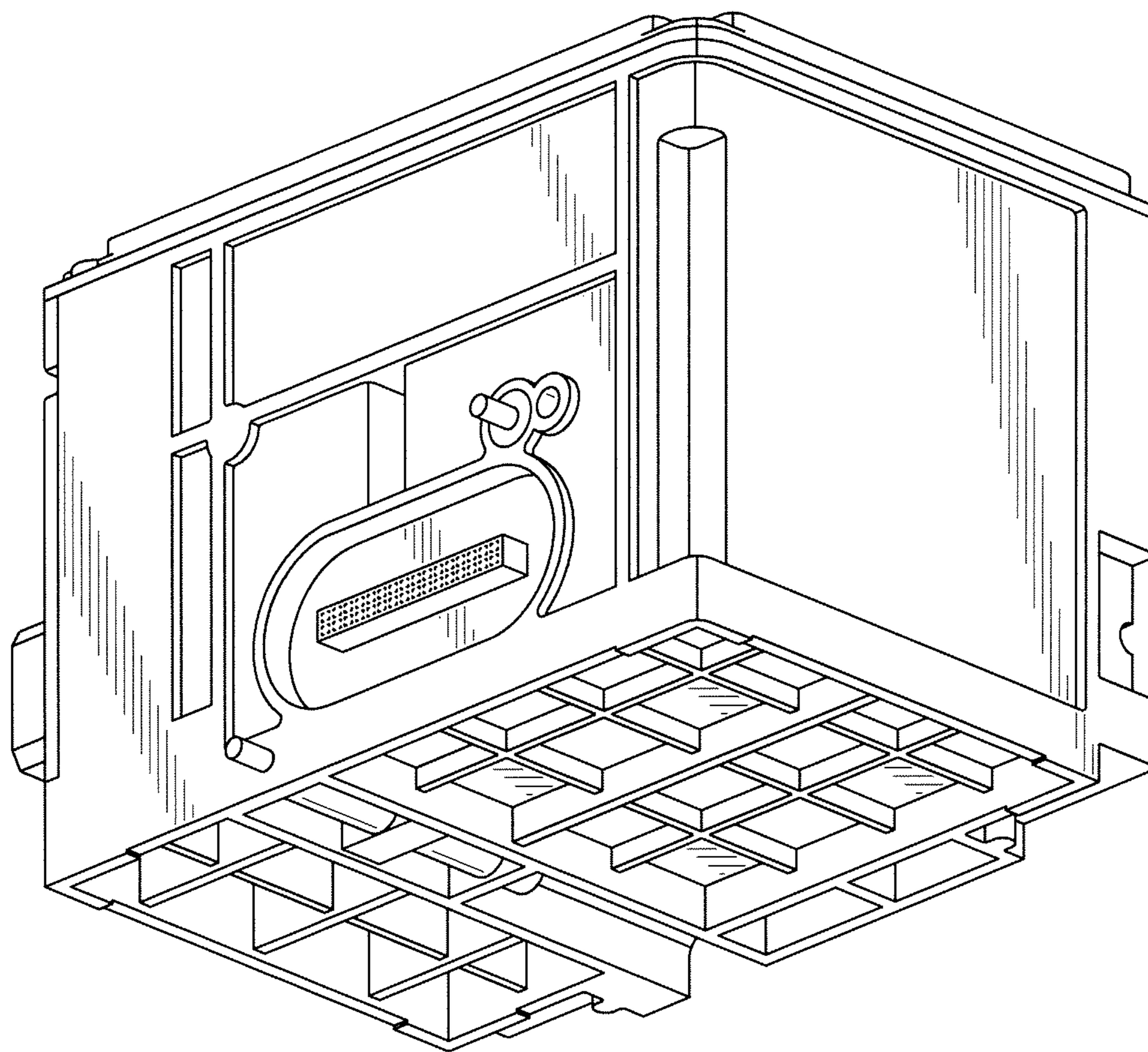


FIG. 4

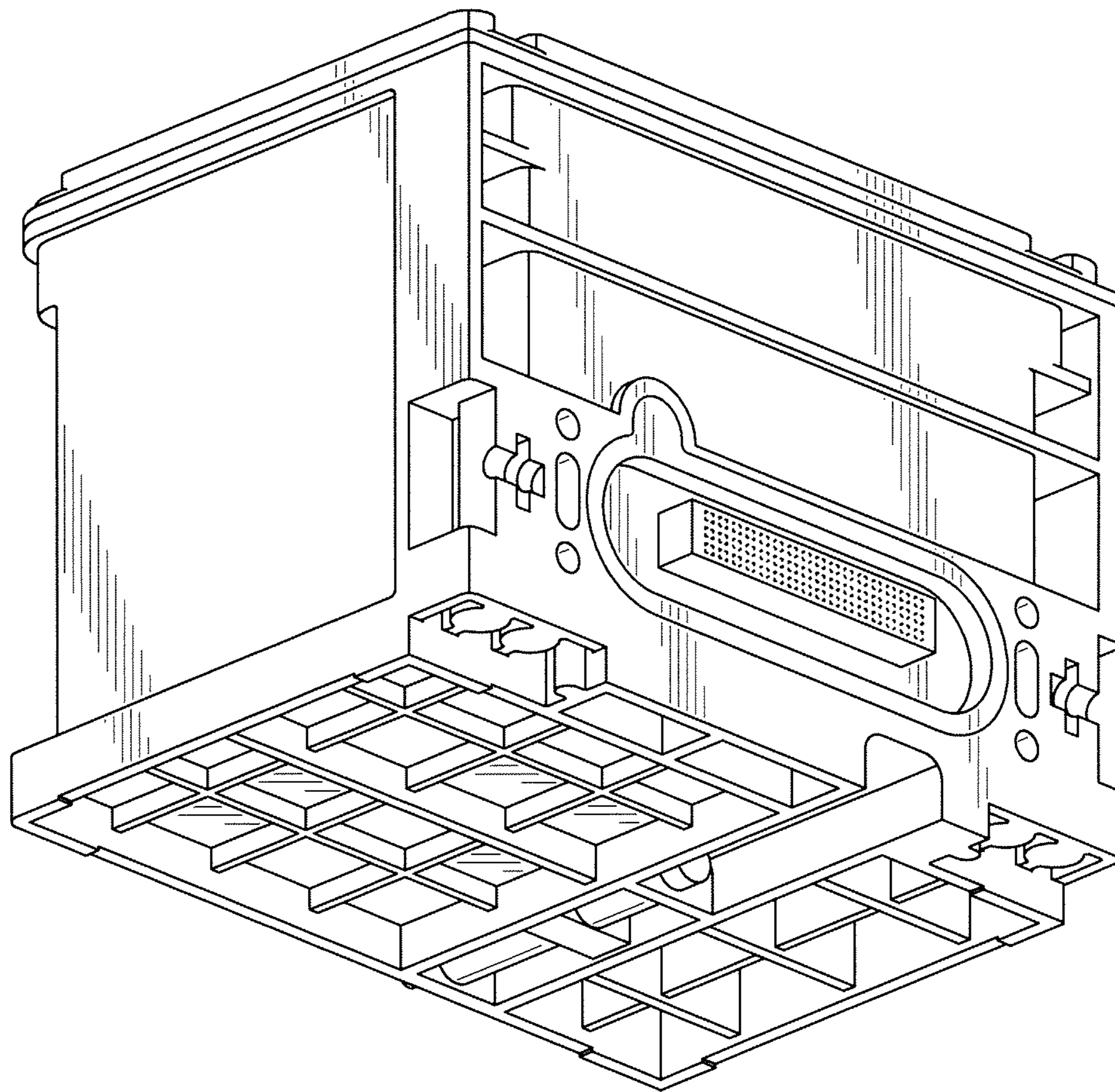


FIG. 5

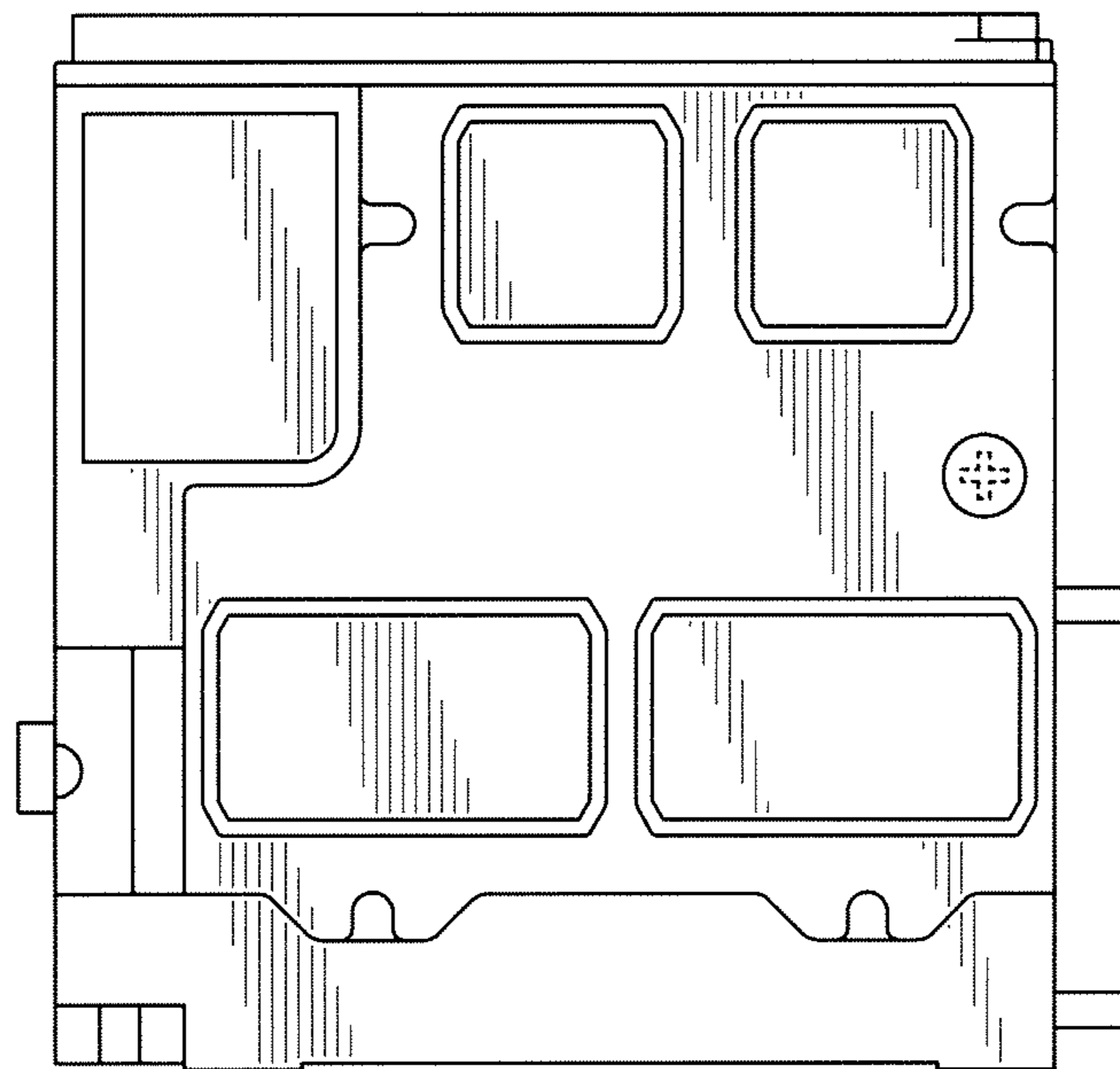


FIG. 6

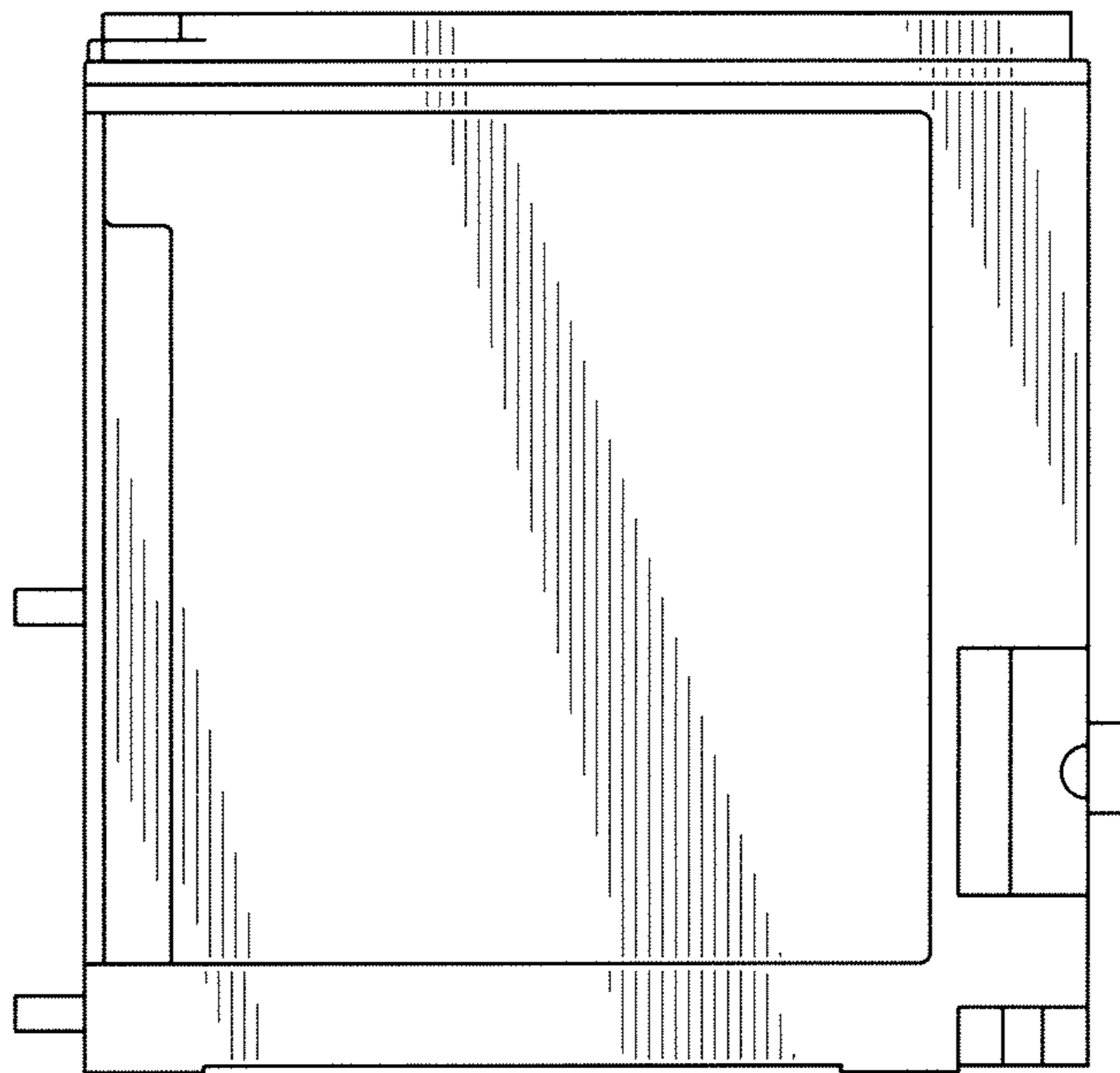


FIG. 7

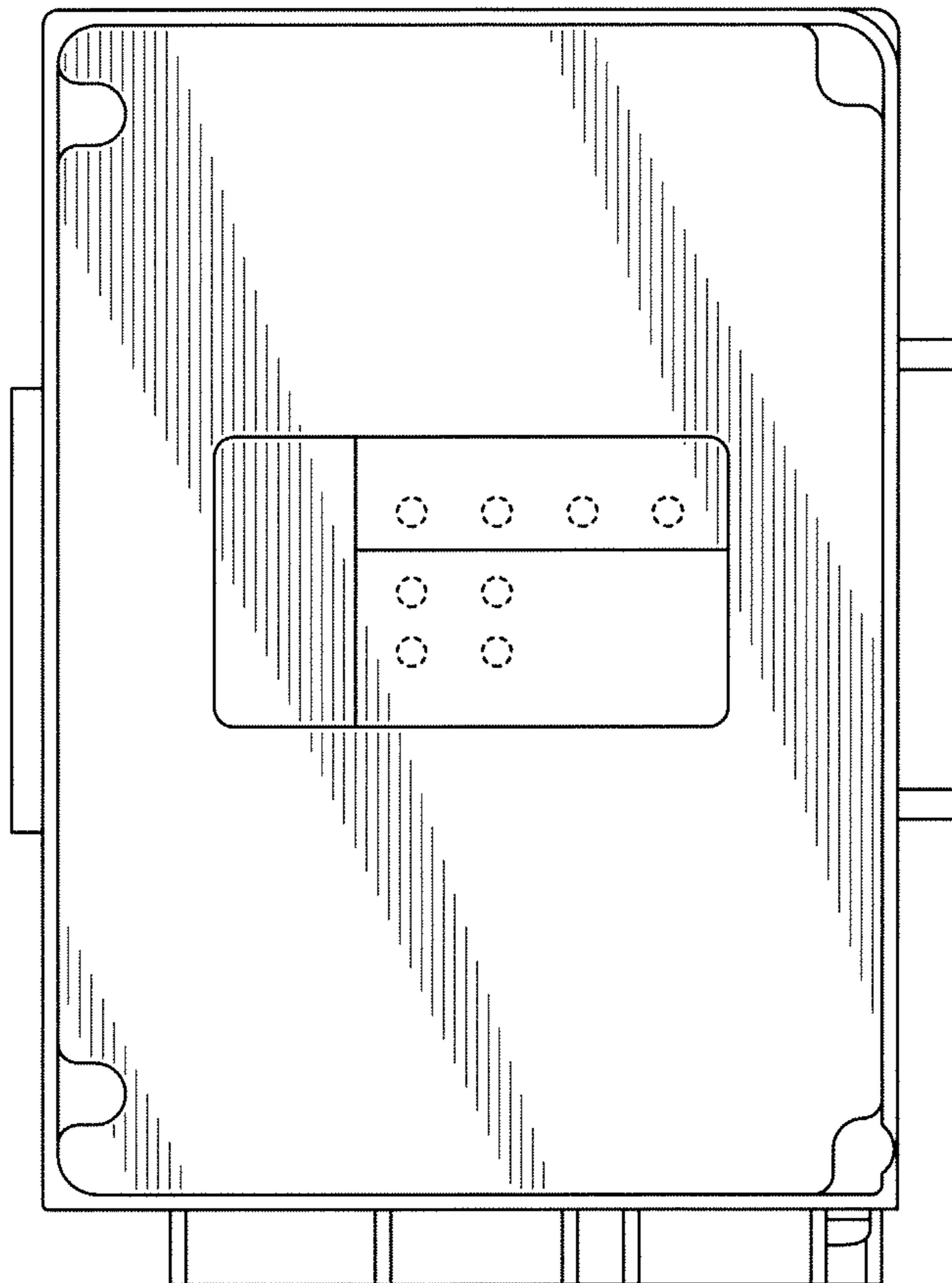


FIG. 8

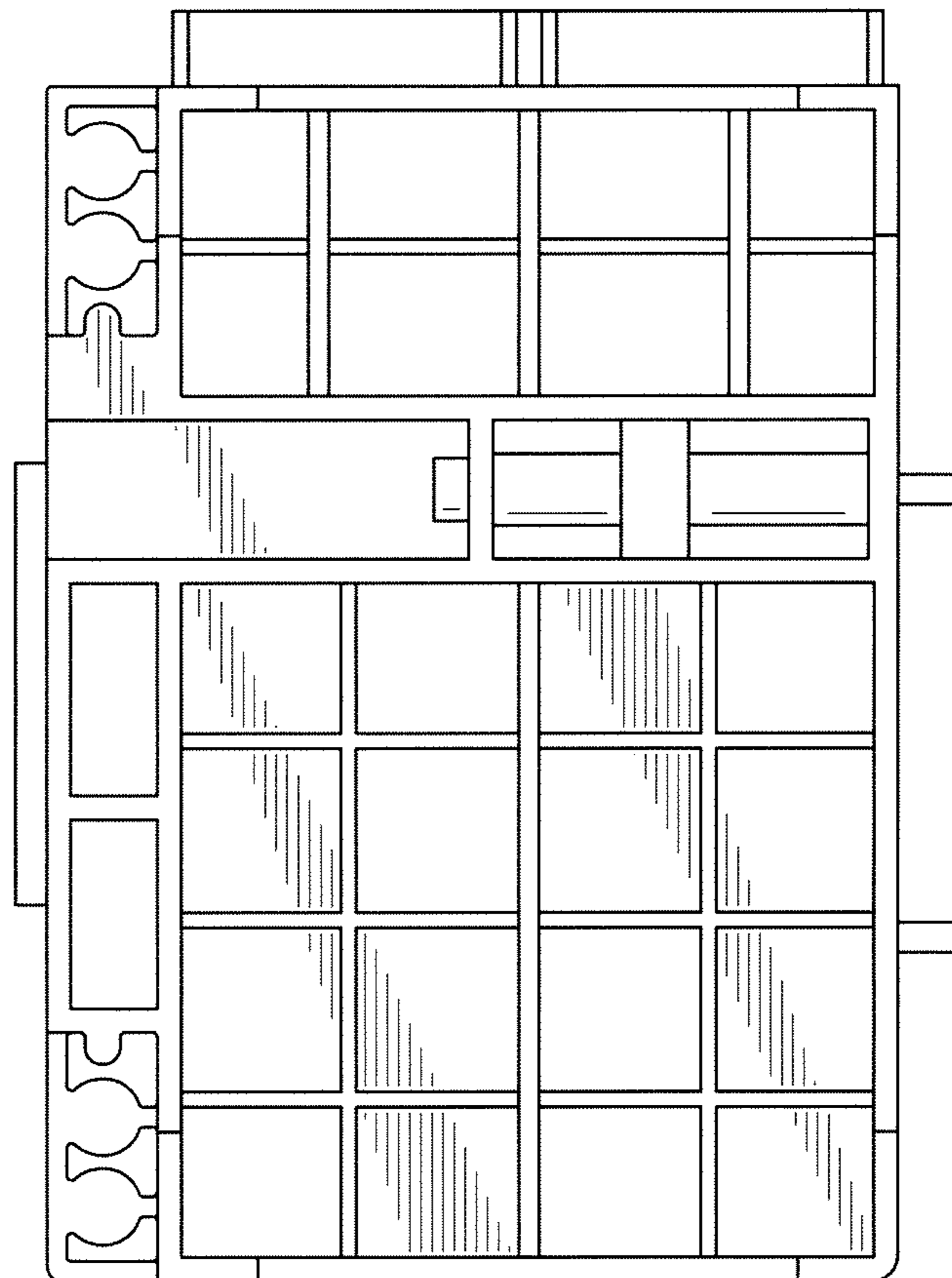


FIG. 9

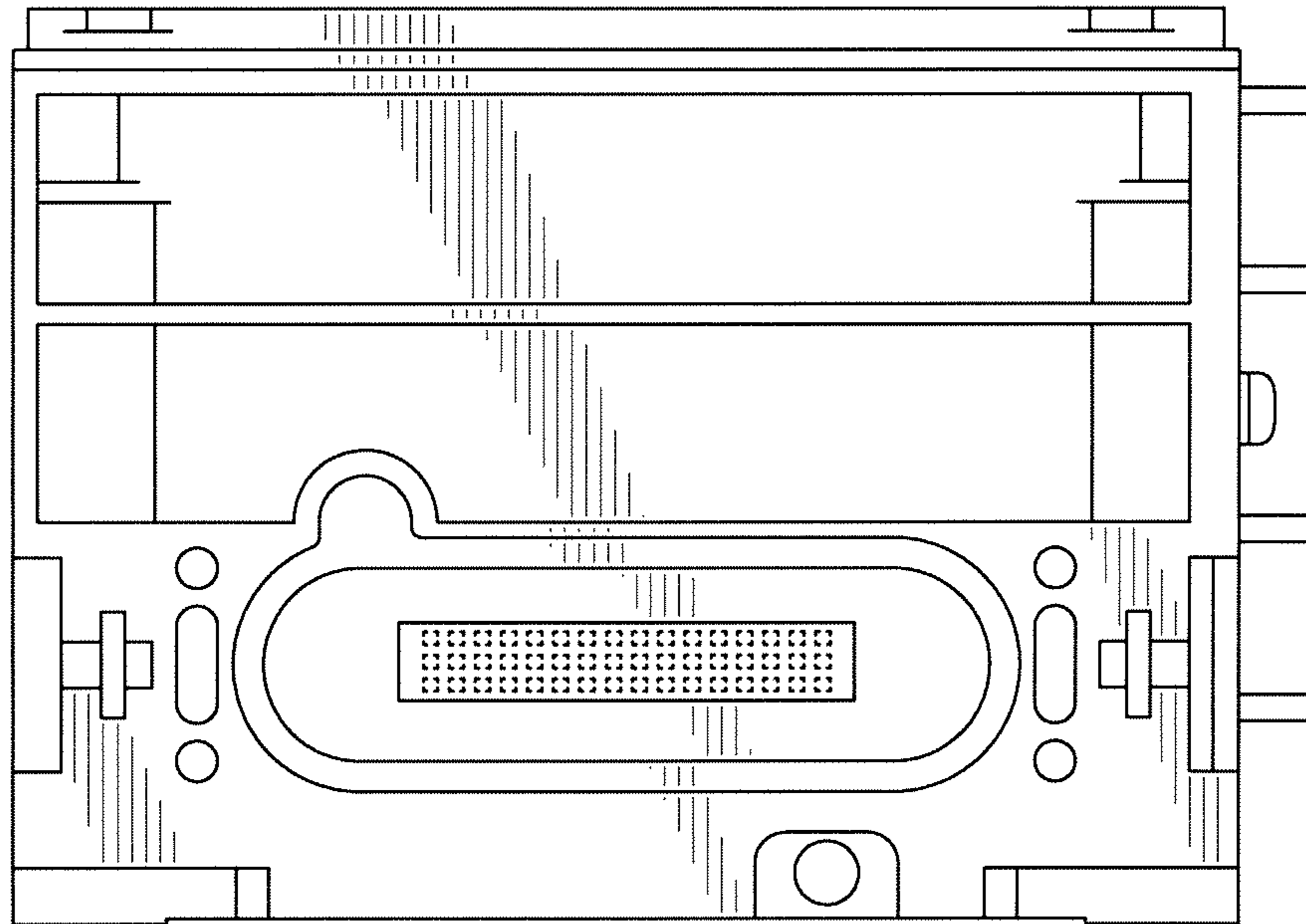


FIG. 10

