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(12) **United States Design Patent** (10) **Patent No.:** **US D920,767 S**  
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(54) **WALL PLATE COVER FOR ELECTRONIC DEVICE**

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(\*\*) Term: **15 Years**

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(52) **U.S. Cl.**  
USPC ..... **D8/352; D13/156**

(58) **Field of Classification Search**  
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D13/152, 162, 164, 169, 170, 171, 174,  
D13/177, 154, 156; D8/350, 351, 352,  
D8/353, 356  
See application file for complete search history.

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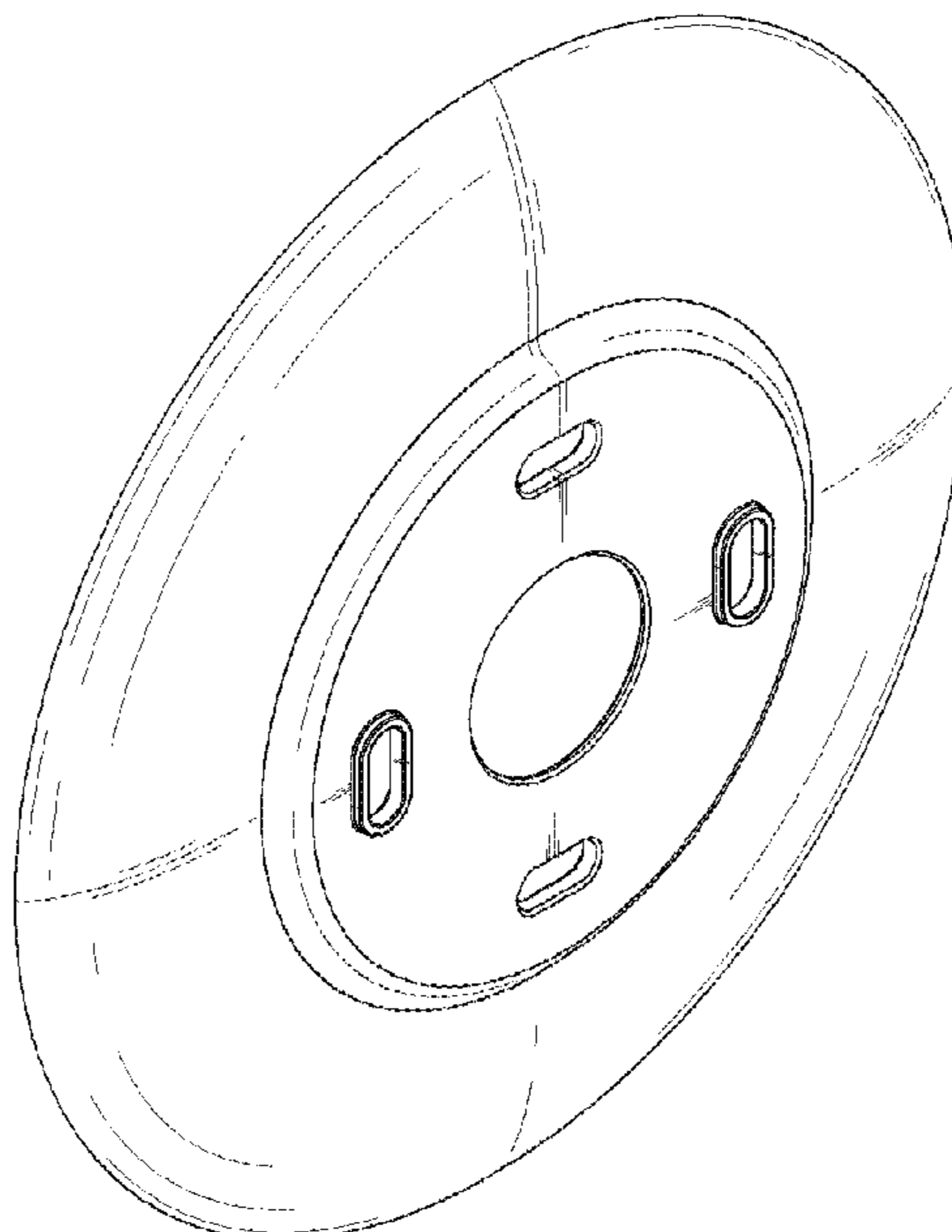
(57) **CLAIM**

The ornamental design for a wall plate cover for electronic device, as shown and described.

**DESCRIPTION**

FIG. 1 is a front, top, and left sides perspective view of wall plate cover for electronic device showing claimed design; FIG. 2 is a front view thereof; FIG. 3 is a back view thereof; FIG. 4 is a left side view thereof; FIG. 5 is a right side view thereof; FIG. 6 is a top view thereof; FIG. 7 is a bottom view thereof; and, FIG. 8 is a back, top, and left sides perspective view thereof.

**1 Claim, 5 Drawing Sheets**



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FIG. 1

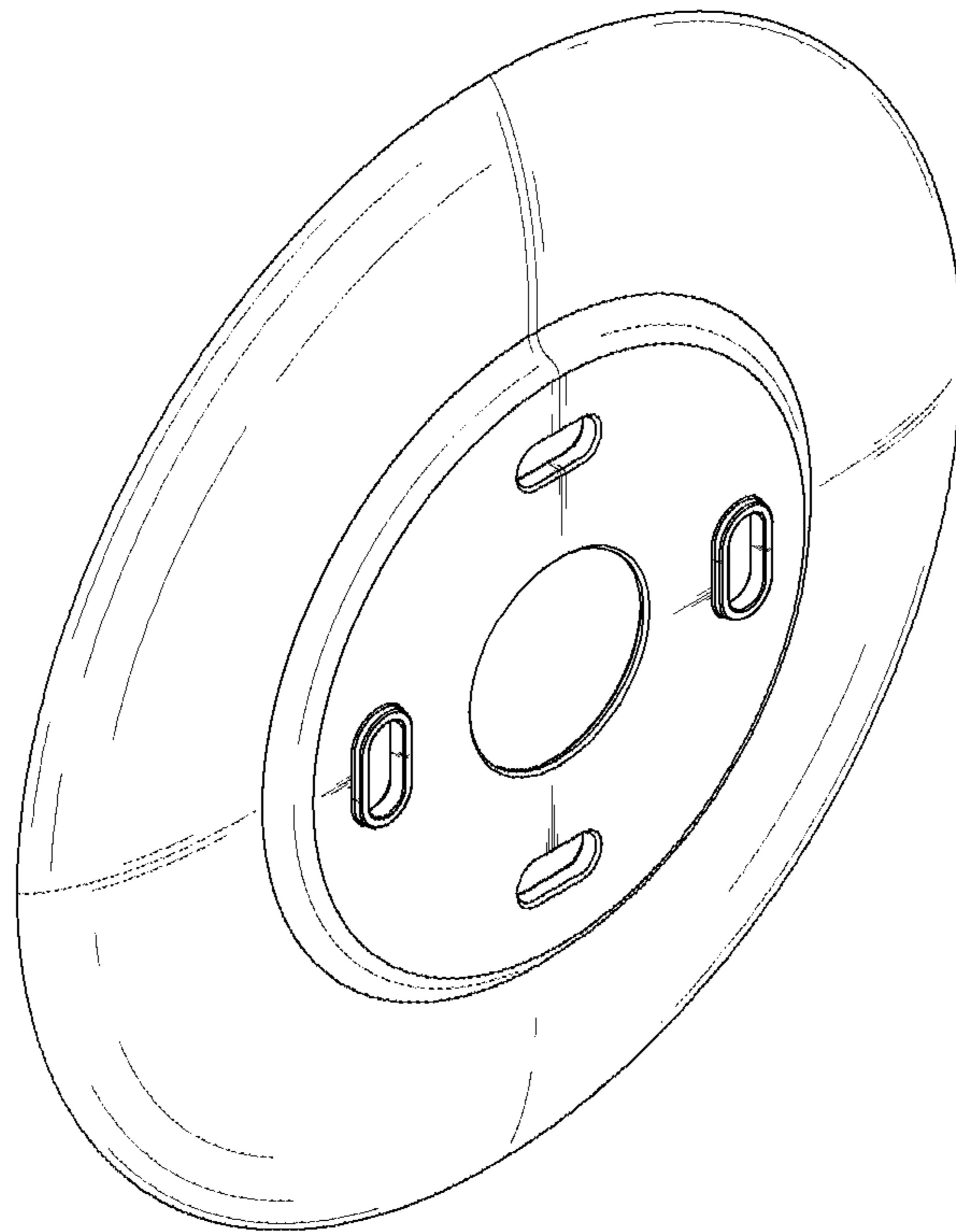


FIG. 2

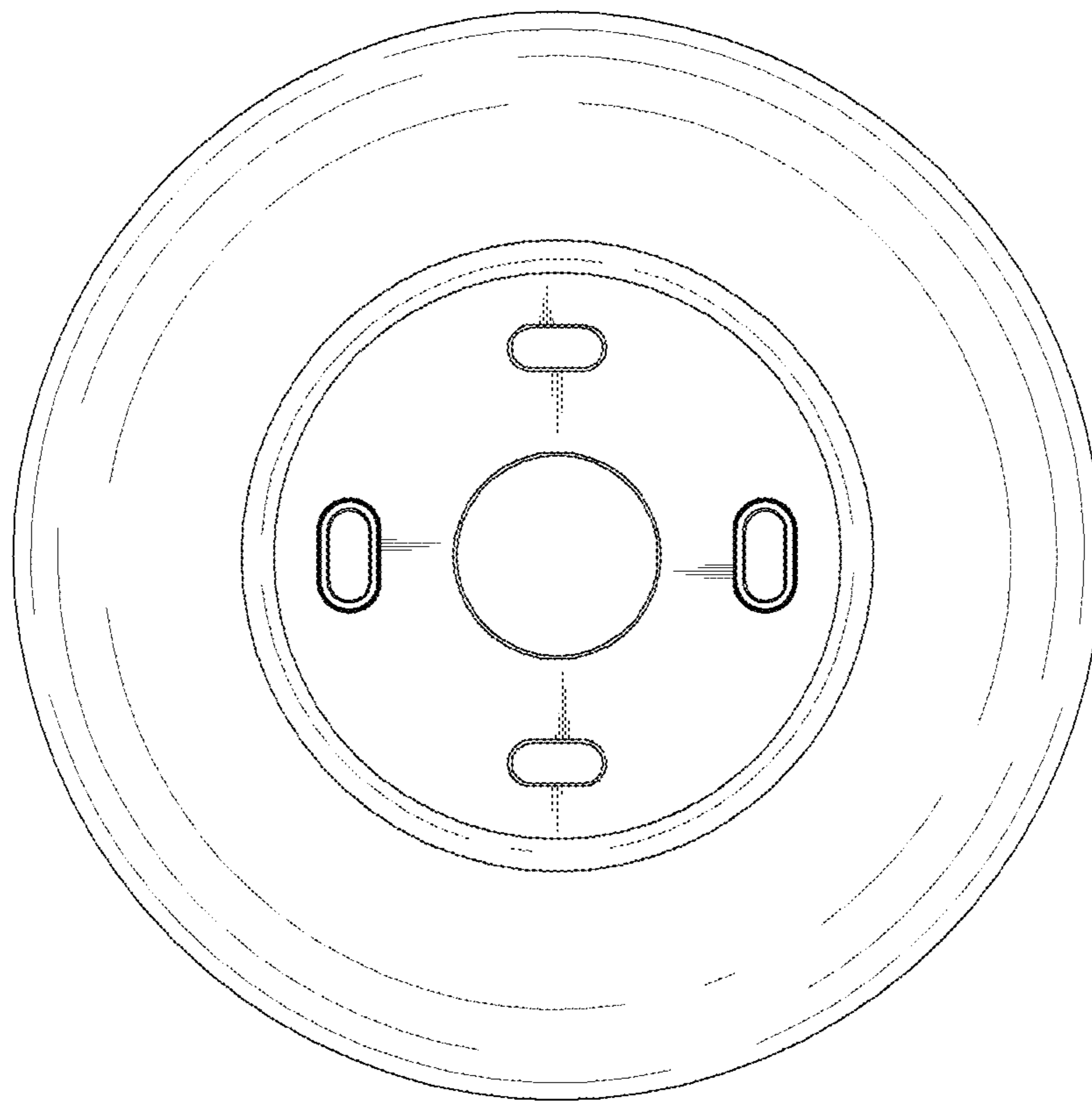


FIG. 3

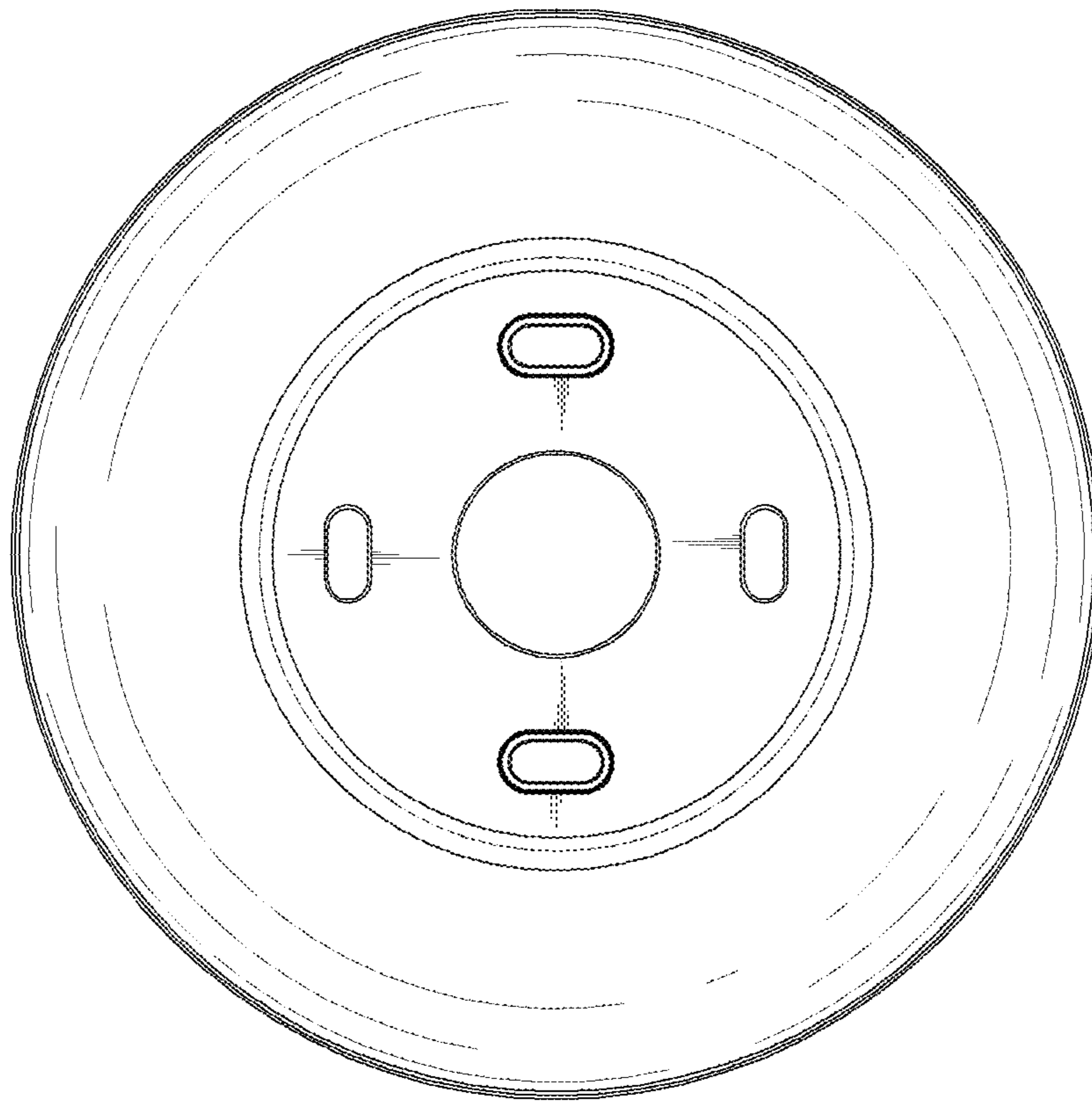


FIG. 4



FIG. 5

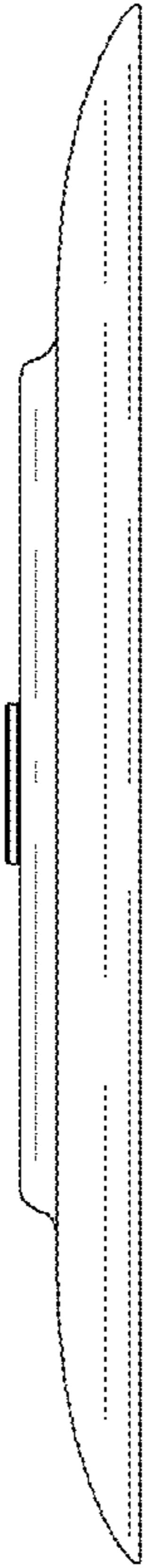


FIG. 6

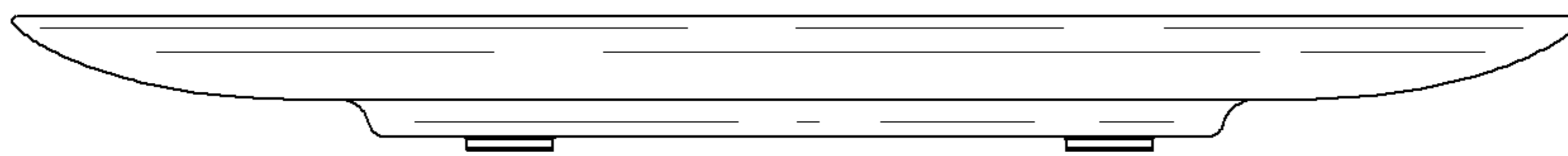


FIG. 7

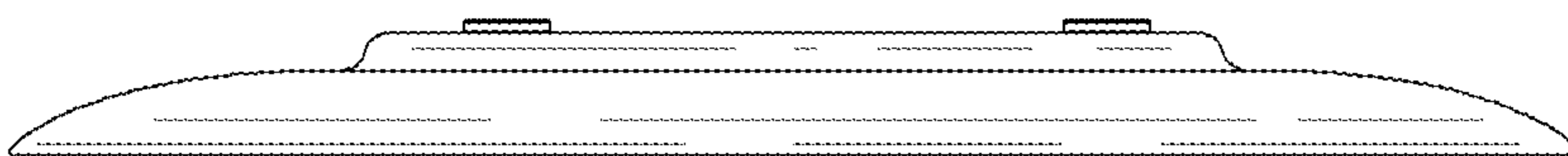


FIG. 8

