



US00D919818S

(12) **United States Design Patent** (10) **Patent No.:** **US D919,818 S**
Levine et al. (45) **Date of Patent:** **** May 18, 2021**

(54) **ELECTROENCEPHALOGRAM SIGNAL AMPLIFICATION DEVICE**

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(73) Assignee: **X Development LLC**, Mountain View, CA (US)

(**) Term: **15 Years**

(21) Appl. No.: **29/679,098**

(22) Filed: **Feb. 1, 2019**

(51) **LOC (13) Cl.** **24-02**

(52) **U.S. Cl.**
USPC **D24/186**

(58) **Field of Classification Search**
USPC D10/104.1, 101, 75; D14/188, 299, 358;
D13/162; D24/186-187
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

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(57) **CLAIM**

The ornamental design for an electroencephalogram signal amplification device, substantially as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of an electroencephalogram signal amplification device with an adaptor unit attached to a top surface of the electronic device.

FIG. 2 is a front view of the electronic device of FIG. 1.

FIG. 3 is a rear view of the electronic device of FIG. 1.

FIG. 4 is a left side view of the electronic device of FIG. 1.

FIG. 5 is a right side view of the electronic device of FIG. 1.

FIG. 6 is a top view of the electronic device of FIG. 1.

FIG. 7 is a bottom view of the electronic device of FIG. 1.

FIG. 8 is a perspective view of the electronic device without the adaptor unit (shown in FIGS. 1-7) attached.

FIG. 9 is a front view of the electronic device of FIG. 8 without the adaptor unit.

FIG. 10 is a rear view of the electronic device of FIG. 8 without the adaptor unit.

FIG. 11 is a left side view of the electronic device of FIG. 8 without the adaptor unit.

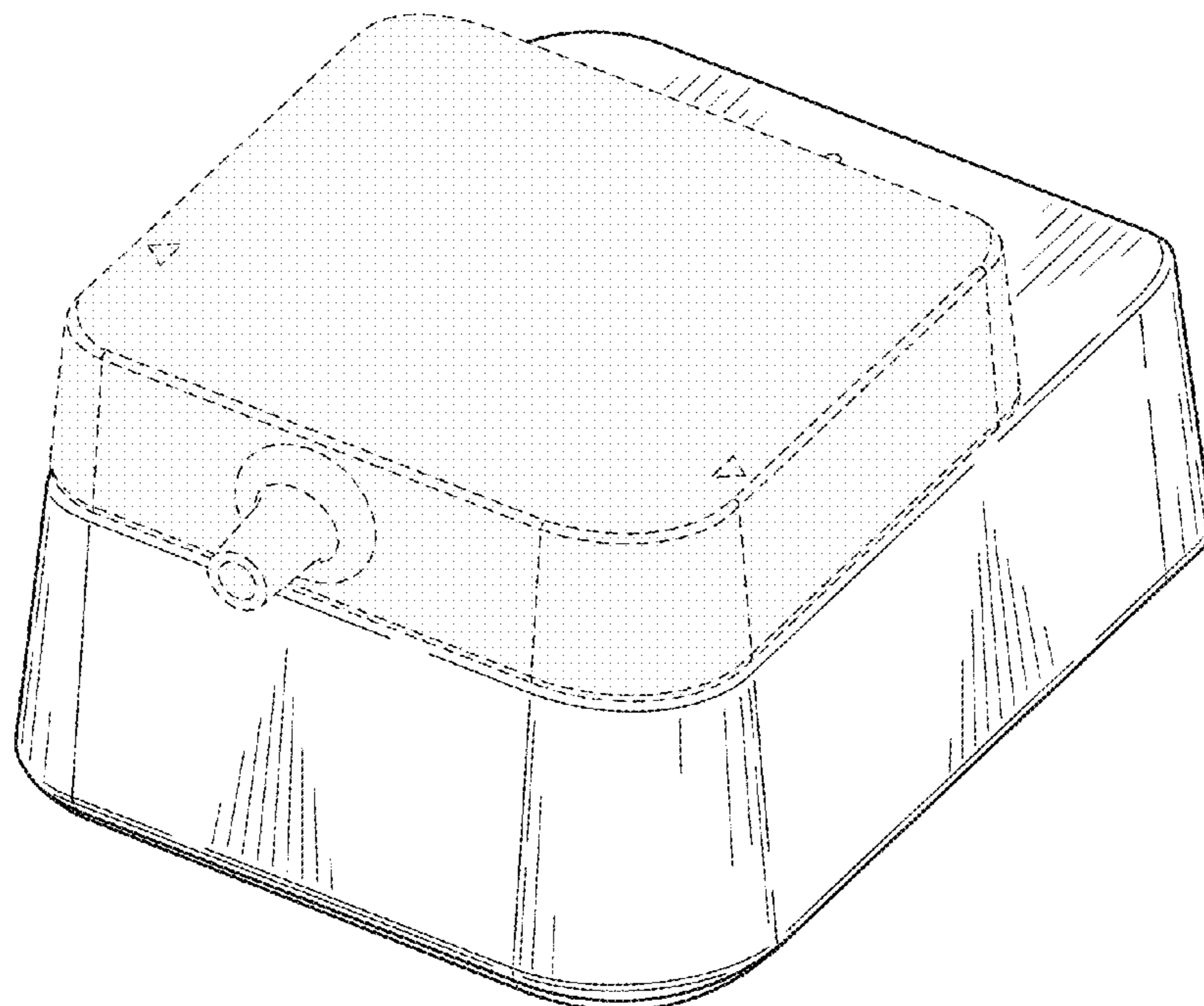
FIG. 12 is a right side view of the electronic device embodiment of FIG. 8 without the adaptor unit.

FIG. 13 is a top view of the electronic device of FIG. 8 without the adaptor unit; and,

FIG. 14 is a bottom view of the electronic device of FIG. 8 without the adaptor unit.

The broken lines in each figure illustrate features that form no part of the claimed design. The adaptor unit shown in (FIGS. 1-7) is represented in broken lines and forms no part of the claimed design.

1 Claim, 8 Drawing Sheets



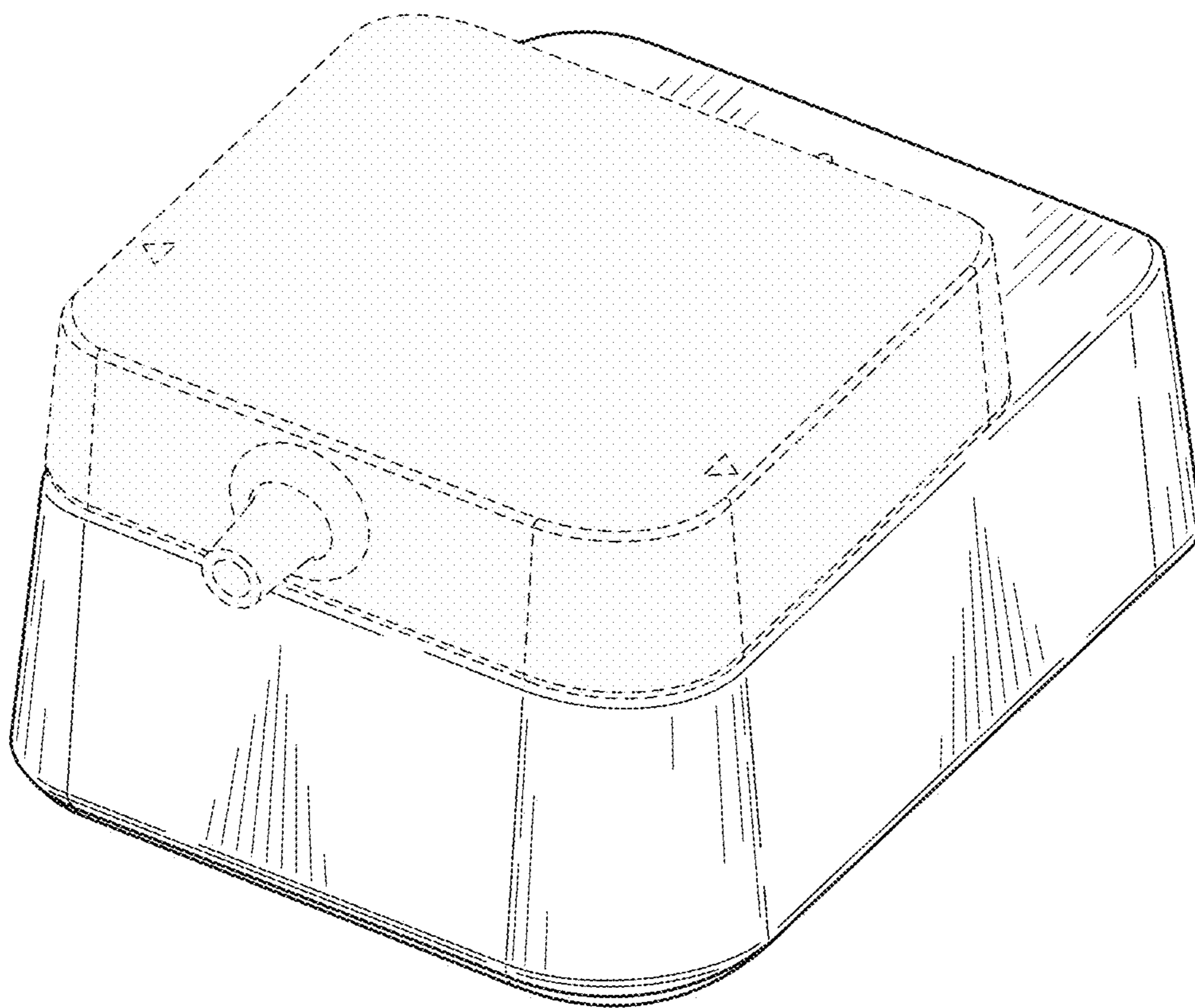


FIG. 1

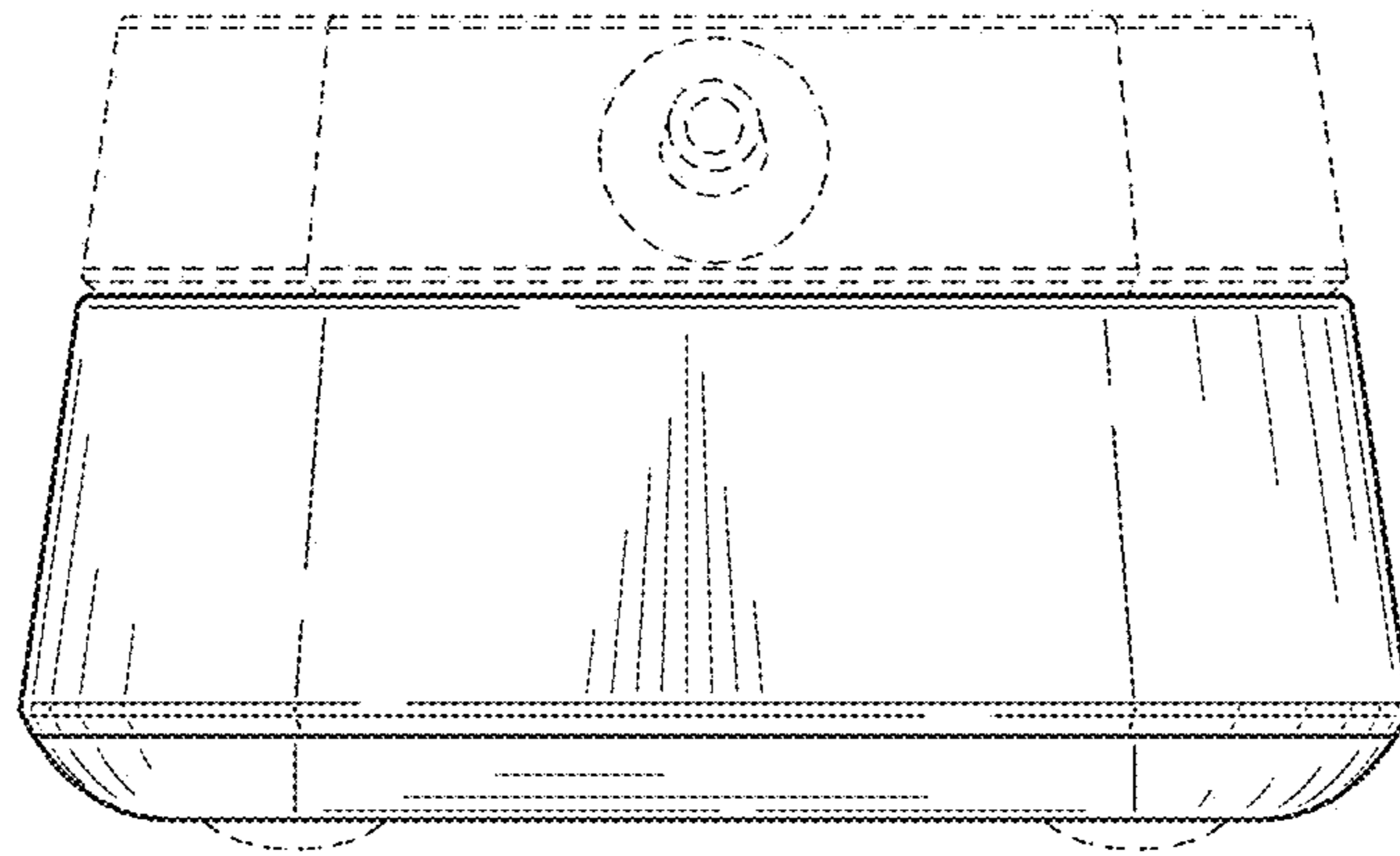


FIG. 2

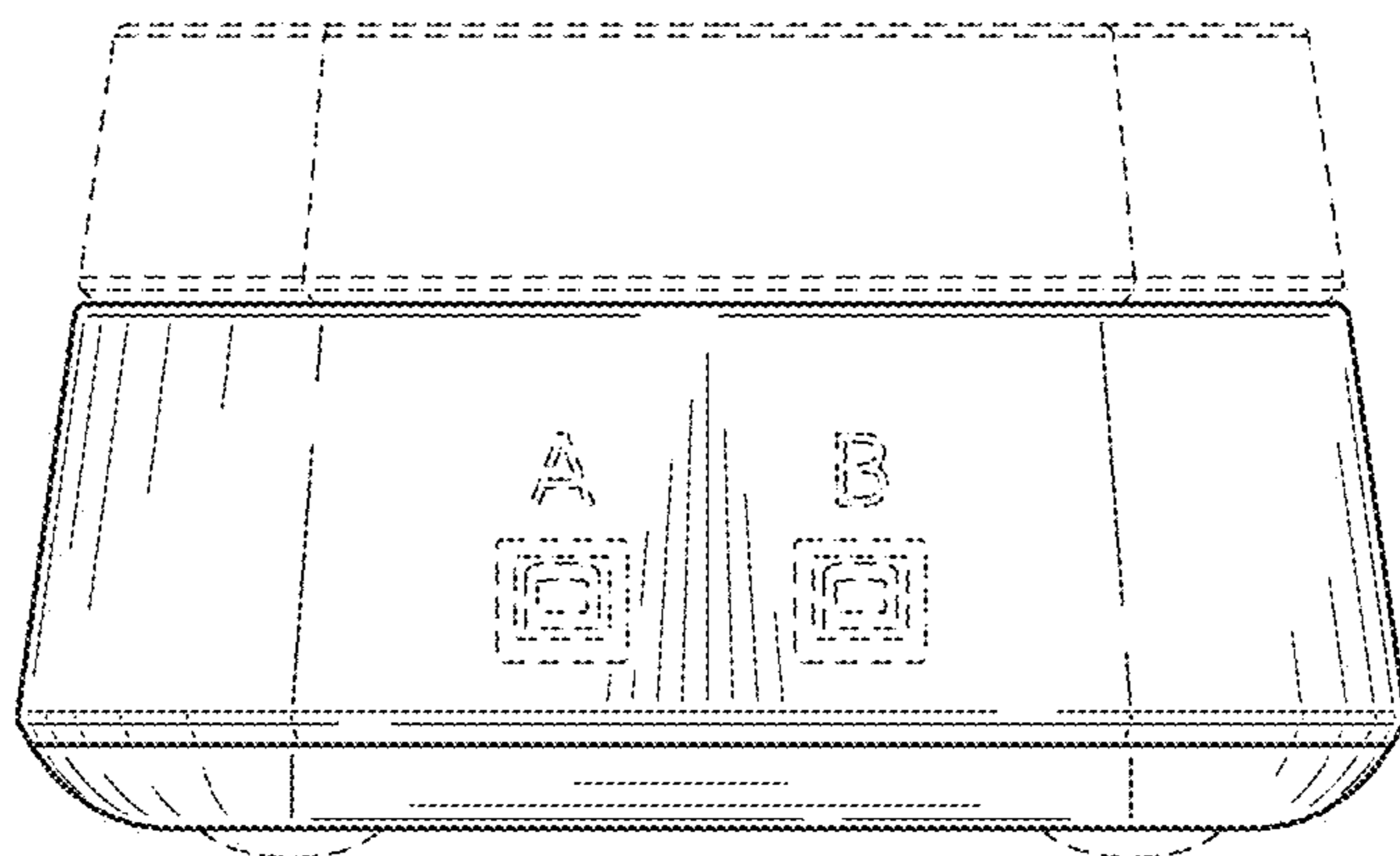


FIG. 3

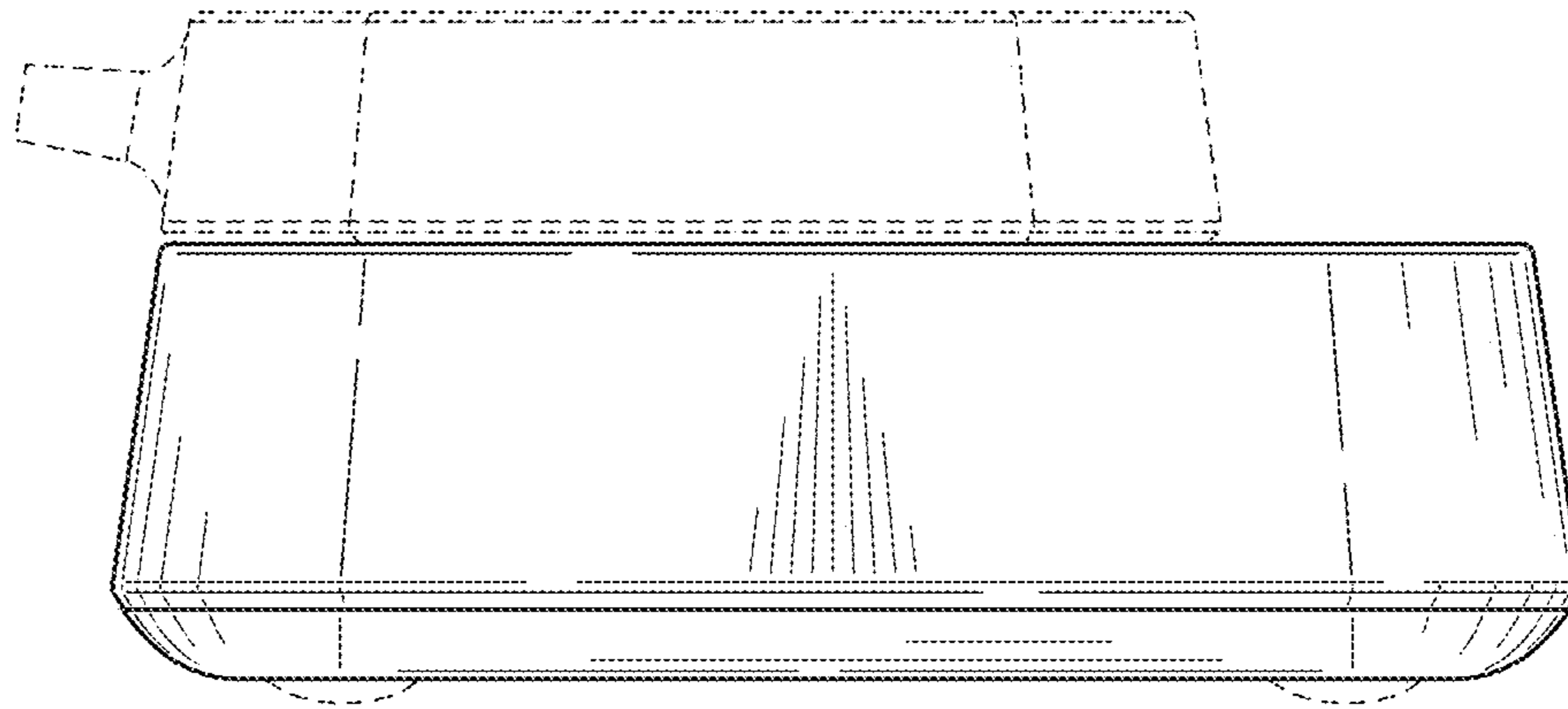


FIG. 4

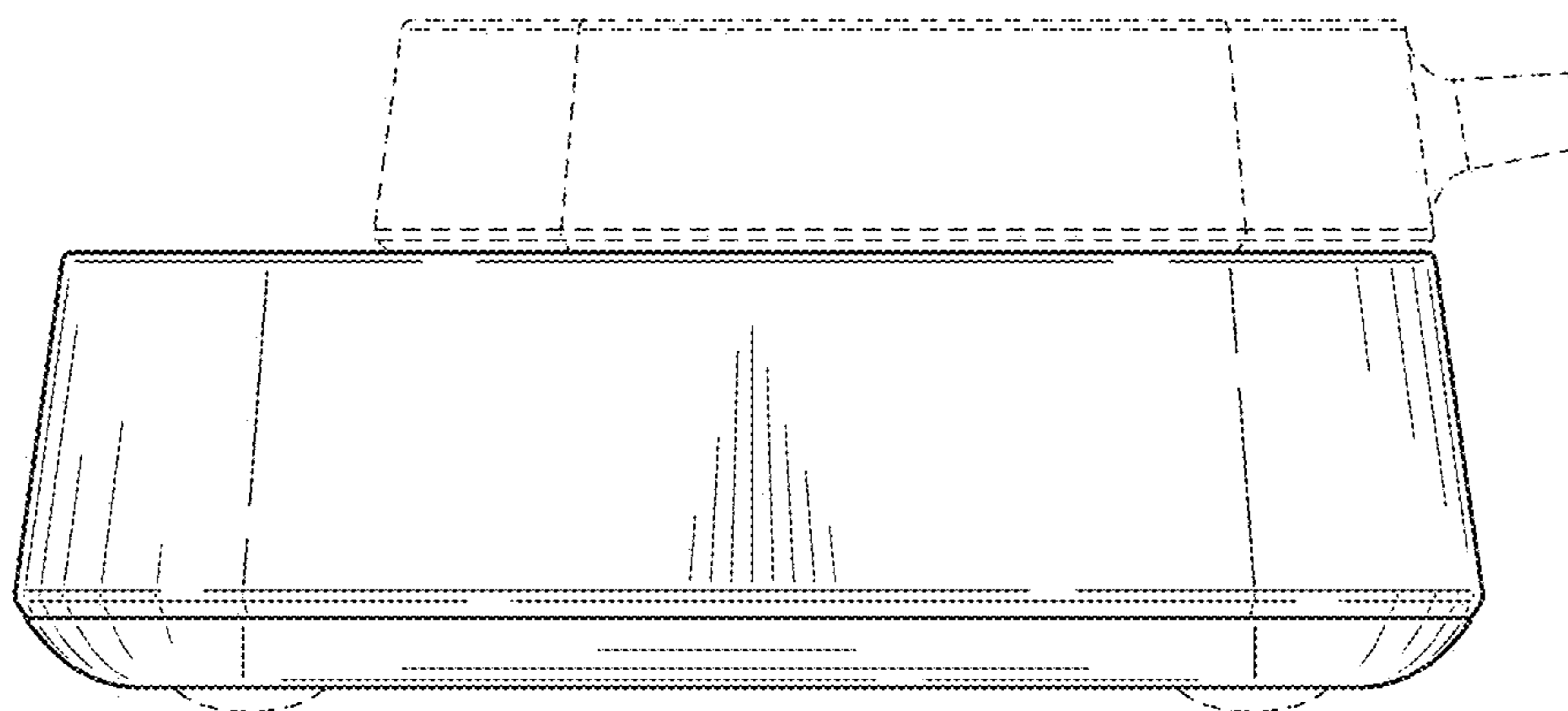


FIG. 5

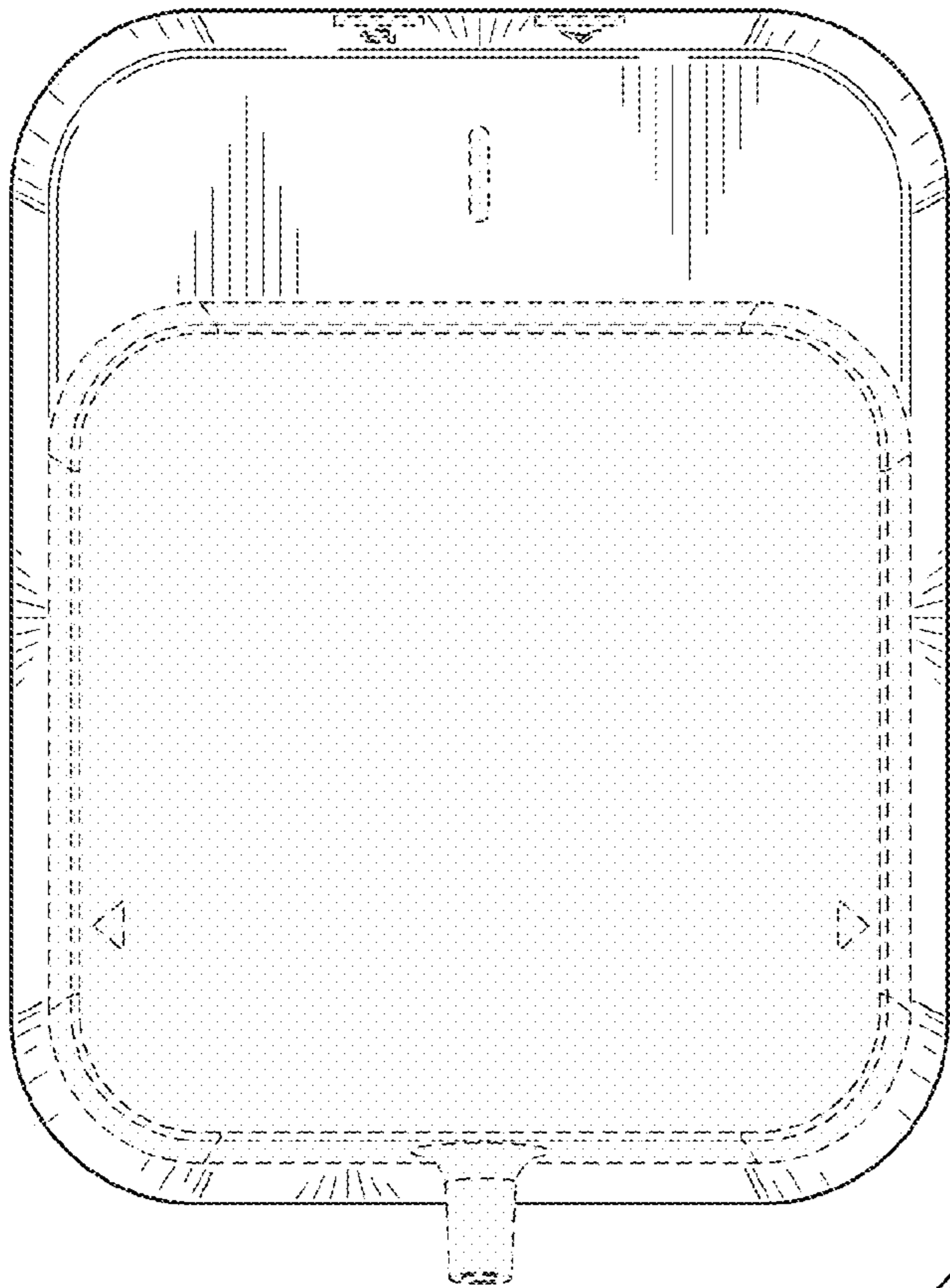


FIG. 6

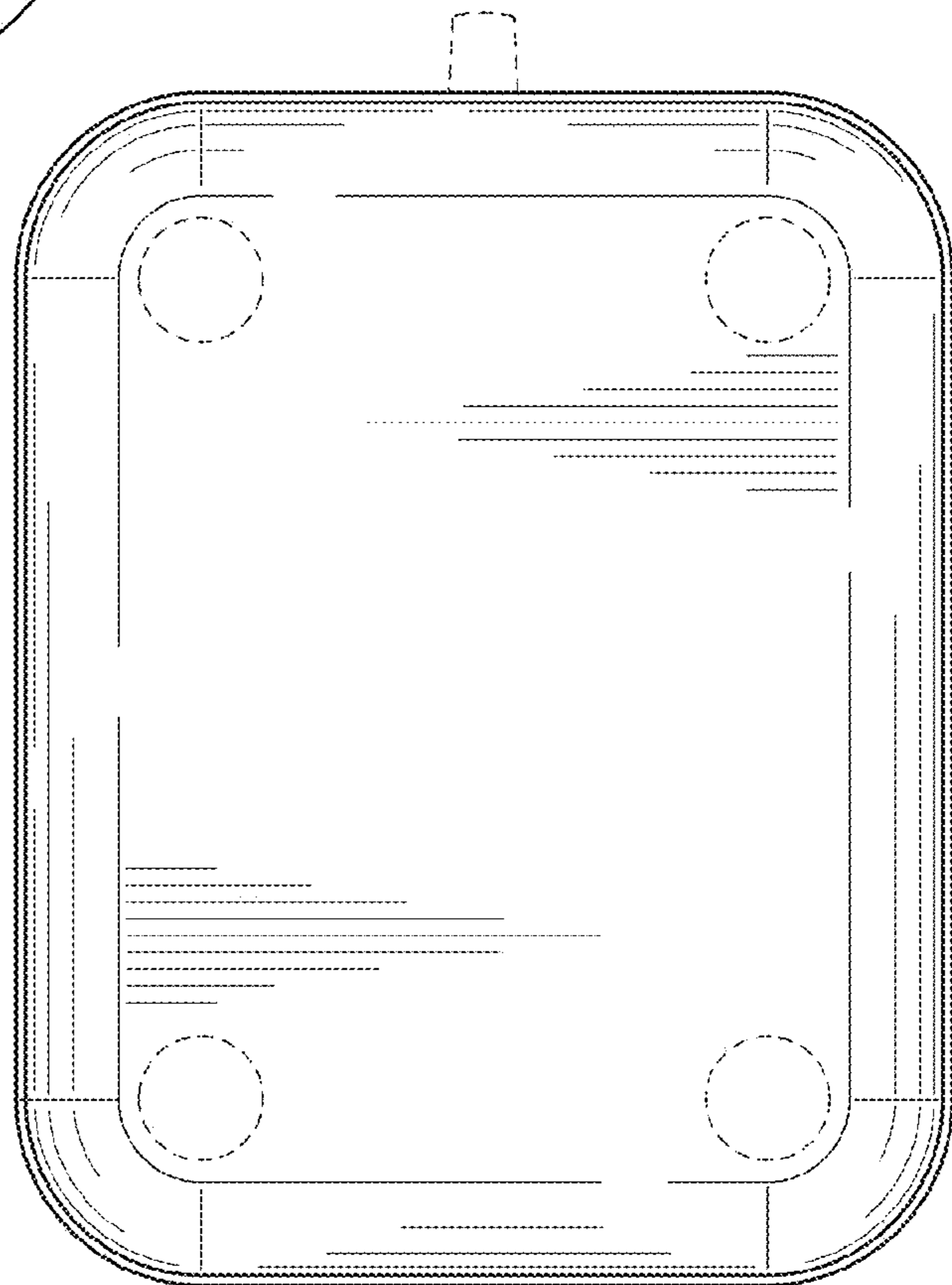


FIG. 7

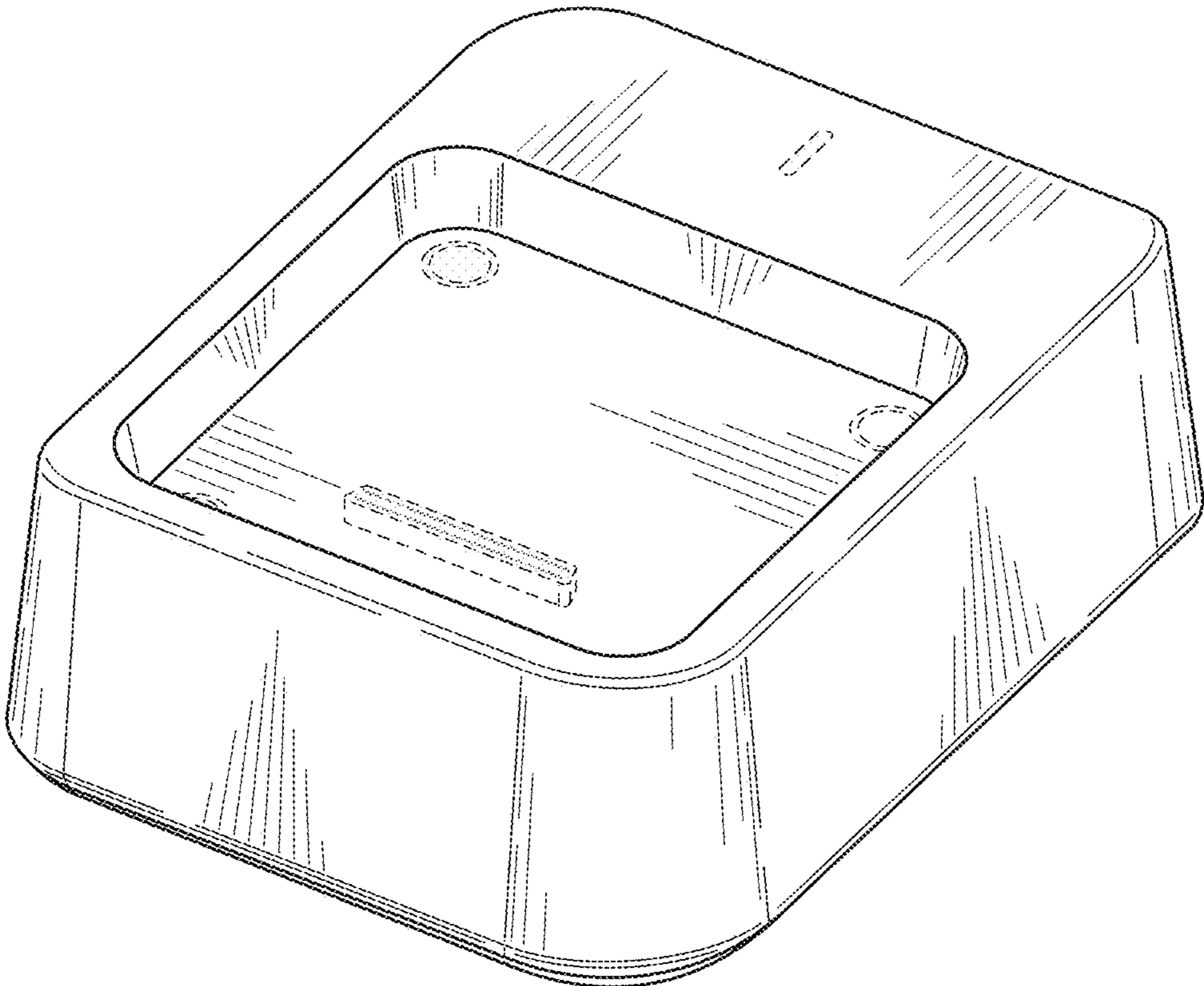


FIG. 8

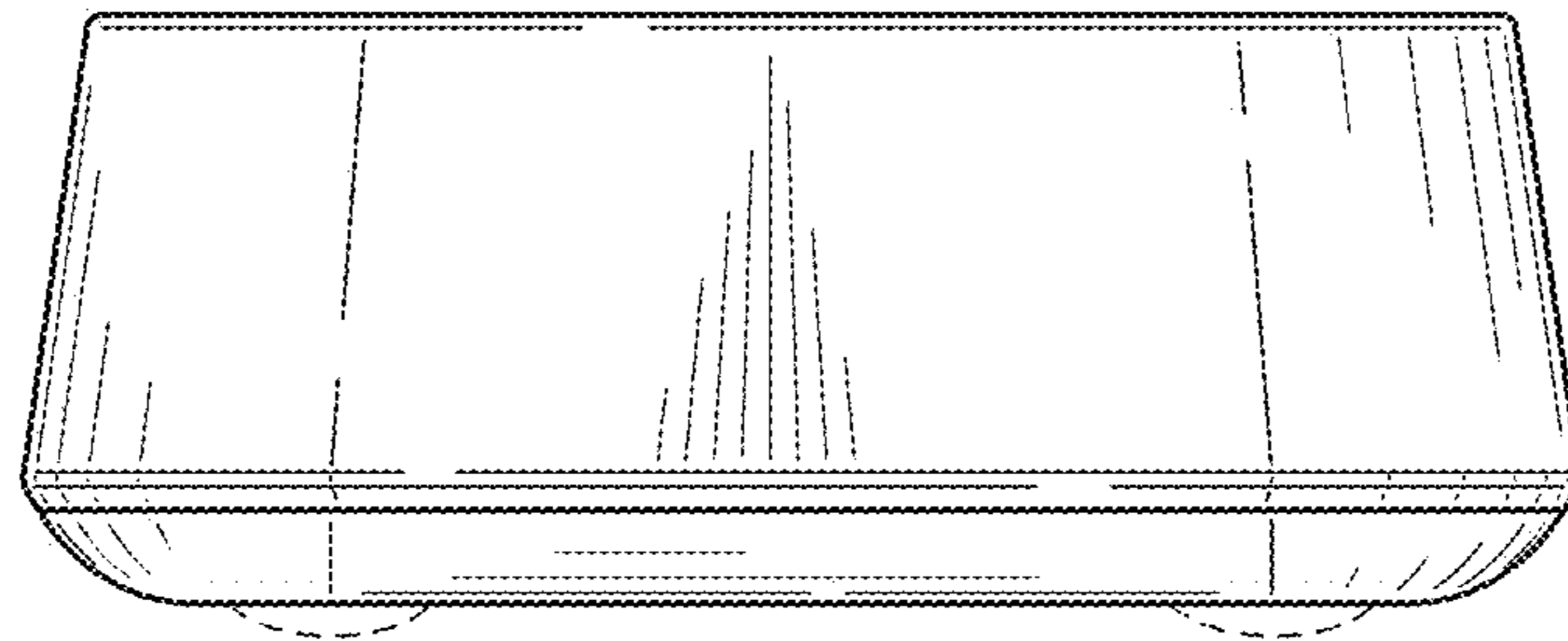


FIG. 9

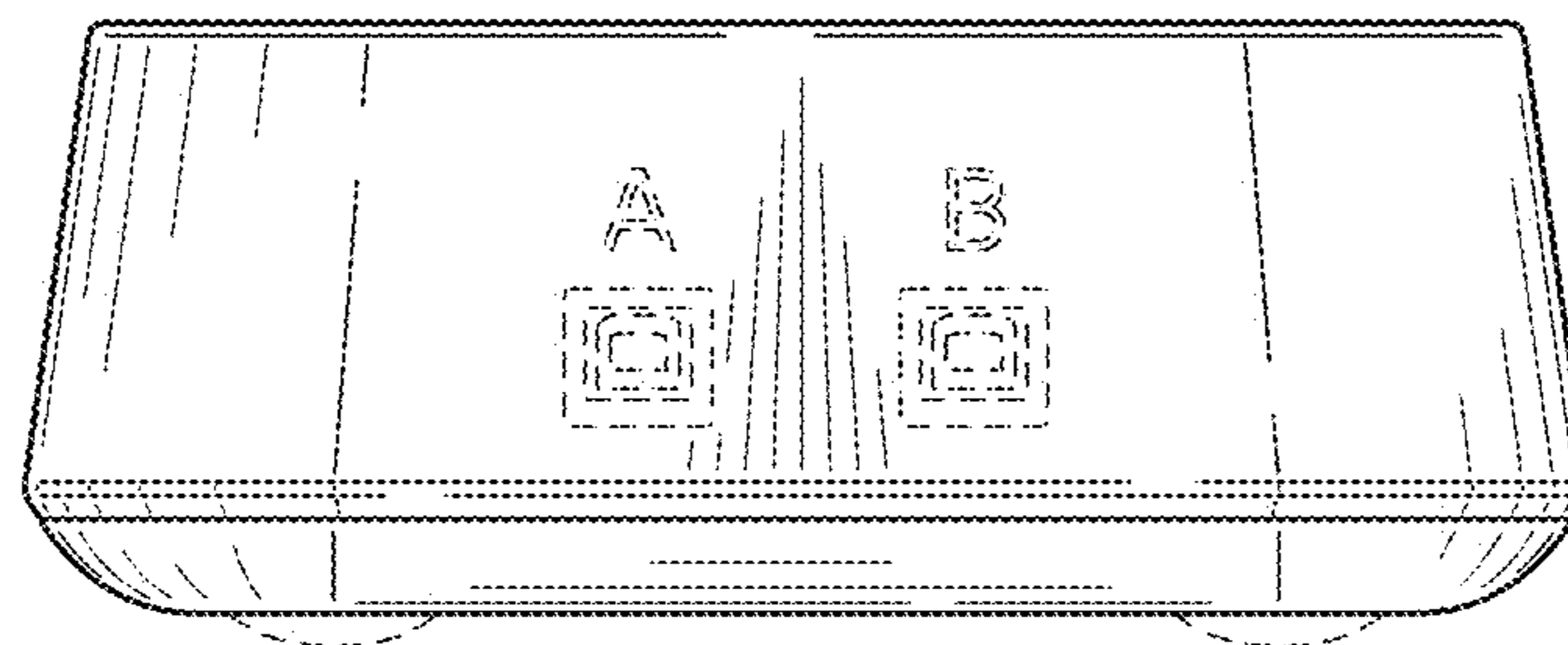


FIG. 10

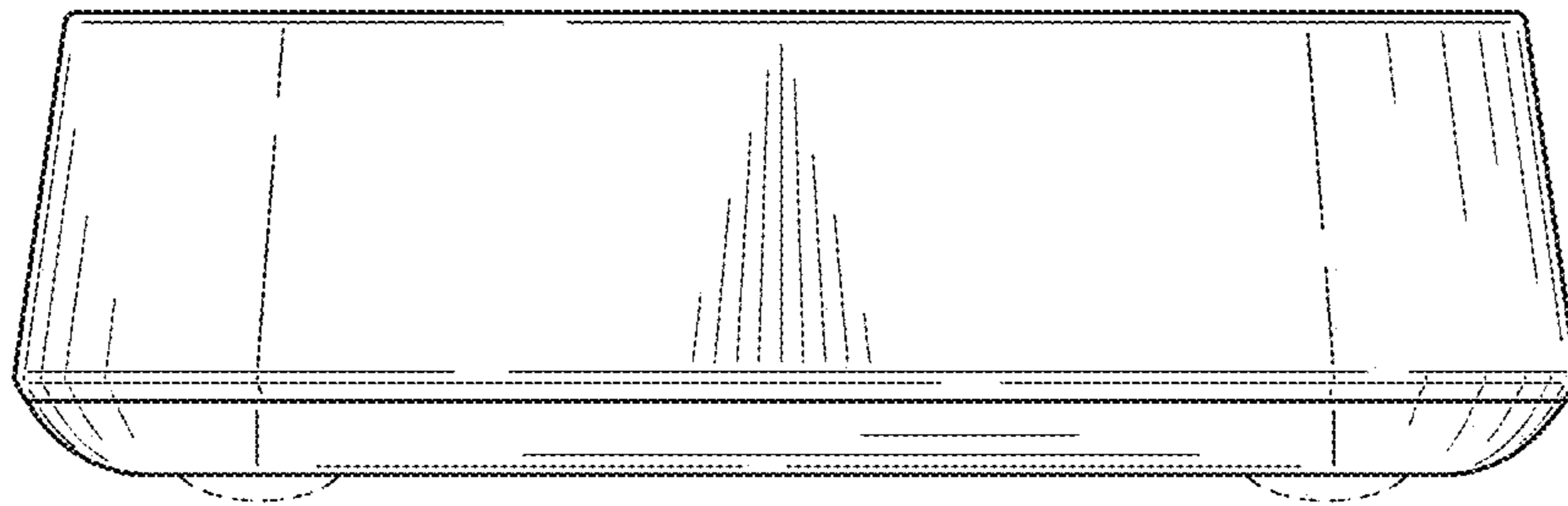


FIG. 11

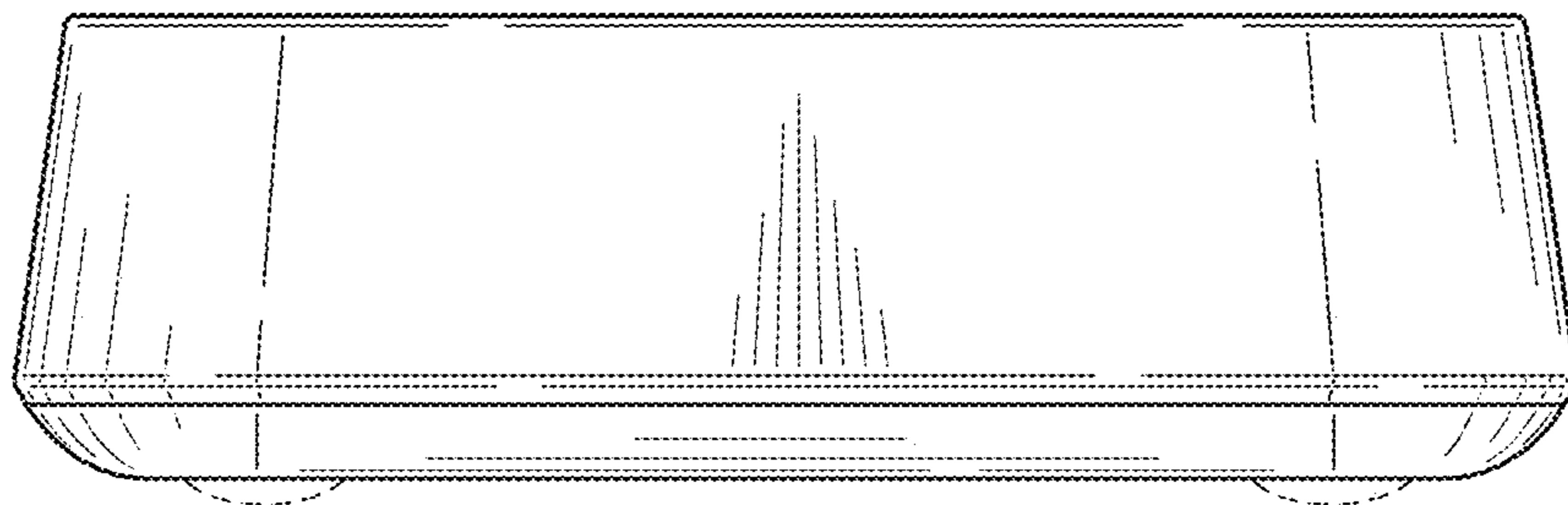


FIG. 12

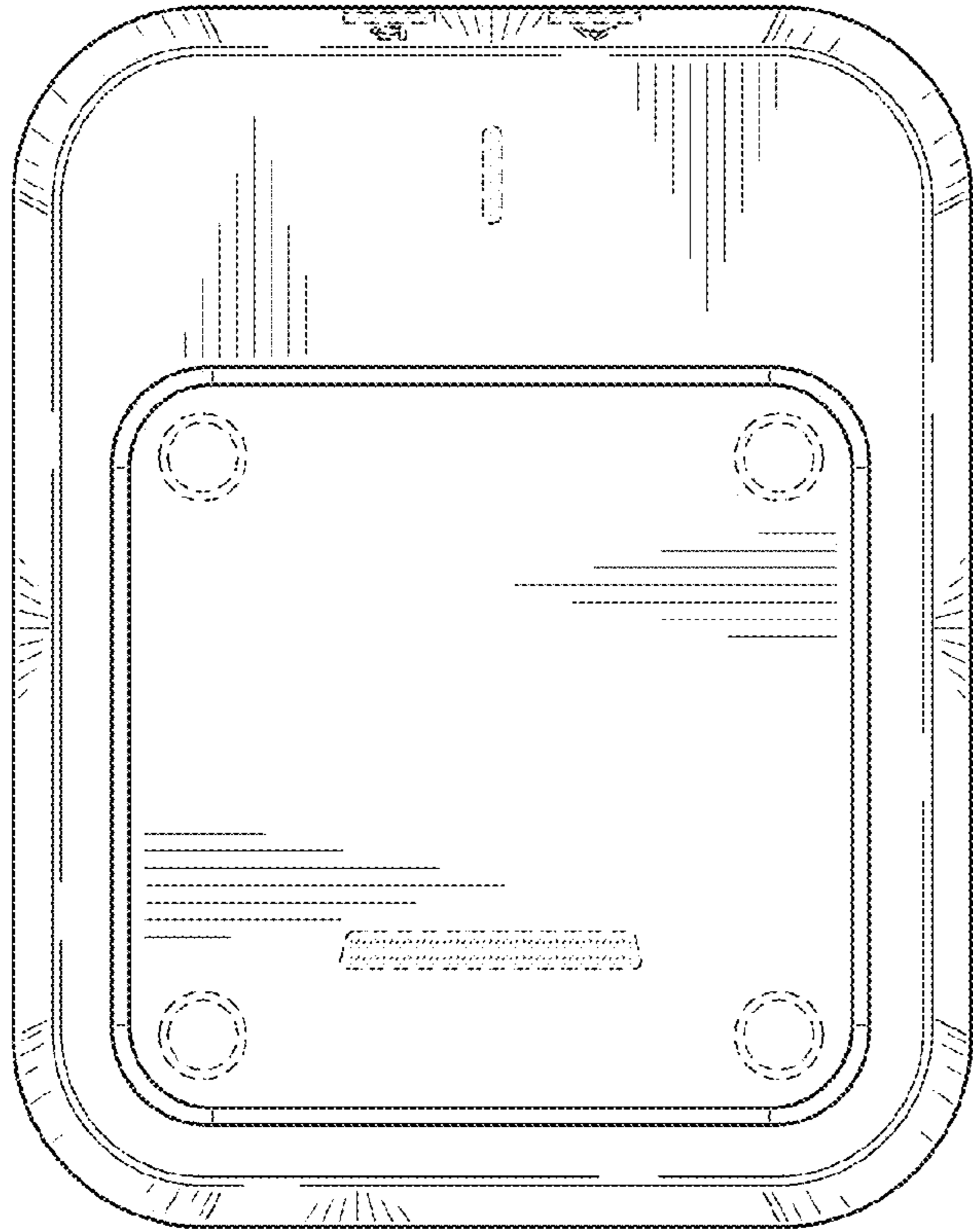


FIG. 13

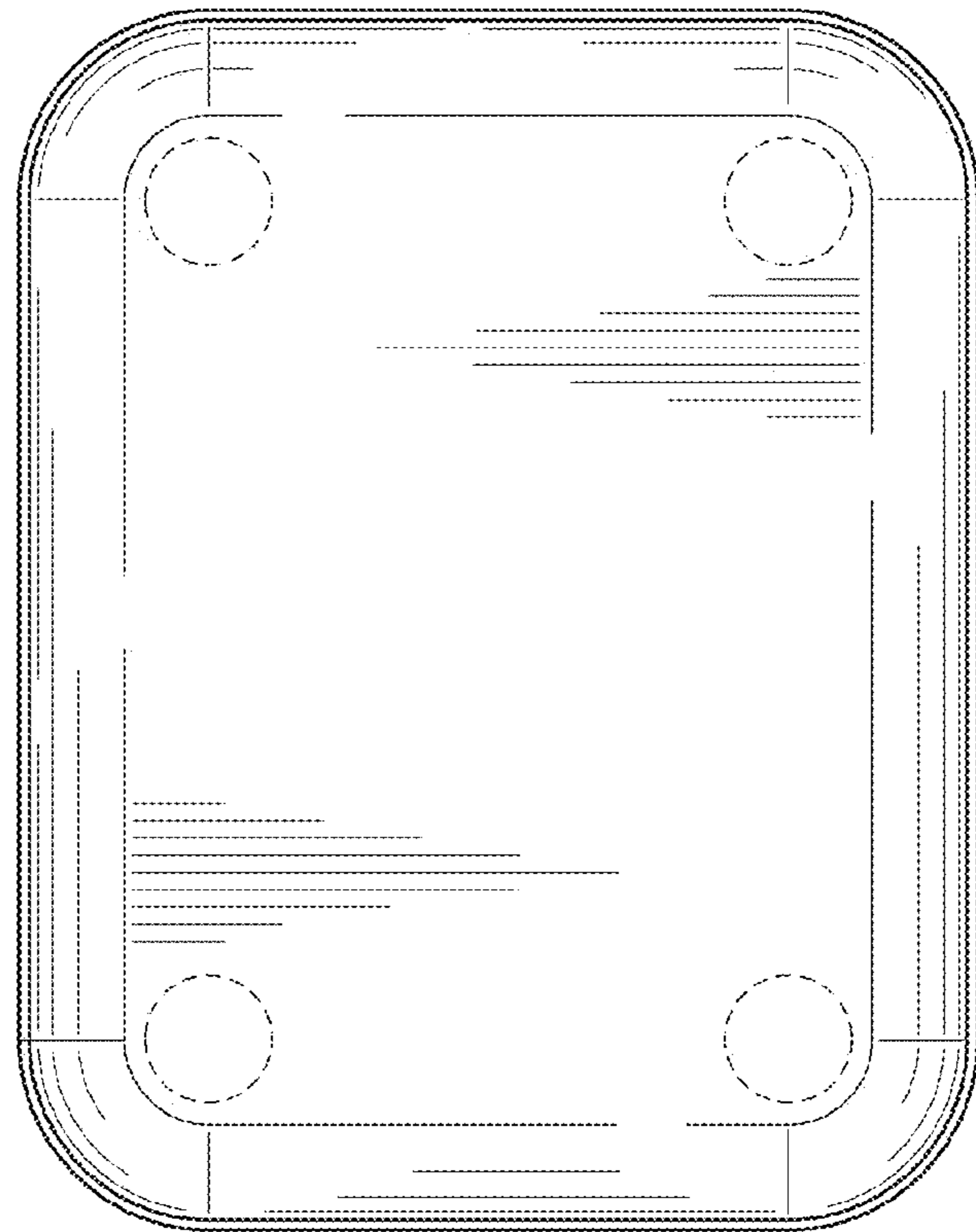


FIG. 14