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(12) **United States Design Patent**
Nada

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(45) **Date of Patent: ** May 18, 2021**

(54) **PROGRAMMABLE CONTROLLER**

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(73) Assignee: **OMRON Corporation**, Kyoto (JP)

(**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

Jun. 26, 2019 (JP) 2019-014180

(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/162.1**

(58) **Field of Classification Search**
USPC D13/123, 162.1, 164, 184; D15/138
CPC G05B 9/02; G05B 19/05; G05B 19/054; G05B 19/056
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D278,710	S	*	5/1985	Shimizu	D13/162.1
D292,394	S	*	10/1987	Boucher	D13/162.1
D302,972	S	*	8/1989	Boucher	D13/162.1
D307,740	S	*	5/1990	Shibayama	D13/162.1
D325,900	S	*	5/1992	Shimizu	D13/162.1
D351,589	S	*	10/1994	Shimizu	D13/162.1
D358,369	S	*	5/1995	Shimizu	D13/164
D418,483	S	*	1/2000	Shimizu	D13/162.1
D478,873	S	*	8/2003	Droulin	D13/162.1
D480,055	S	*	9/2003	Droulin	D13/162.1
D480,367	S	*	10/2003	Droulin	D13/162.1
D480,368	S	*	10/2003	Droulin	D13/162.1
D480,369	S	*	10/2003	Droulin	D13/162.1
D481,014	S	*	10/2003	Droulin	D13/162.1
D482,005	S	*	11/2003	Droulin	D13/162.1
D482,663	S	*	11/2003	Droulin	D13/162.1
D488,133	S	*	4/2004	Droulin	D13/162.1

(Continued)

FOREIGN PATENT DOCUMENTS

CN	208781917 U	4/2019

OTHER PUBLICATIONS

Heita Nada, Programmable Controller, Design U.S. Appl. No. 29/712,193, filed Nov. 6, 2019, in the USPTO.

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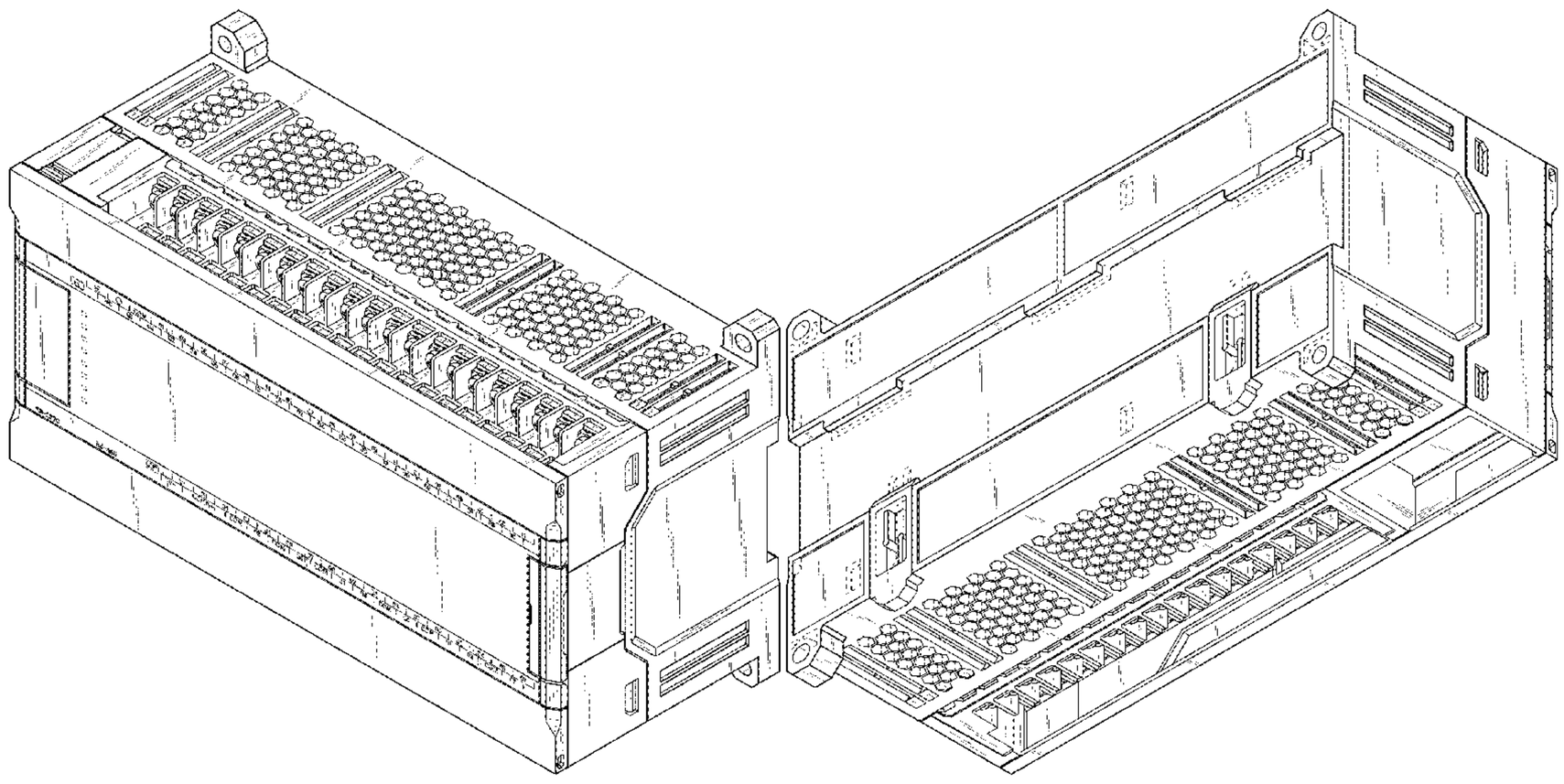
(57) **CLAIM**

The ornamental design for a programmable controller, as shown and described.

DESCRIPTION

FIG. **1** is a top, front, and right side perspective view of a programmable controller showing my new design;
FIG. **2** is a bottom, rear, and left side perspective view thereof;
FIG. **3** is a front view thereof;
FIG. **4** is a rear view thereof;
FIG. **5** is a left side view thereof;
FIG. **6** is a right side view thereof;
FIG. **7** is a top view thereof;
FIG. **8** is a bottom view thereof;
FIG. **9** is a top, front, and right side perspective view thereof with terminal block covers in an opened position;
FIG. **10** is a top, front, and right side perspective view thereof with a USB port cover in an opened position; and,
FIG. **11** is a front view thereof with the USB port cover in an opened position.
The dashed broken lines in the figures show portions of the programmable controller that form no part of the claimed design.

1 Claim, 10 Drawing Sheets



(56) **References Cited**

U.S. PATENT DOCUMENTS

D770,975	S	*	11/2016	Nada	D13/110
D815,606	S	*	4/2018	Nada	D13/162.1
D857,636	S	*	8/2019	Ueda	D13/162.1
D857,637	S	*	8/2019	Ueda	D13/162.1
D868,006	S	*	11/2019	Ueda	D13/162.1
D892,740	S	*	8/2020	Nada	D13/123
D893,425	S	*	8/2020	Nada	D13/123
D896,766	S	*	9/2020	Kato	D13/162.1
D897,294	S	*	9/2020	Kato	D13/162.1

* cited by examiner

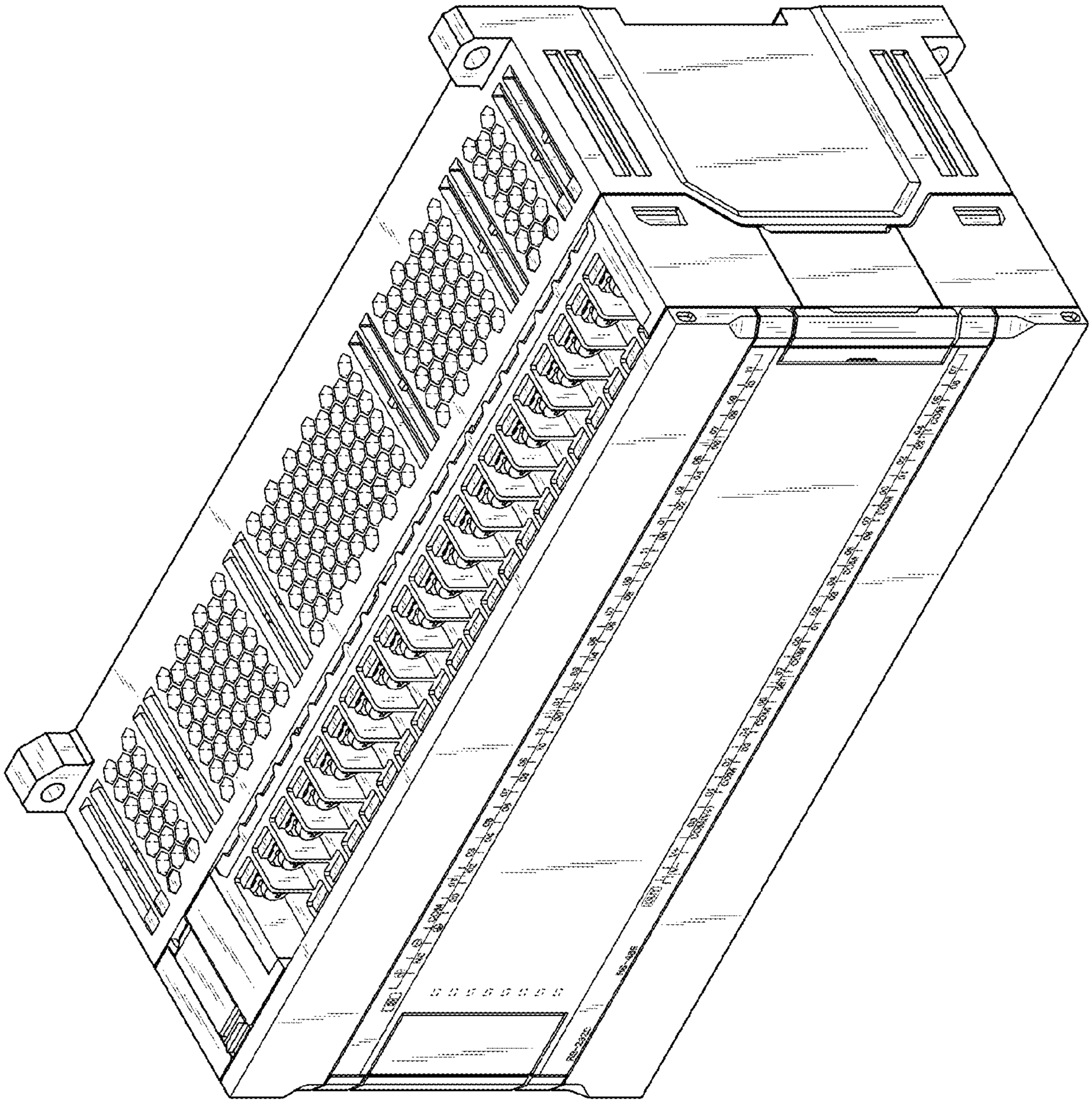


Fig. 1

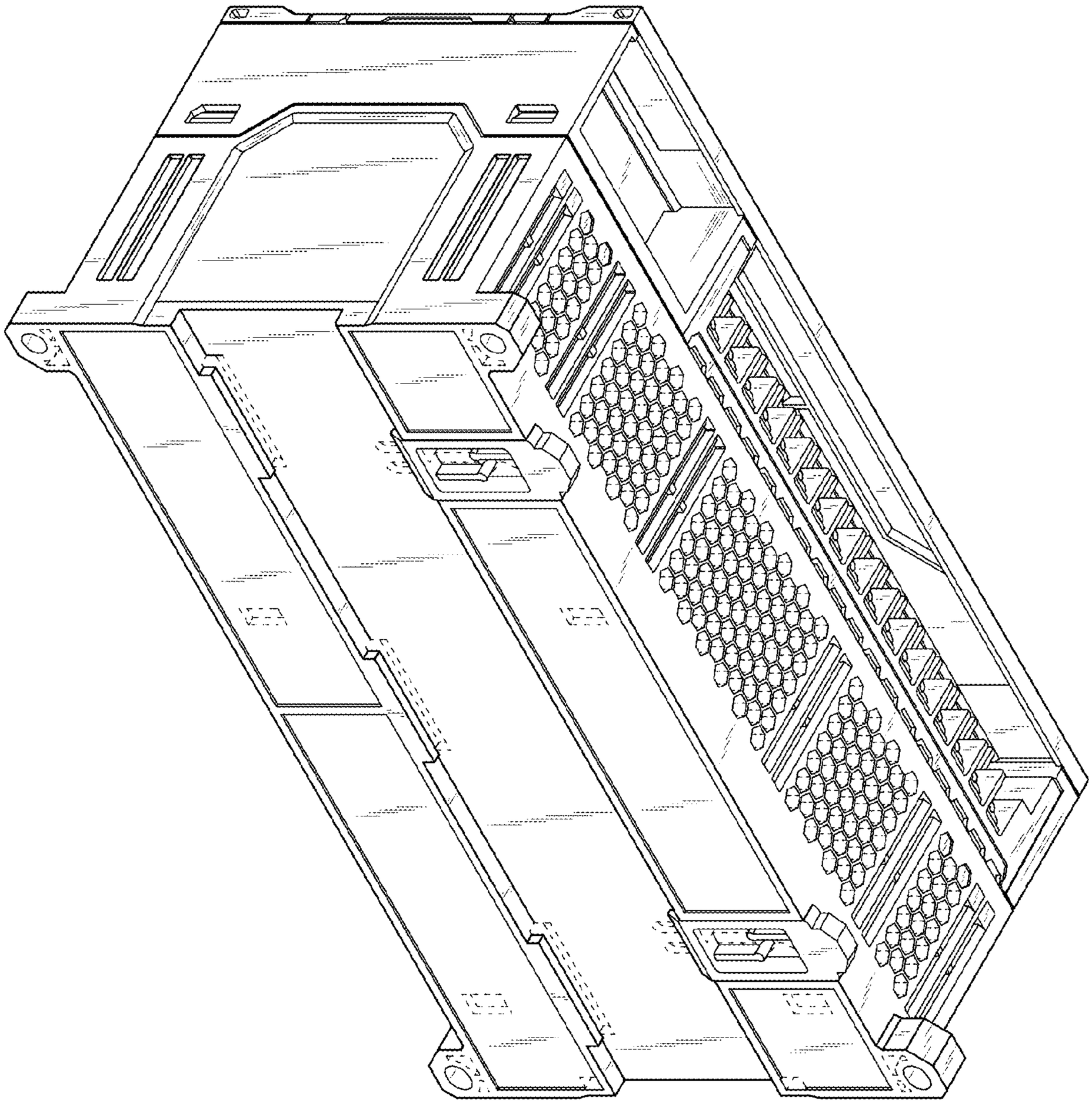


Fig. 2

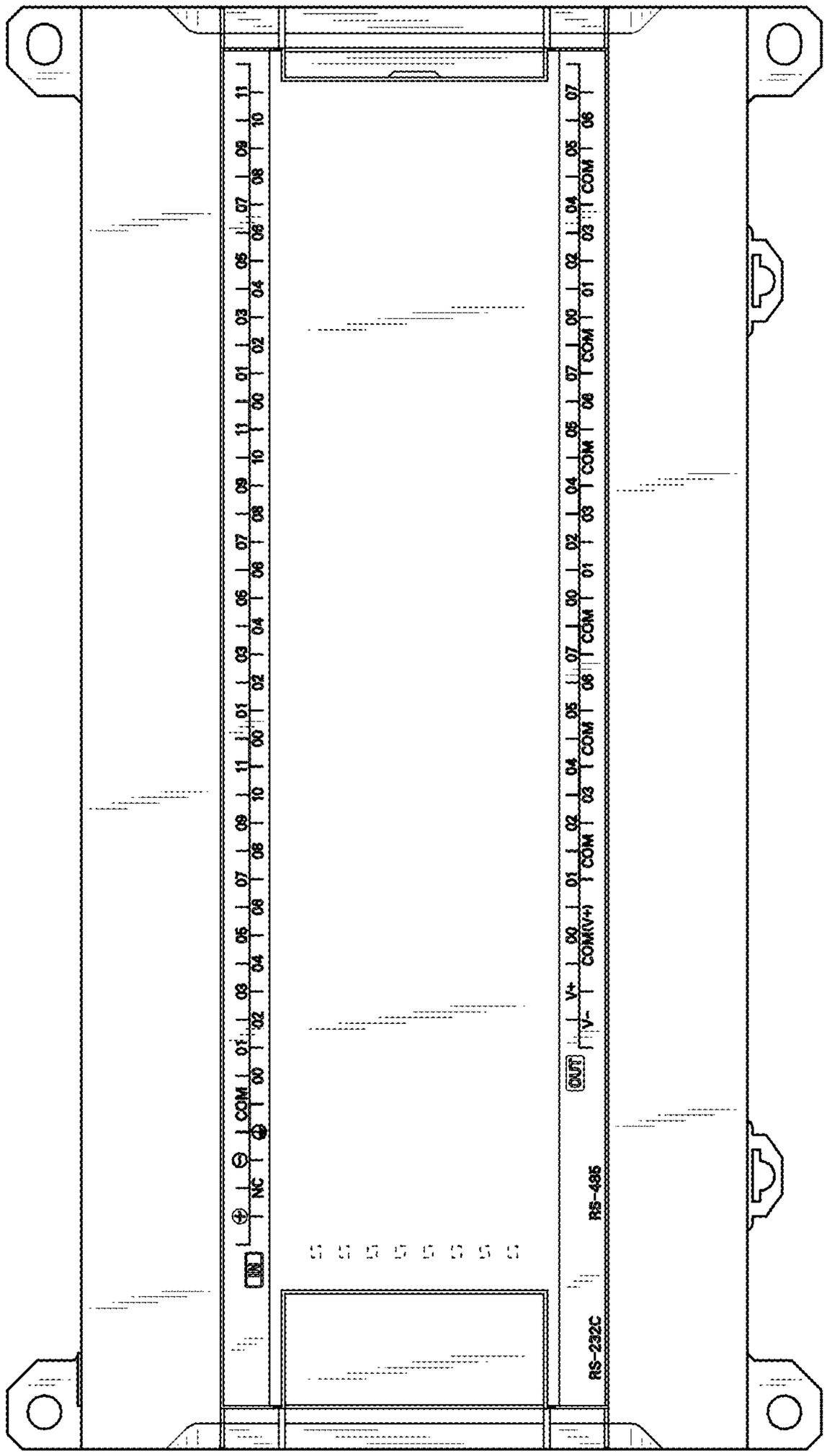


Fig. 3

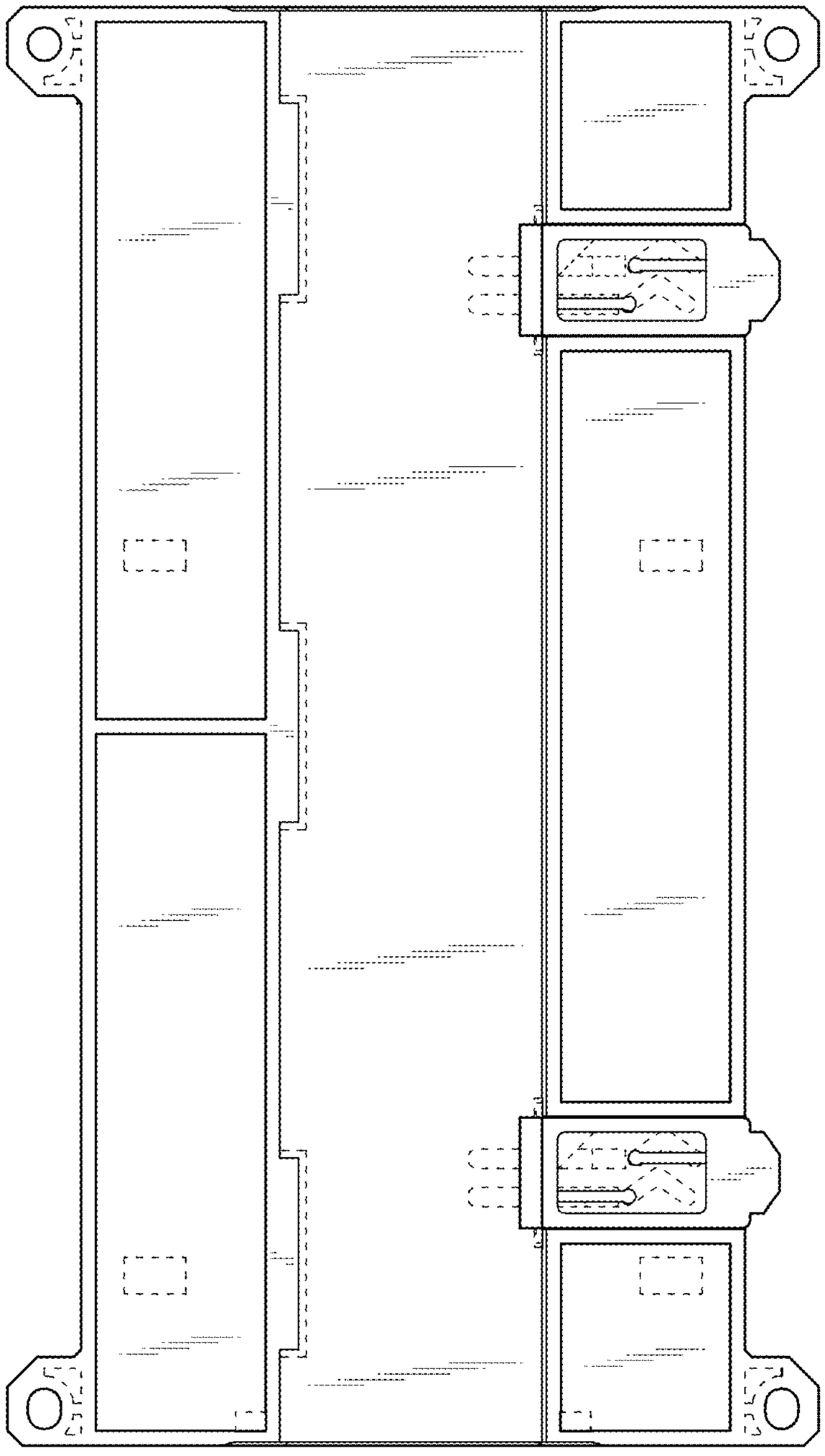


Fig. 4

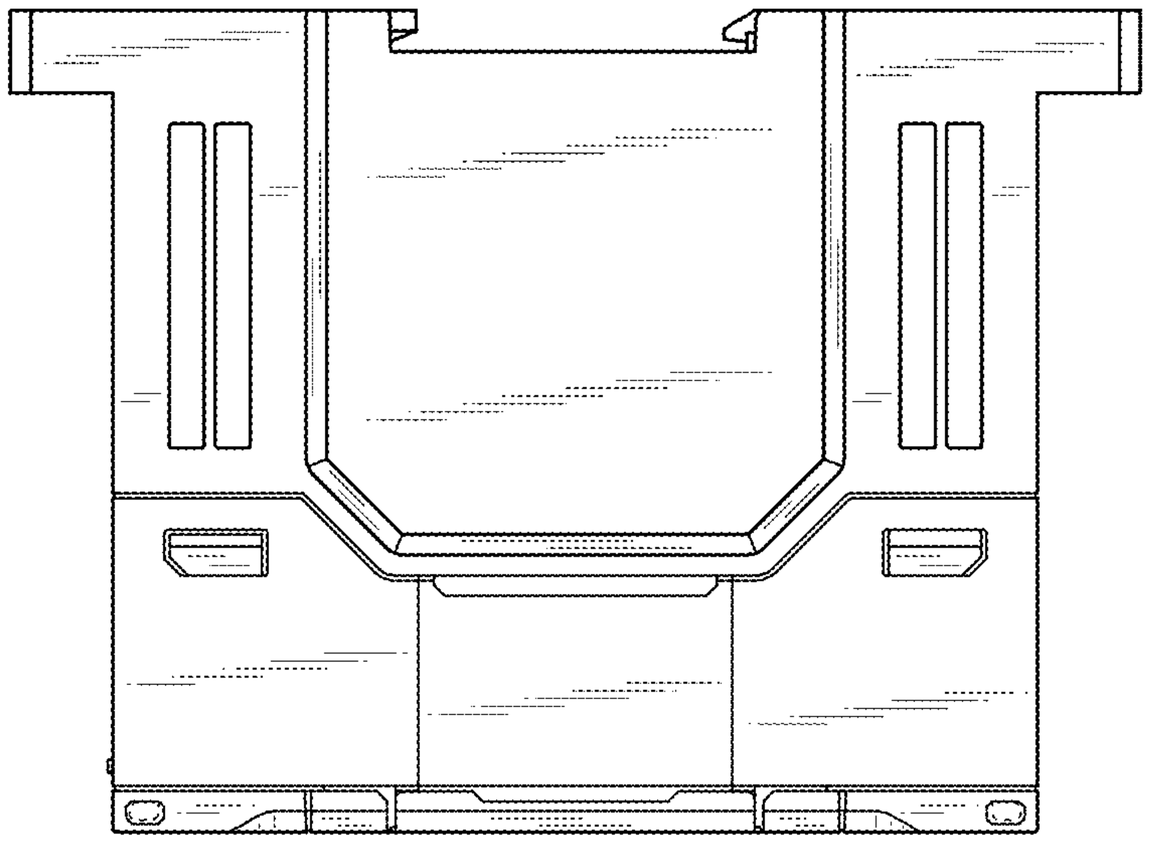


Fig. 6

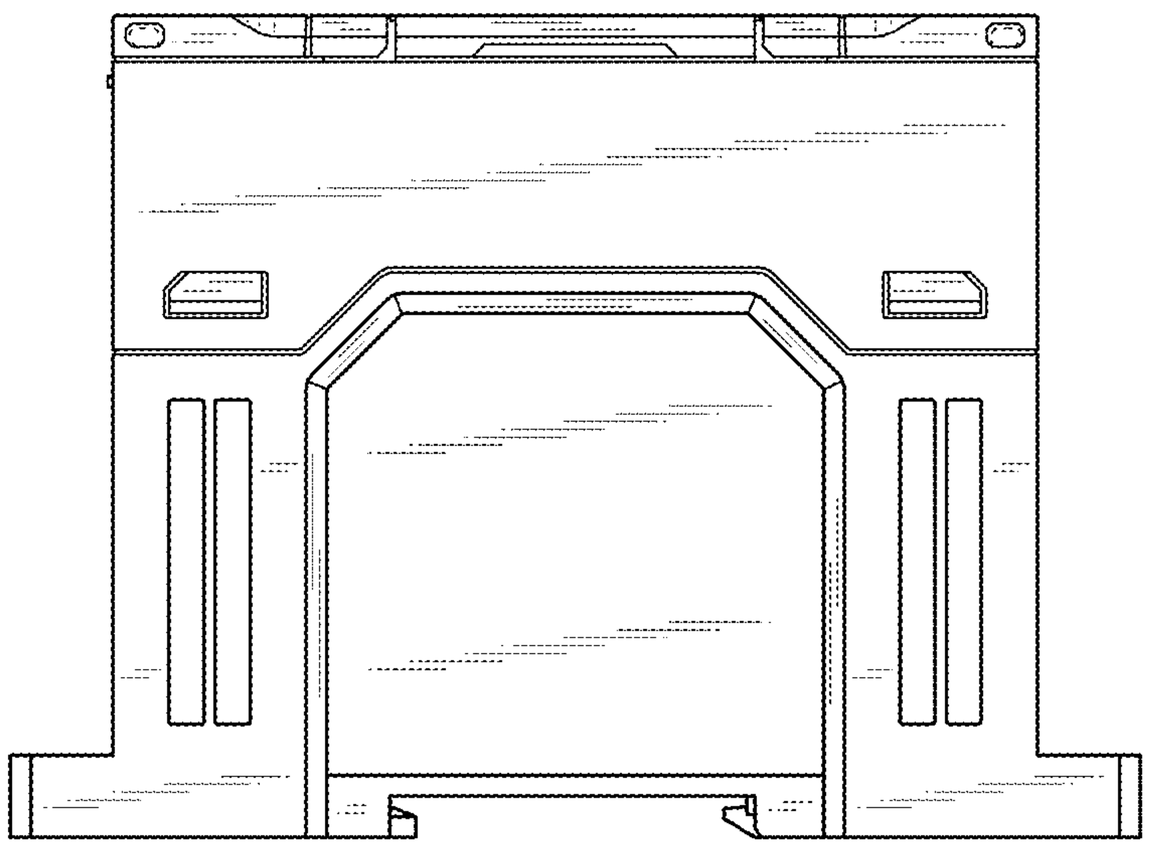


Fig. 5

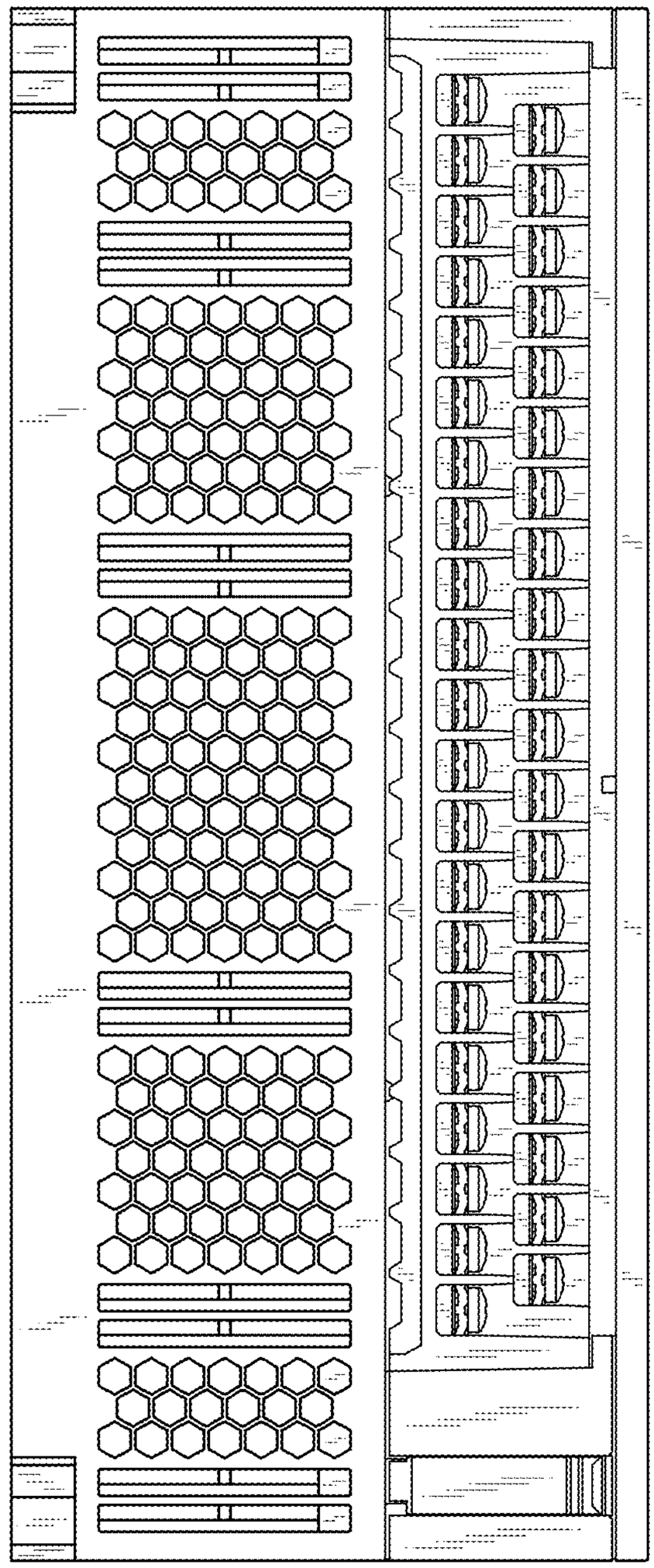


Fig. 7

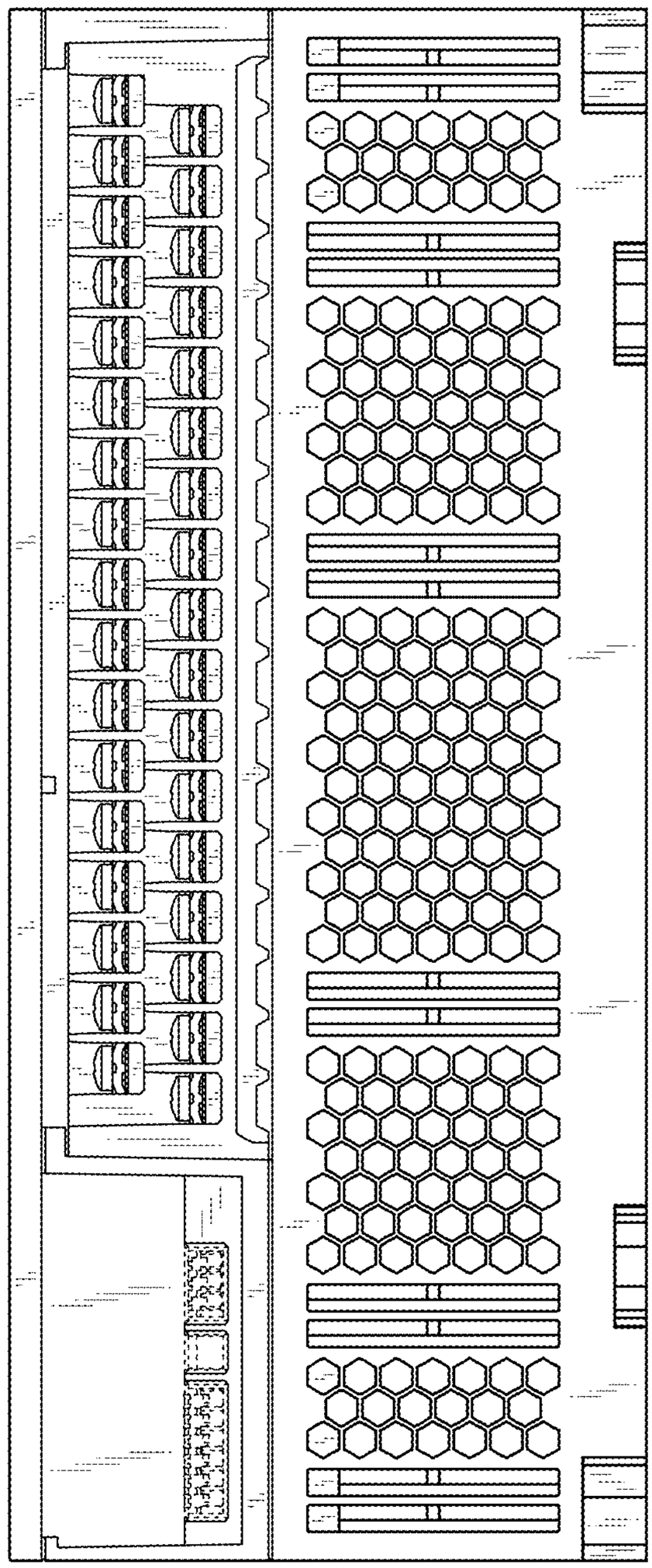


Fig. 8

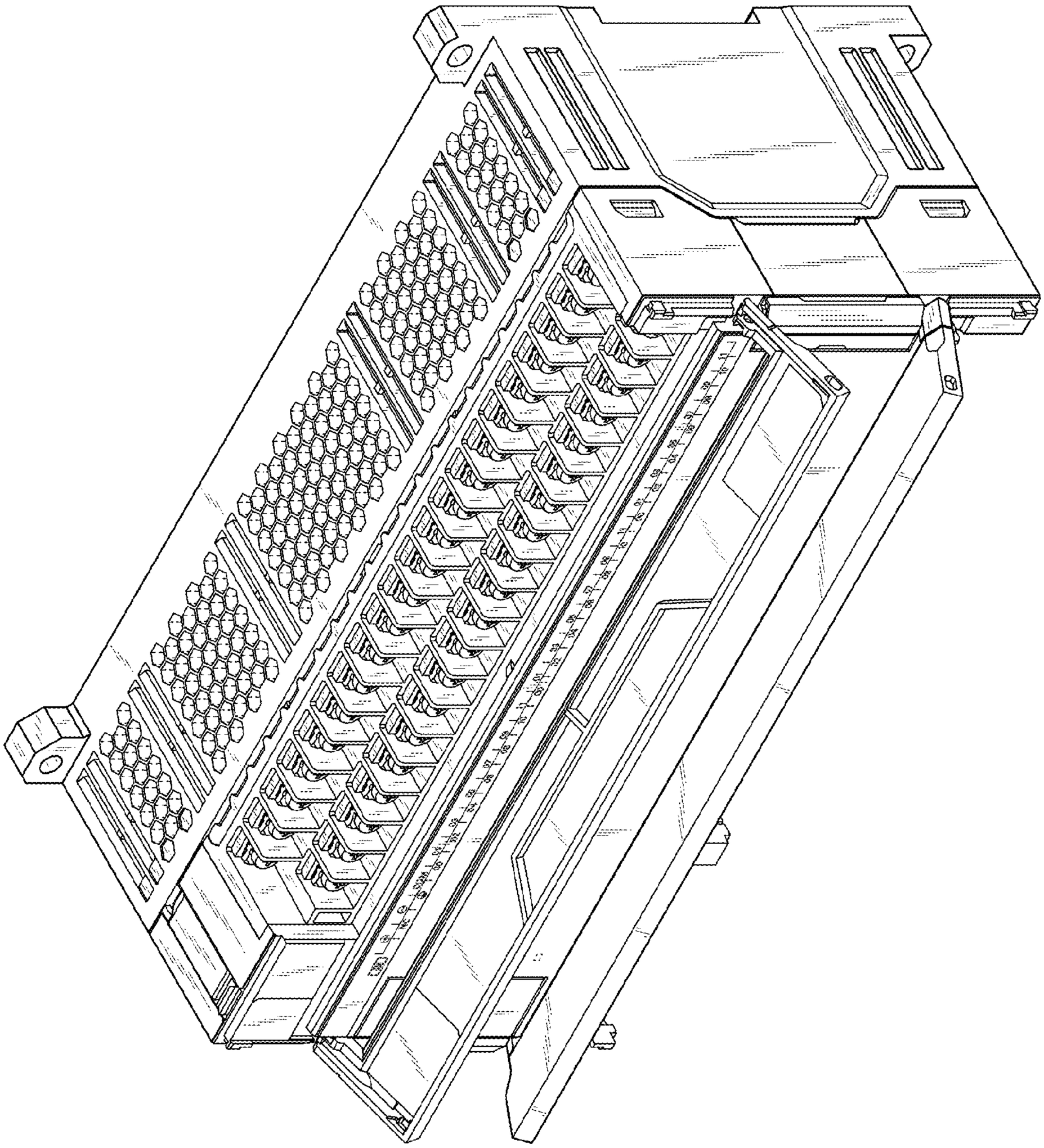


Fig. 9

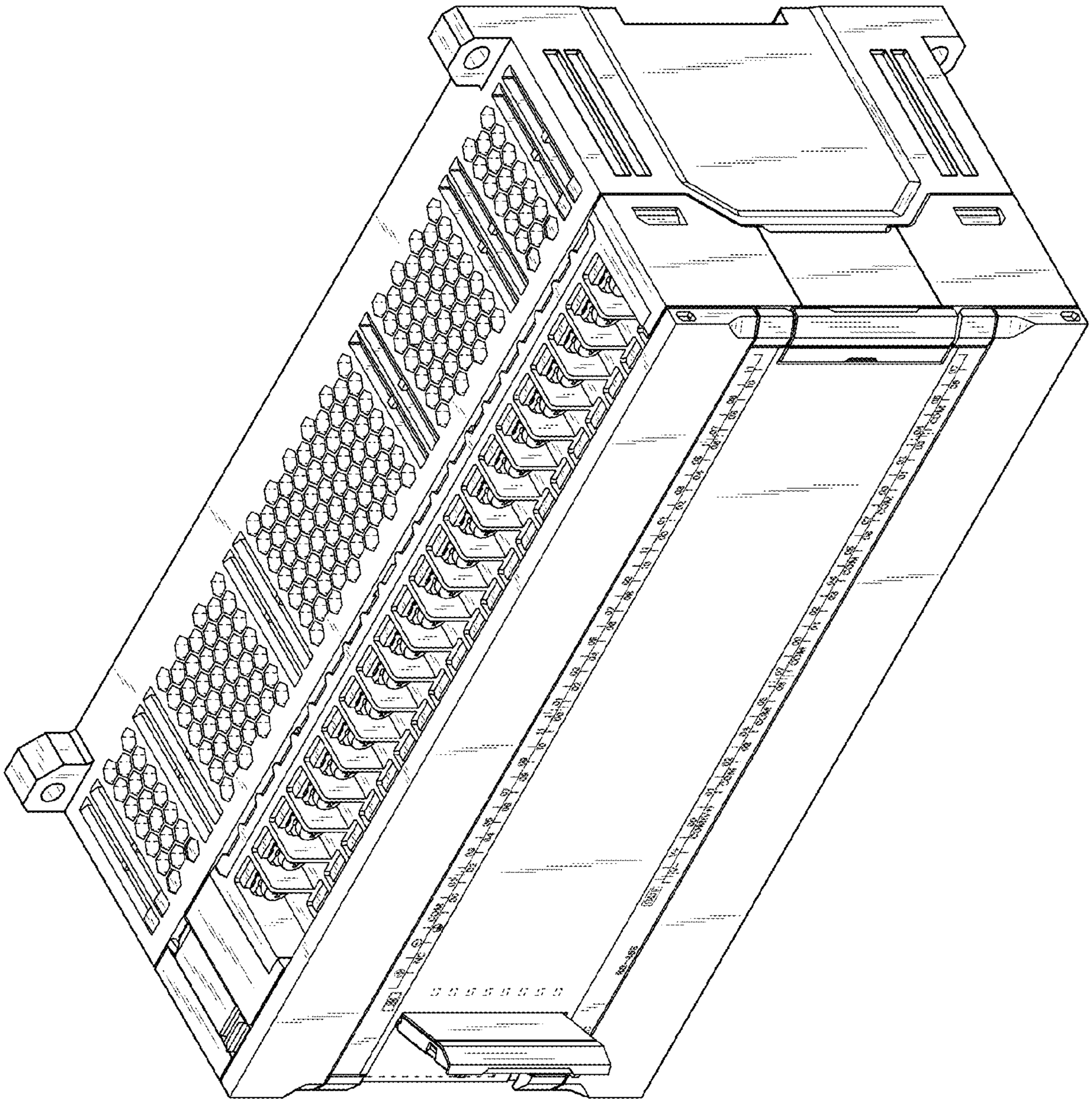


Fig. 10

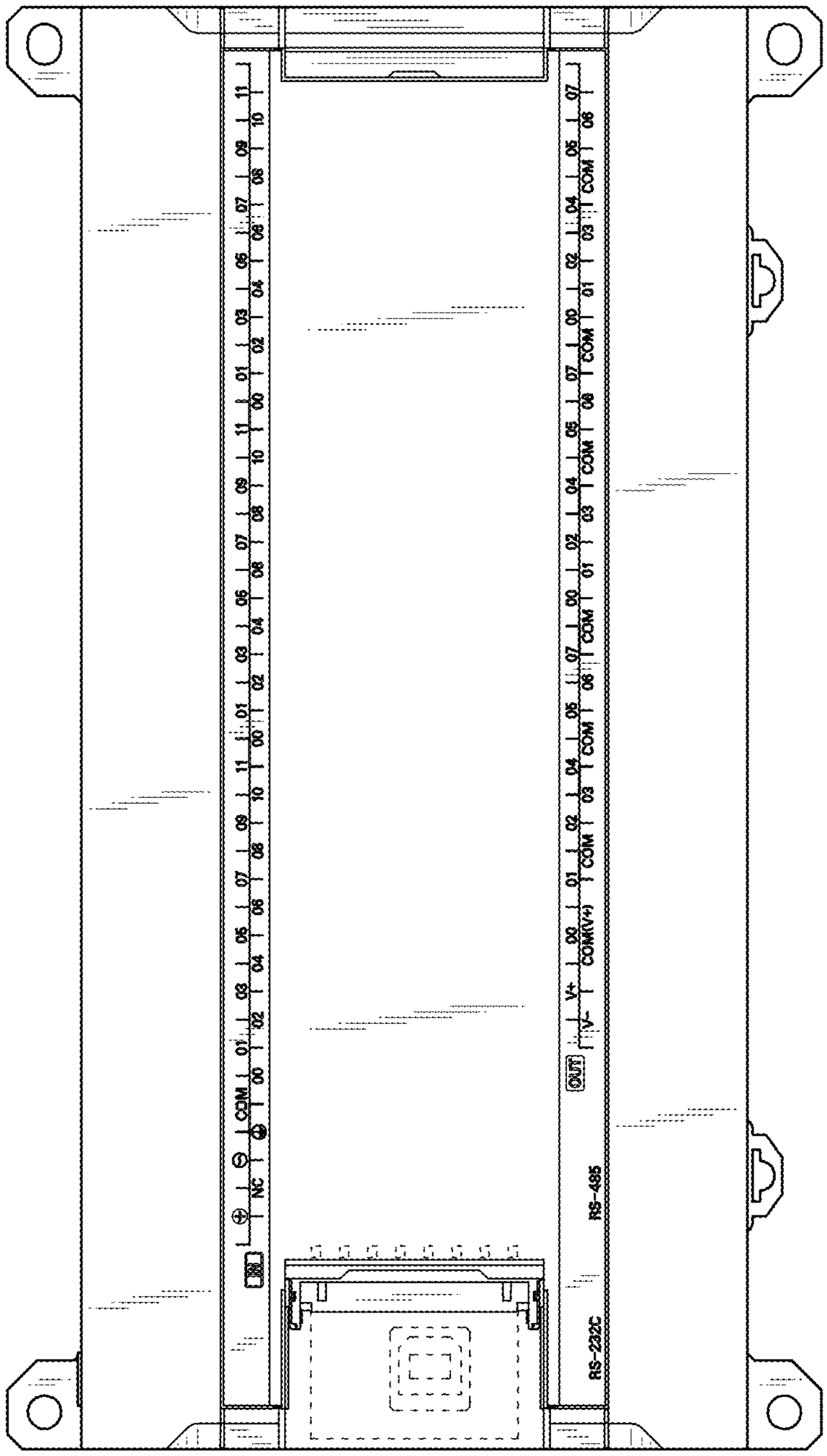


Fig. 11