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(12) **United States Design Patent** (10) **Patent No.:** **US D916,972 S**
Dunn (45) **Date of Patent:** **** Apr. 20, 2021**

(54) **COMBINED ELECTRONIC DISPLAY ASSEMBLY AND POLE**

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(**) Term: **15 Years**

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(51) **LOC (13) Cl.** **20-02**

(52) **U.S. Cl.**
USPC **D20/41**

(58) **Field of Classification Search**
USPC D10/109.1, 109.2, 113.4; D11/181, 182, D11/183; D14/125, 126, 127, 128, 129; D18/26; D20/10, 12, 16, 17, 18, 19, 21, D20/31, 29, 30, 39, 40, 41, 42, 43, 44, 99
CPC G09F 1/00; G09F 1/08; G09F 5/00; G09F 5/02; G09F 7/00; G09F 19/00; G09F 23/00; G09F 27/00; G09F 21/00; G09F 21/06; G09F 21/08; Y10S 362/812; A47F 3/00; A47F 8/00; A47F 11/00
See application file for complete search history.

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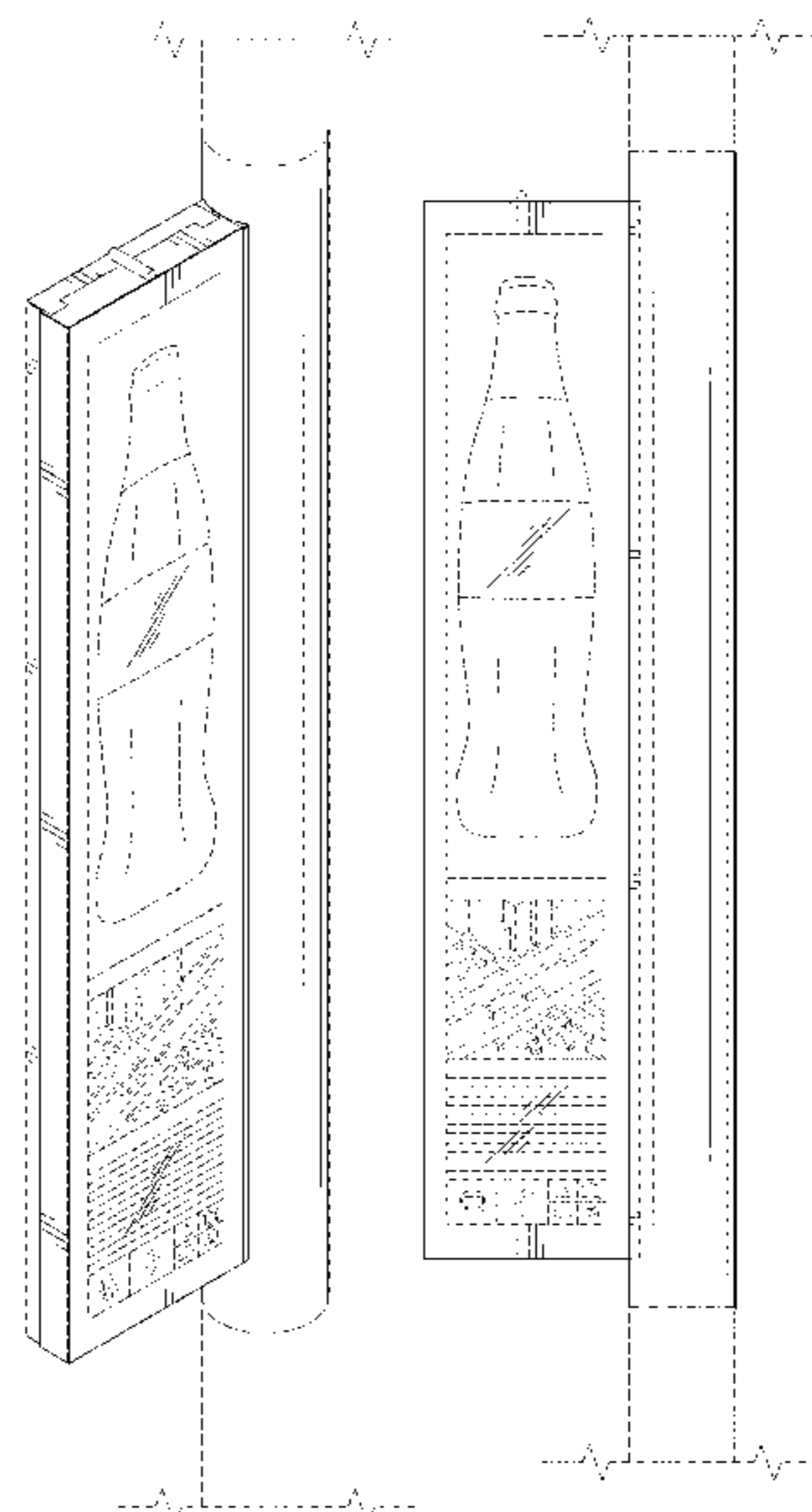
(57) **CLAIM**

I claim the ornamental design for a combined electronic display assembly and pole, as shown and described.

DESCRIPTION

FIG. 1 is a front perspective view of the combined electronic display assembly and pole, showing my new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a right side elevation view thereof;
FIG. 5 is a left side elevation view thereof;
FIG. 6 is a top view thereof;
FIG. 7 is a bottom view thereof; and,
FIG. 8 is a perspective view of the combined electronic display assembly and pole shown in an environment of use. The evenly spaced broken line portions show environment that form no part of the claimed design. The dot-dashed broken lines define the boundary of the claimed design and form no part thereof.

1 Claim, 5 Drawing Sheets



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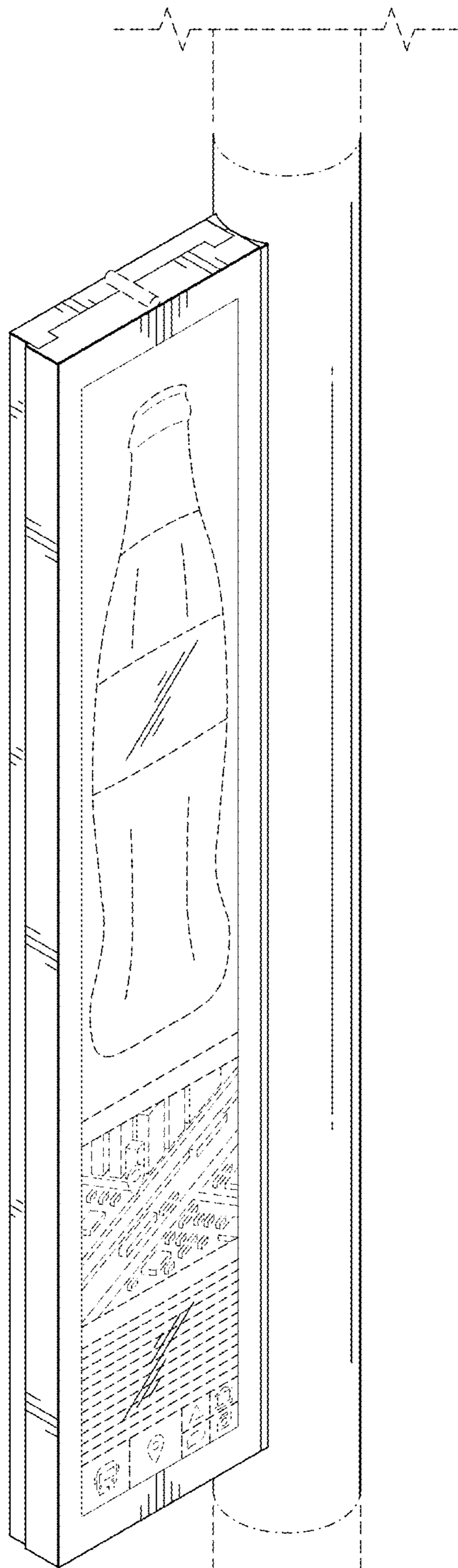


FIG. 1

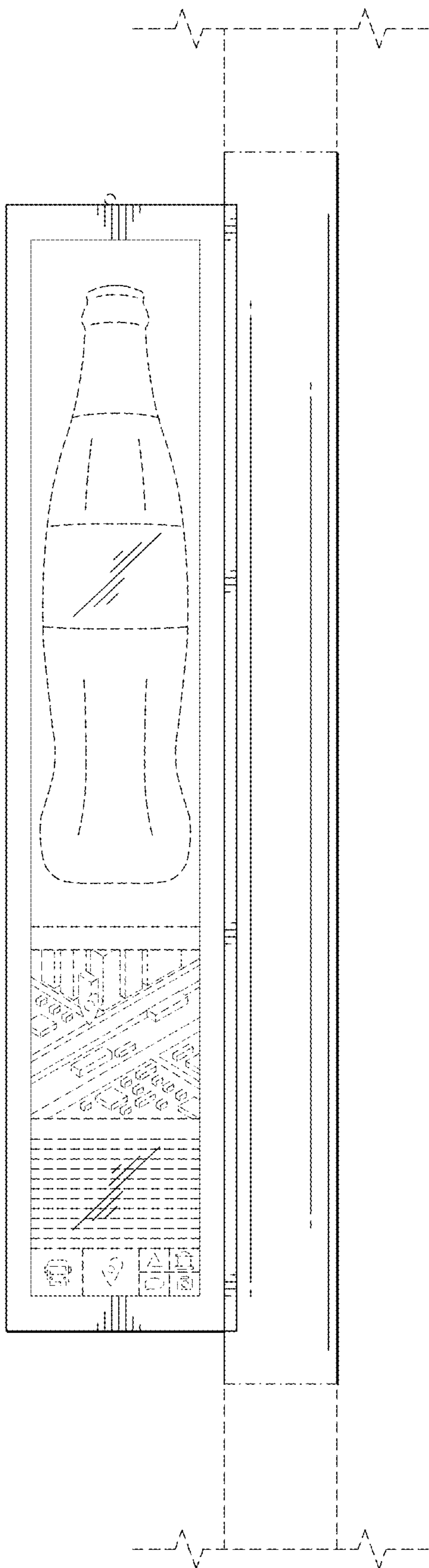


FIG. 2

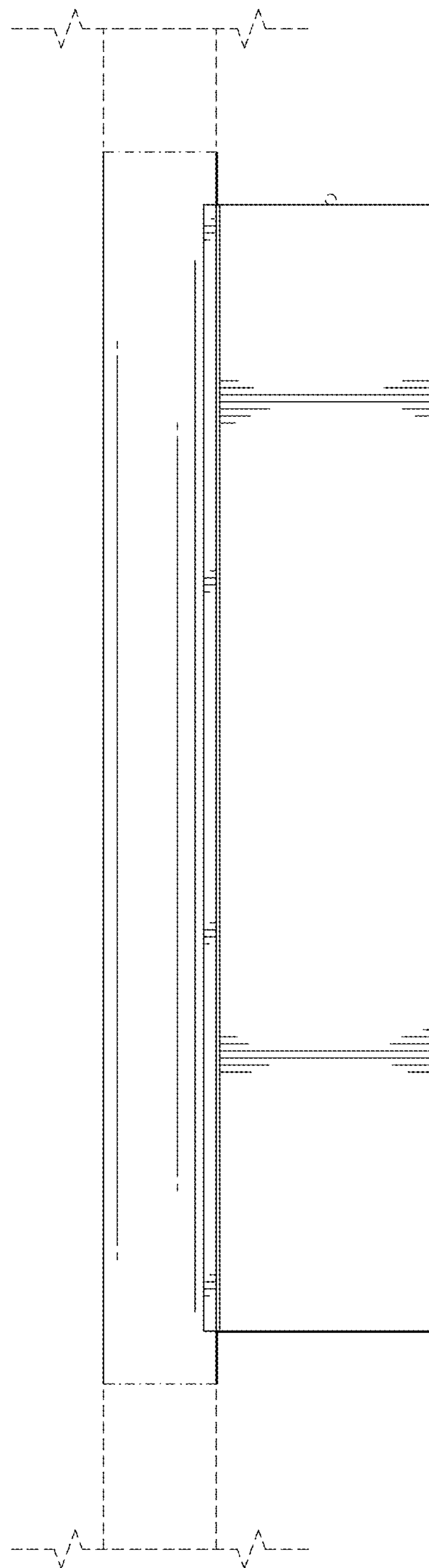


FIG. 3

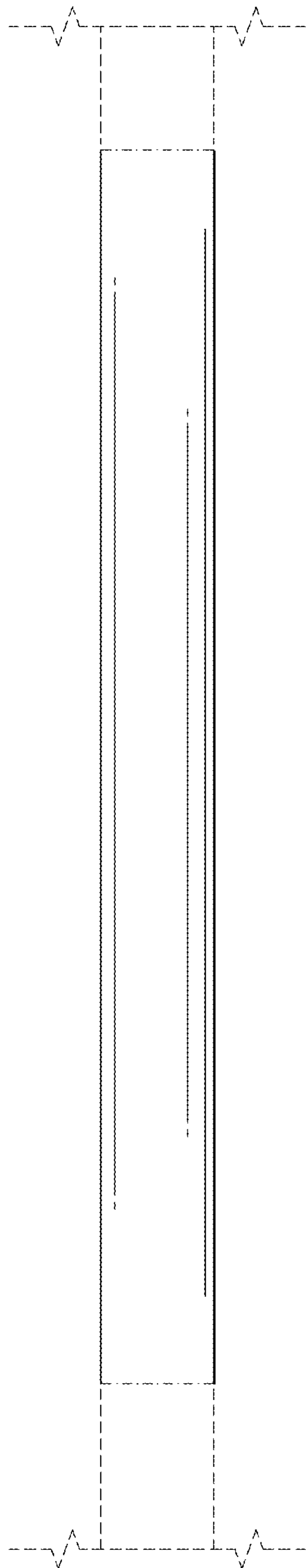


FIG. 4

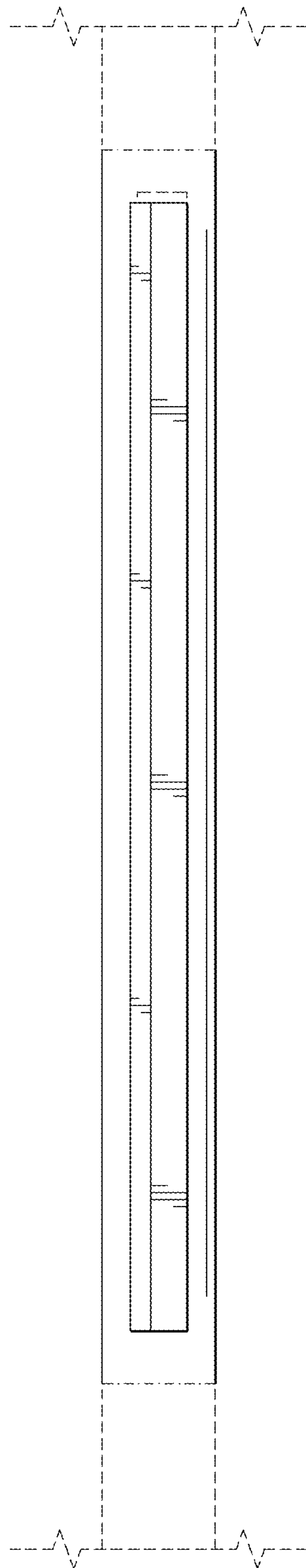


FIG. 5

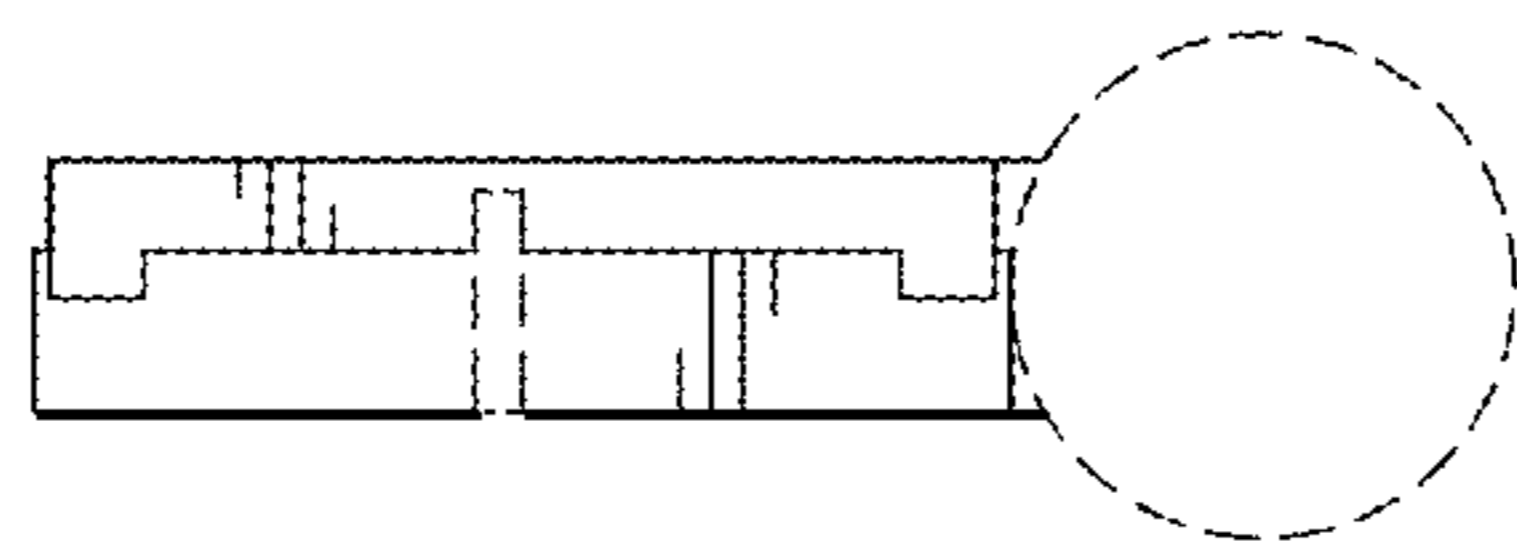


FIG. 6

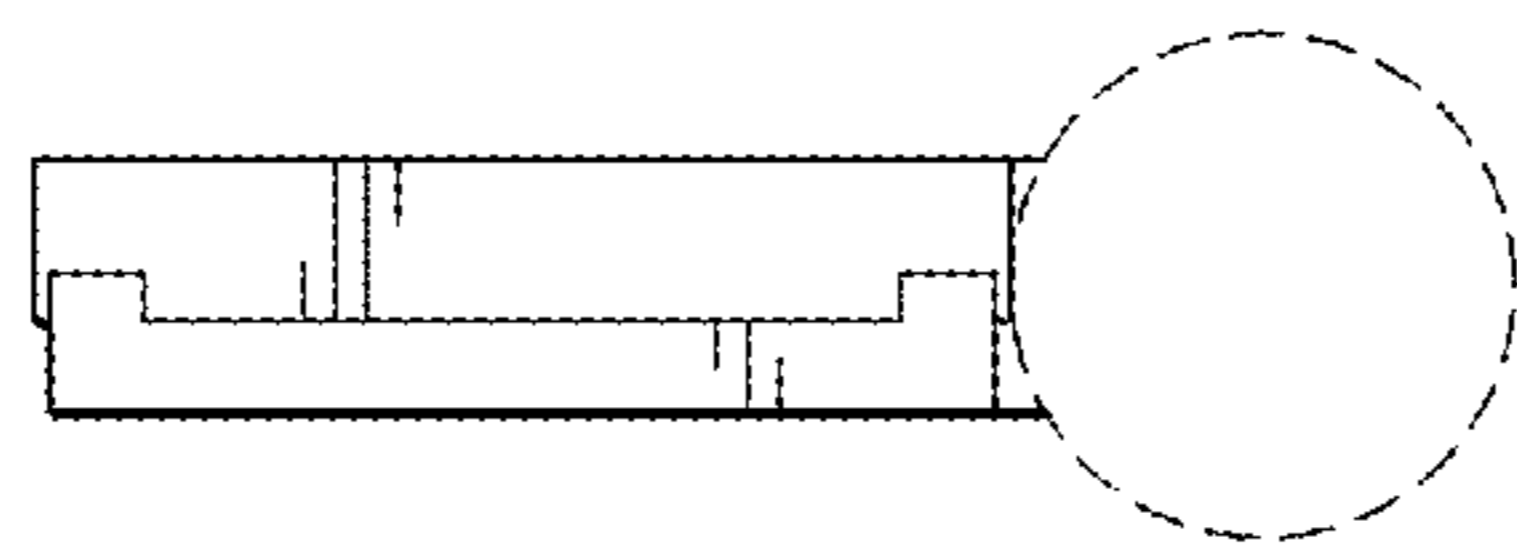


FIG. 7

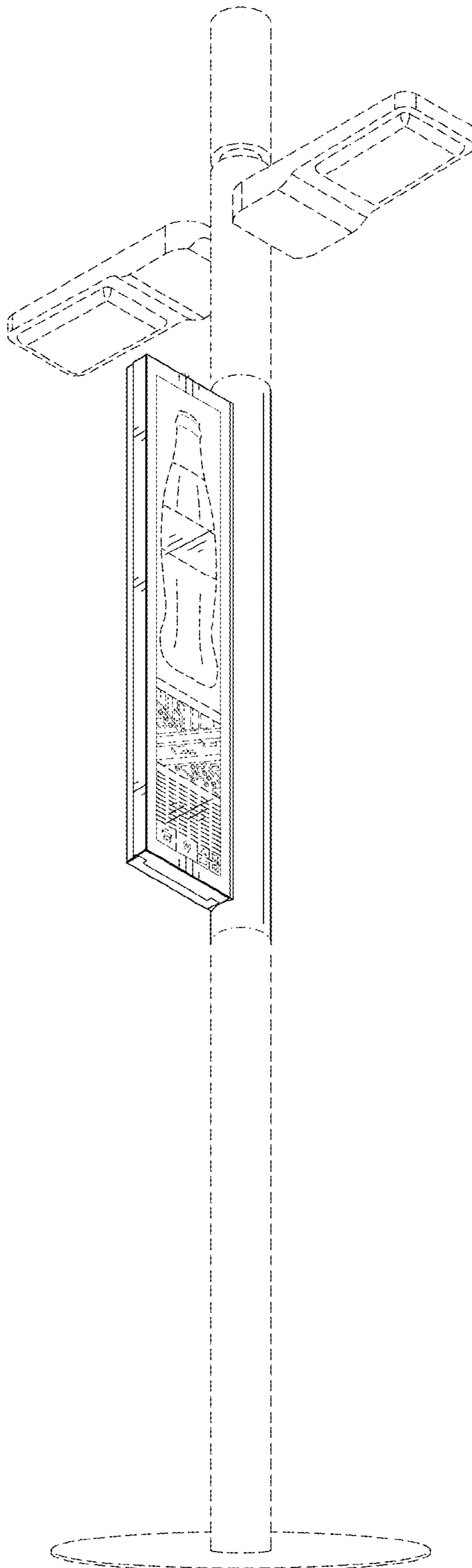


FIG. 8