



US00D915309S

(12) **United States Design Patent**
Maruyama

(10) **Patent No.:** **US D915,309 S**

(45) **Date of Patent:** **** Apr. 6, 2021**

- (54) **LIGHT EMITTING DIODE CHIP**
- (71) Applicant: **DOWA Electronics Materials Co., Ltd., Tokyo (JP)**
- (72) Inventor: **Tsukasa Maruyama, Akita (JP)**
- (73) Assignee: **DOWA Electronics Materials Co., Ltd., Tokyo (JP)**
- (**) Term: **15 Years**
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- (30) **Foreign Application Priority Data**
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- (51) **LOC (13) Cl.** **13-03**
- (52) **U.S. Cl.**
USPC **D13/180**
- (58) **Field of Classification Search**
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CPC ... H01L 25/167; H01L 25/0753; H01L 27/15;
H01L 27/156; H01L 33/00; H01L 33/04;
H01L 33/08; H01L 33/10; H01L 33/20;
H01L 33/38; H01L 33/42; H01L 33/48;
H01L 33/483; H01L 33/486; F21K 9/00
See application file for complete search history.

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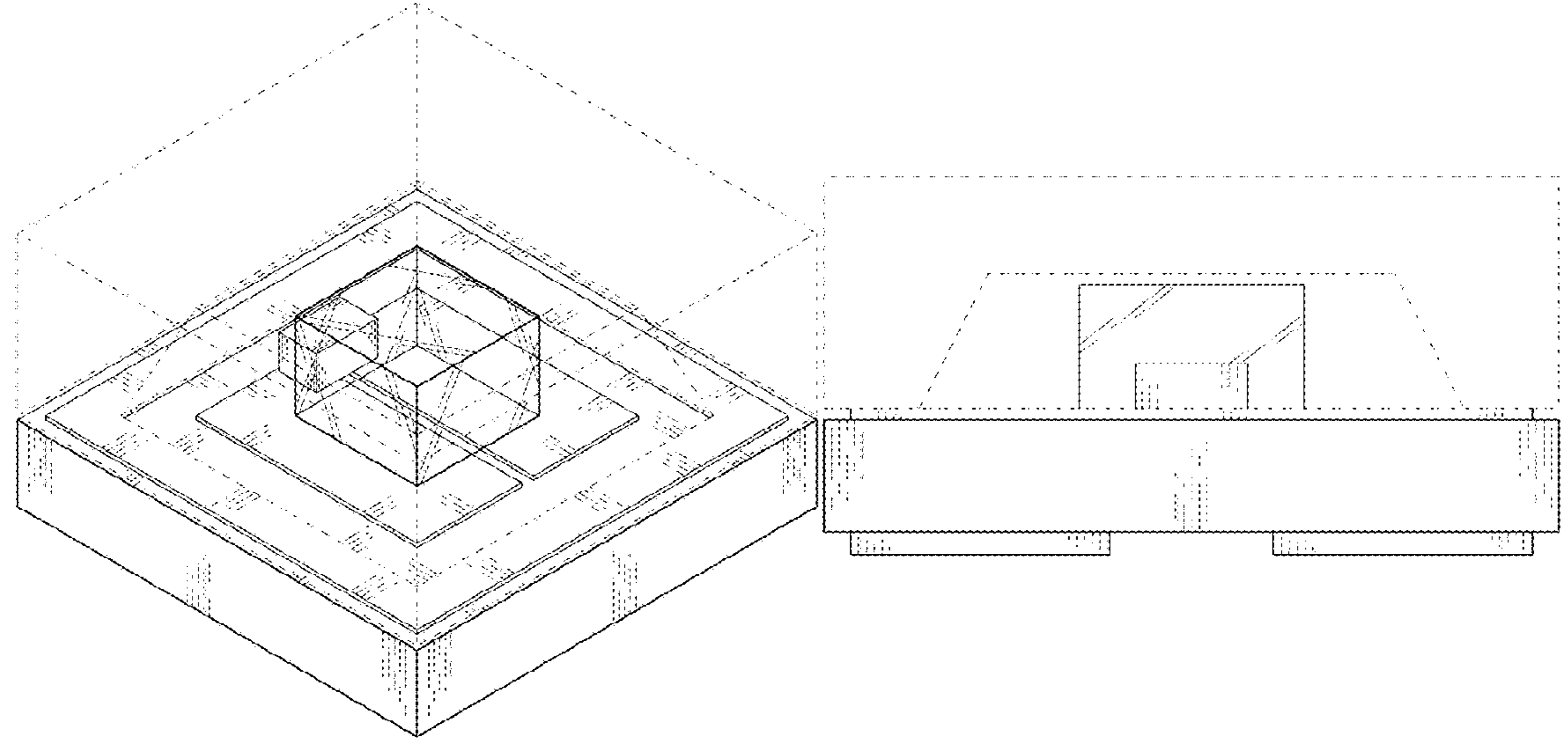
Primary Examiner — Selina Sikder
(74) *Attorney, Agent, or Firm* — Kenja IP Law PC

(57) **CLAIM**
The ornamental design for a light emitting diode chip, as shown and described.

DESCRIPTION

FIG. 1 is a top, front perspective view of a light emitting diode chip showing my new design;
FIG. 2 is a bottom, rear perspective view thereof;
FIG. 3 is a front view thereof;
FIG. 4 is a rear view thereof;
FIG. 5 is a right side view thereof;
FIG. 6 is a left side view thereof;
FIG. 7 is a top plan view thereof;
FIG. 8 is a bottom view thereof; and
FIG. 9 is a top plan view thereof, shown without the transparent cavity lens; and,
FIG. 10 is a cross sectional view taken along line 10-10 in FIG. 7.
Broken lines depictions of transparent cavity lens of the light emitting diode chip shown in the drawings indicate unclaimed portions of the design, and thus illustrative purpose only and form no part of the claimed design.

1 Claim, 10 Drawing Sheets



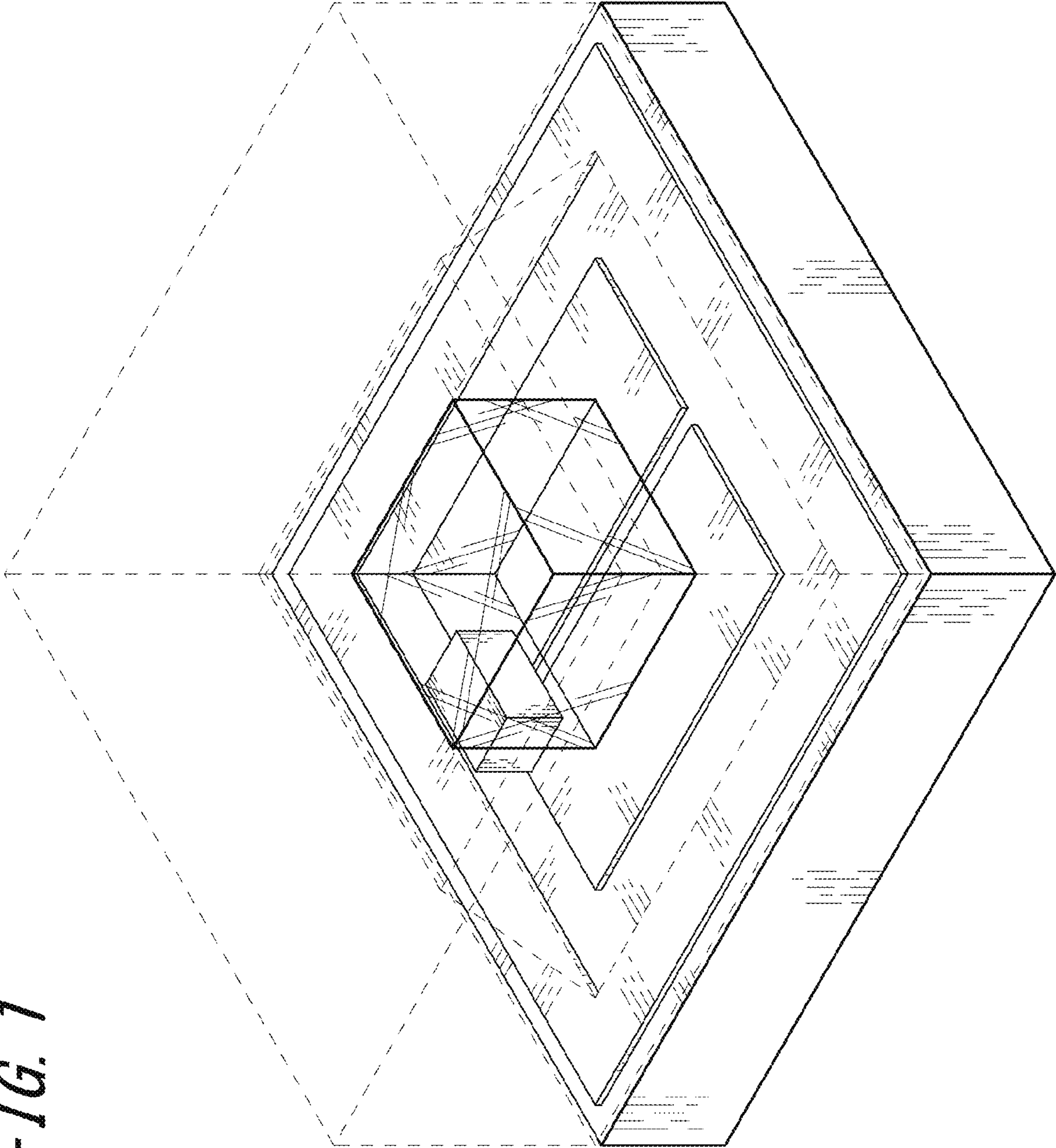


FIG. 1

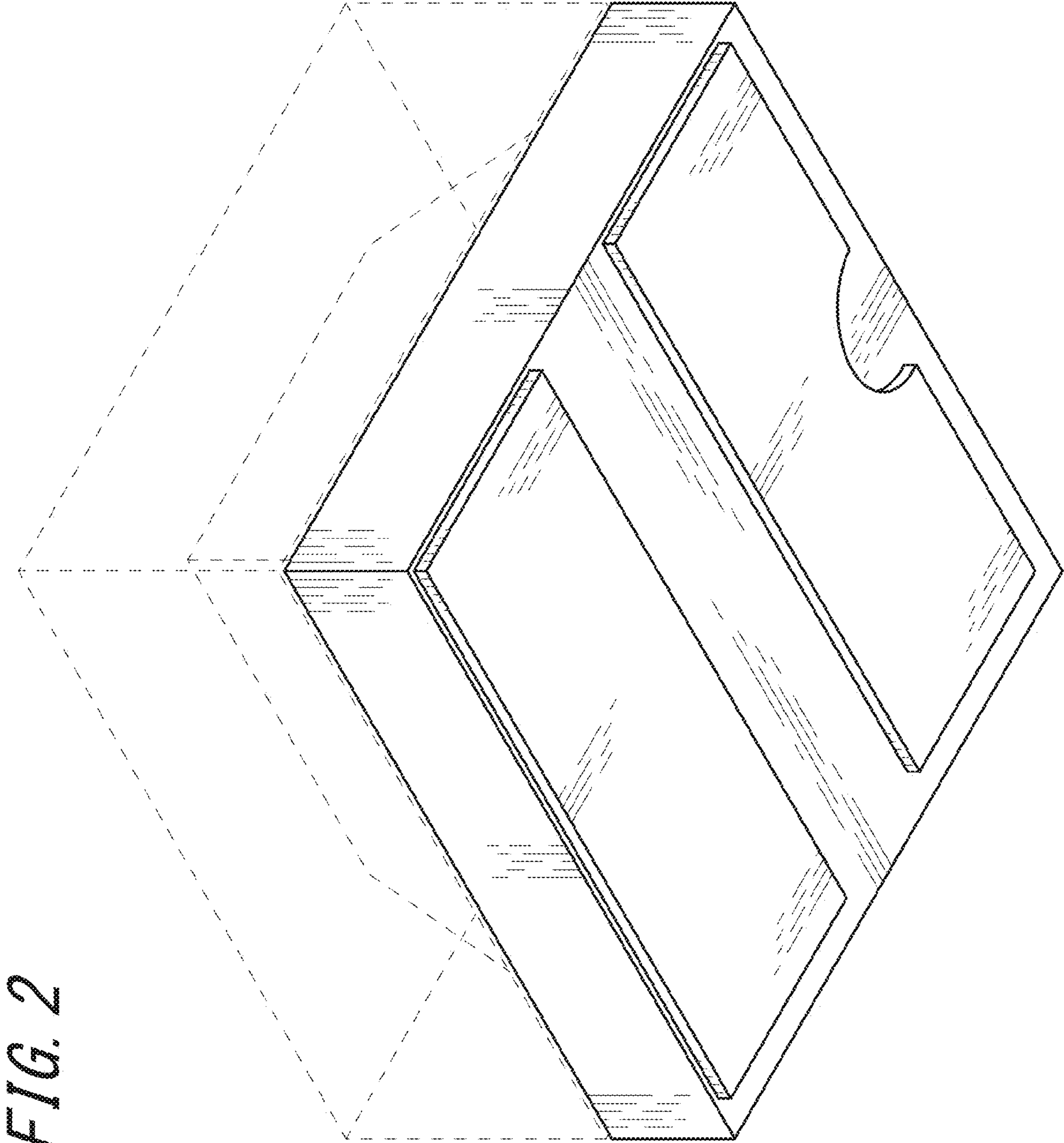


FIG. 2

FIG. 3

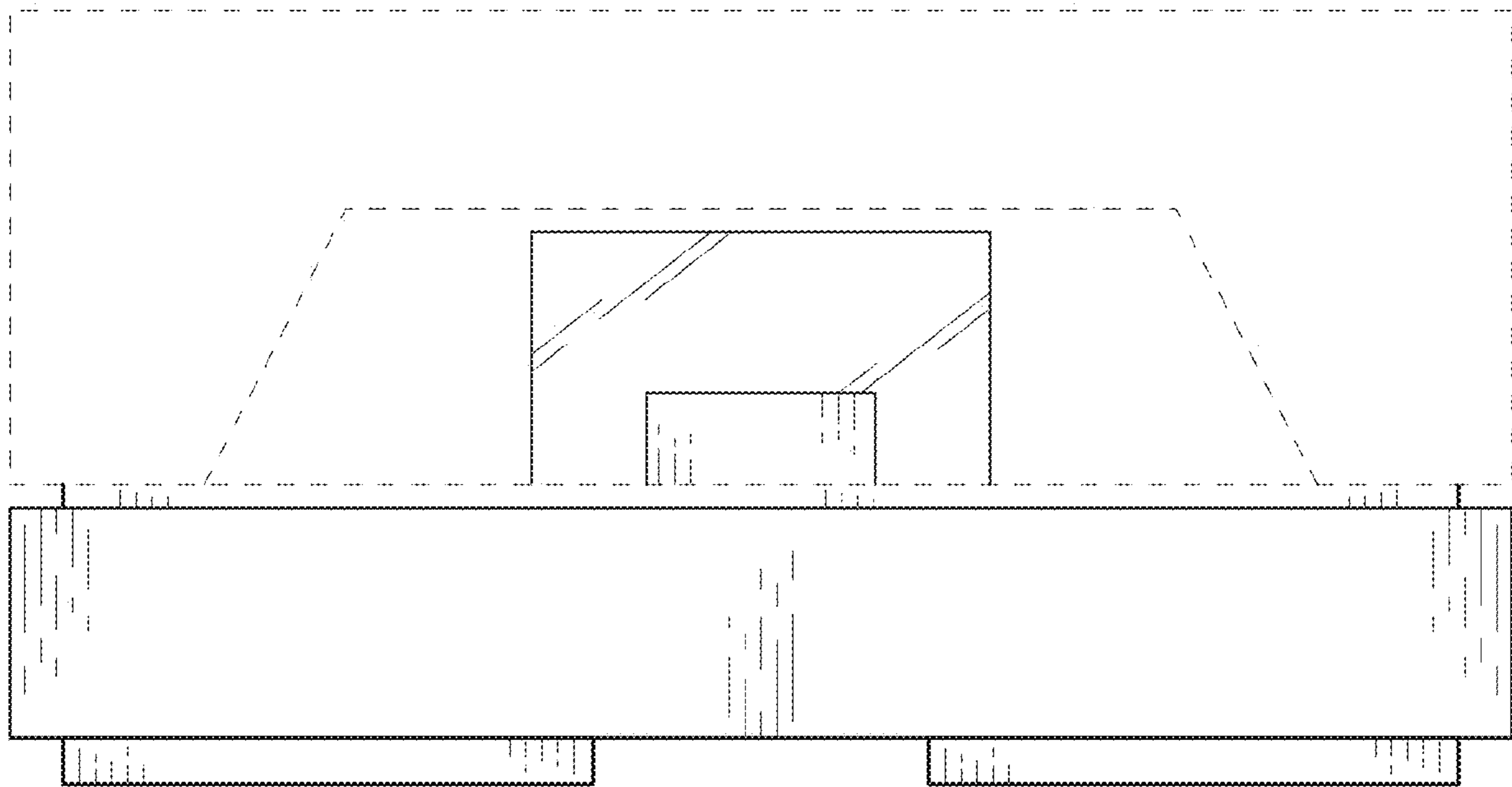


FIG. 4

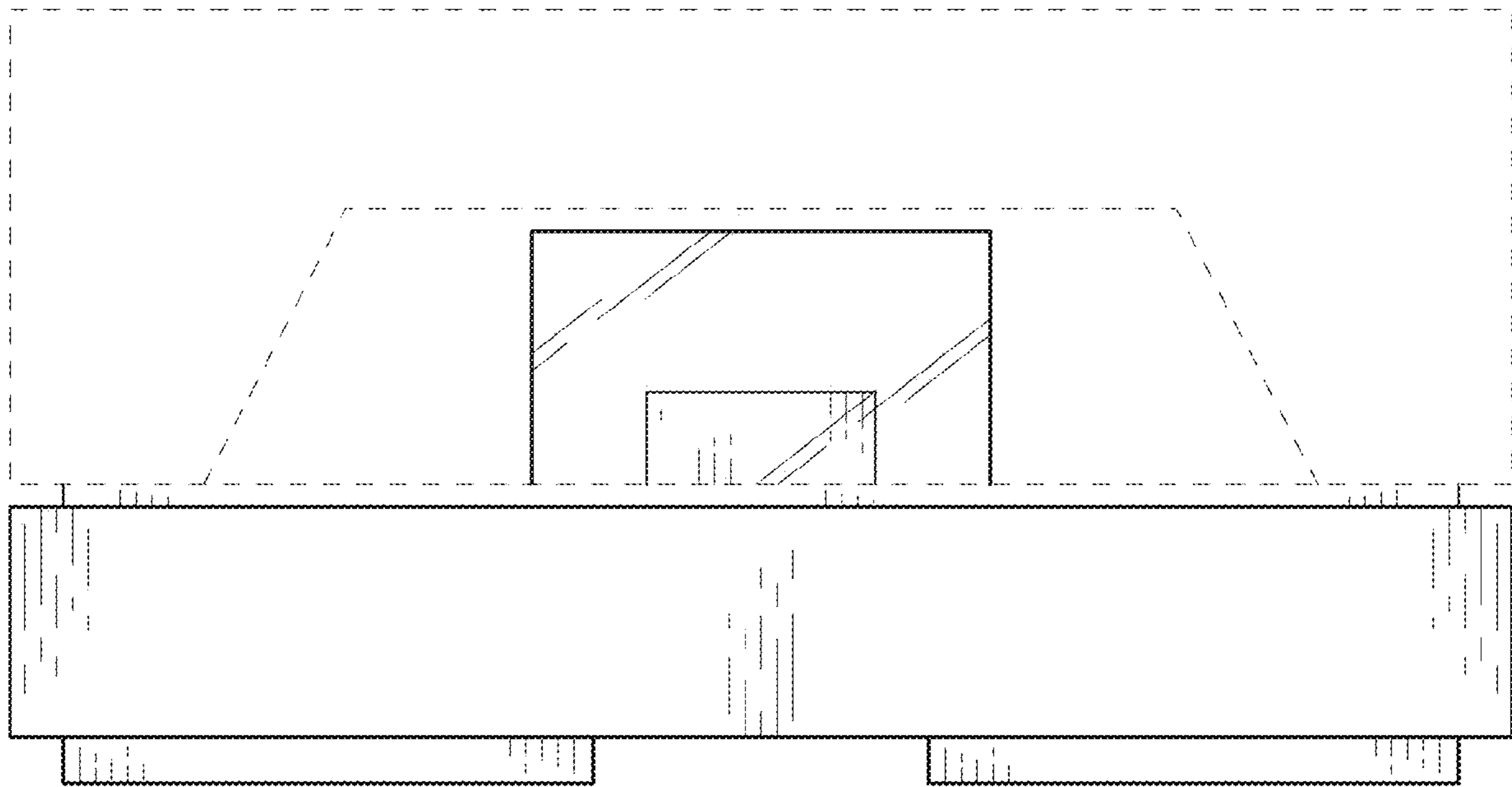


FIG. 5

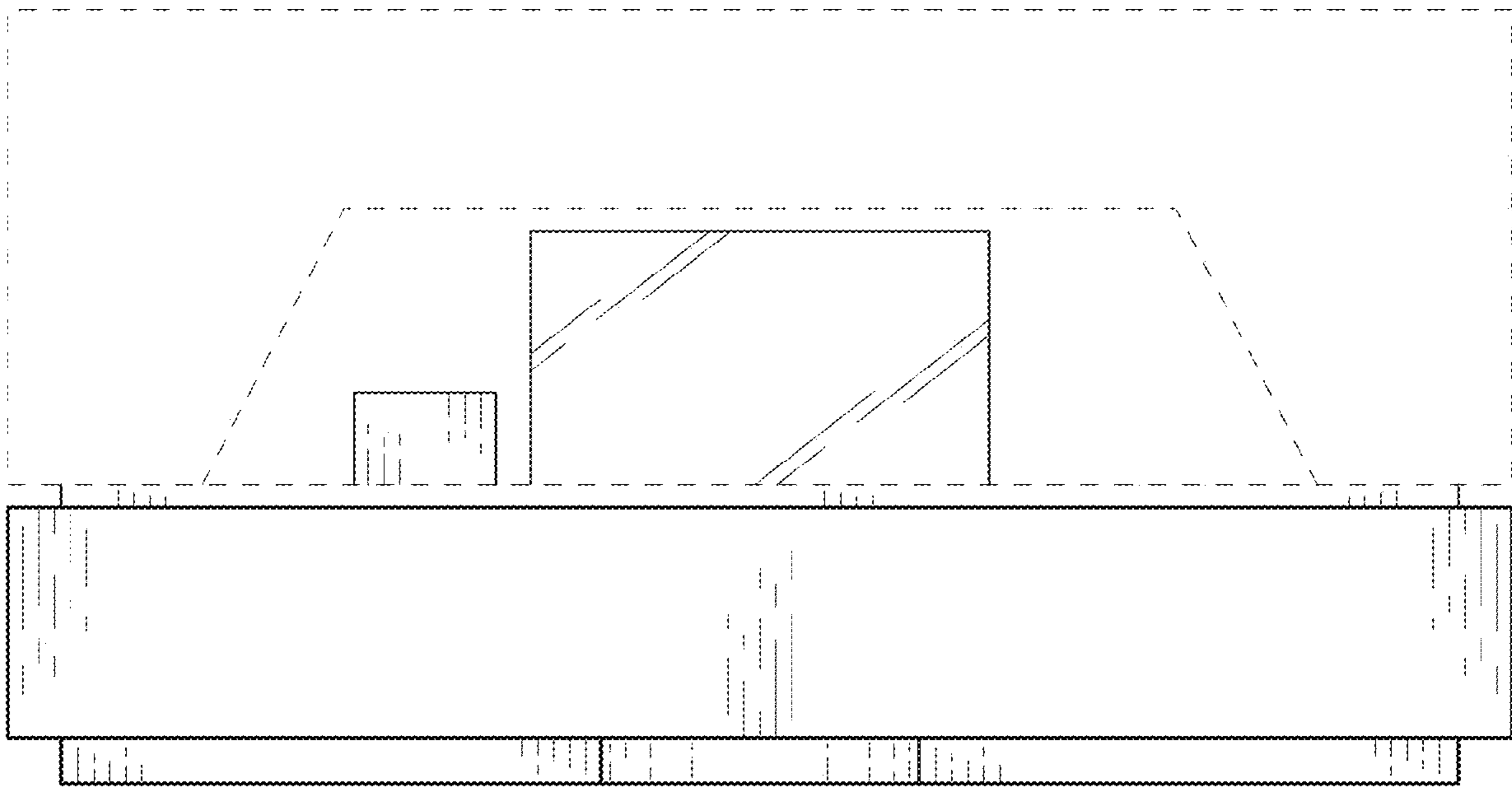


FIG. 6

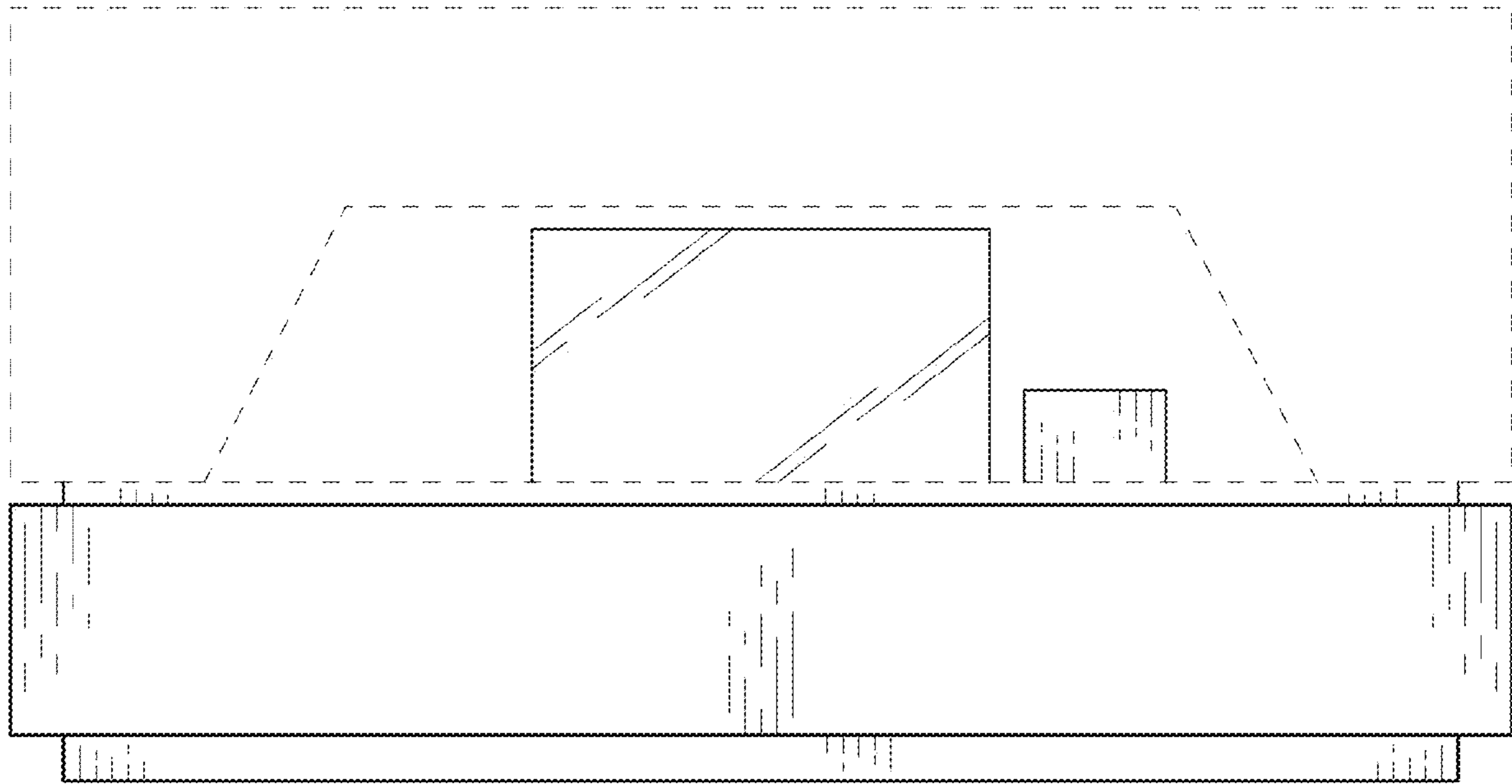


FIG. 7

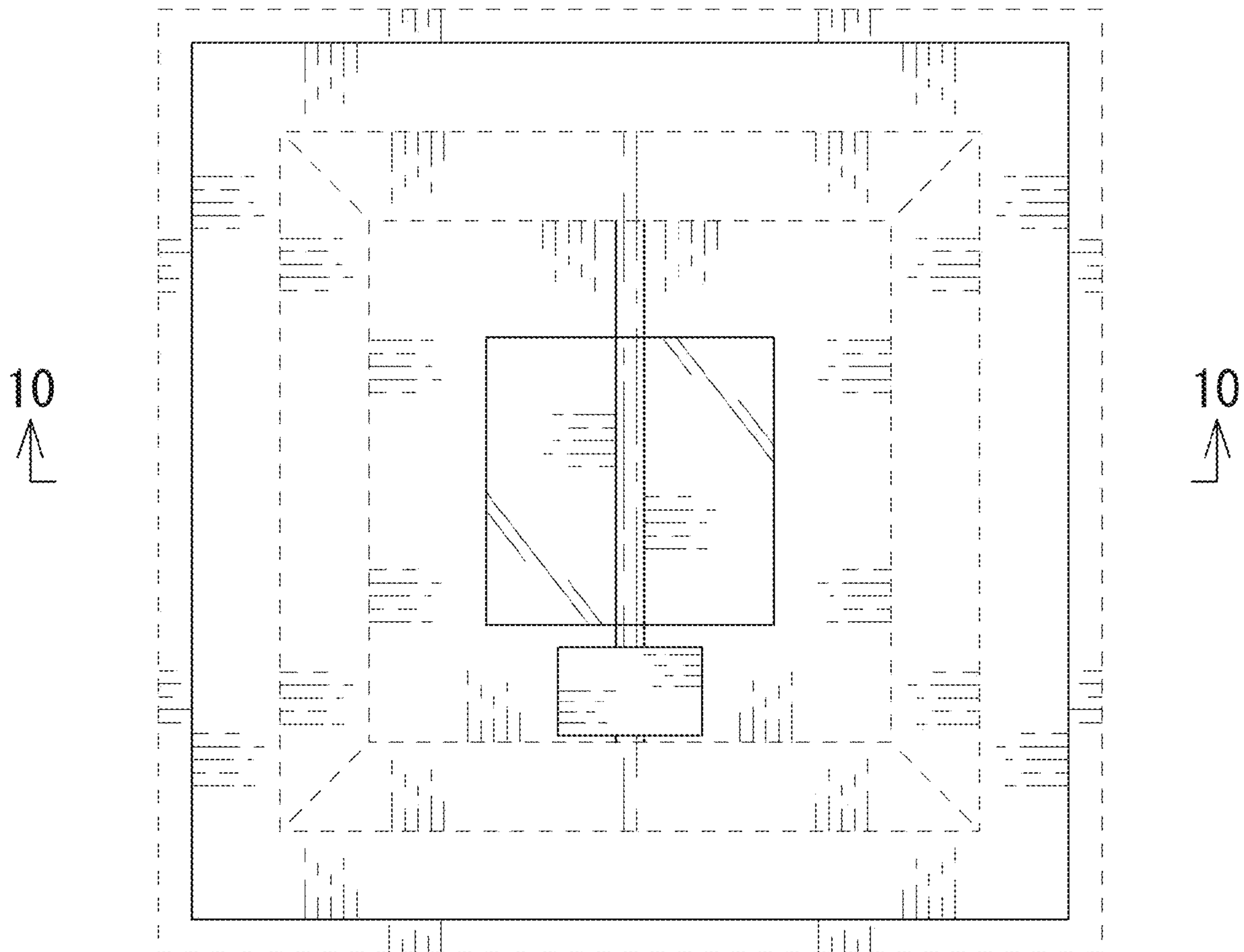


FIG. 8

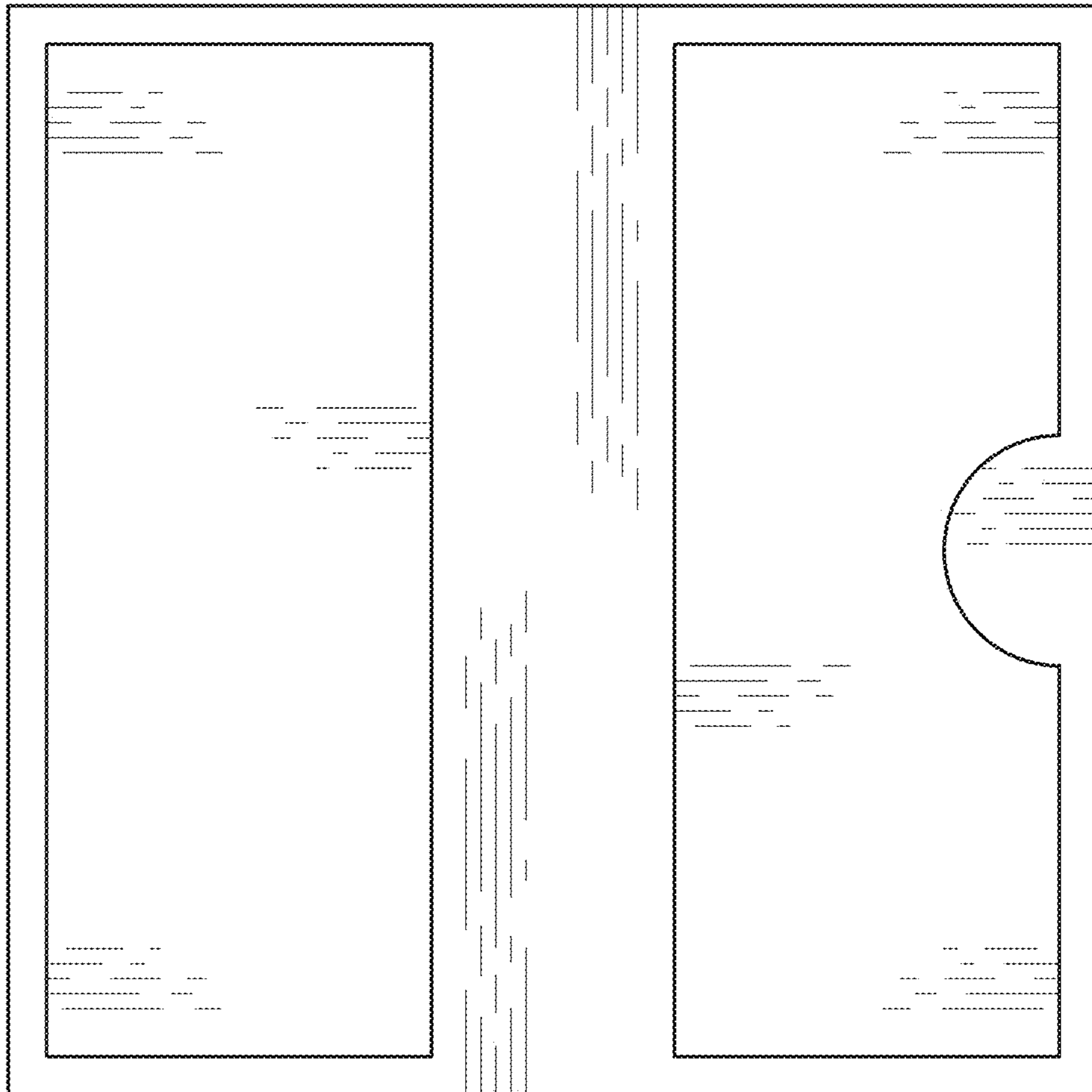


FIG. 9

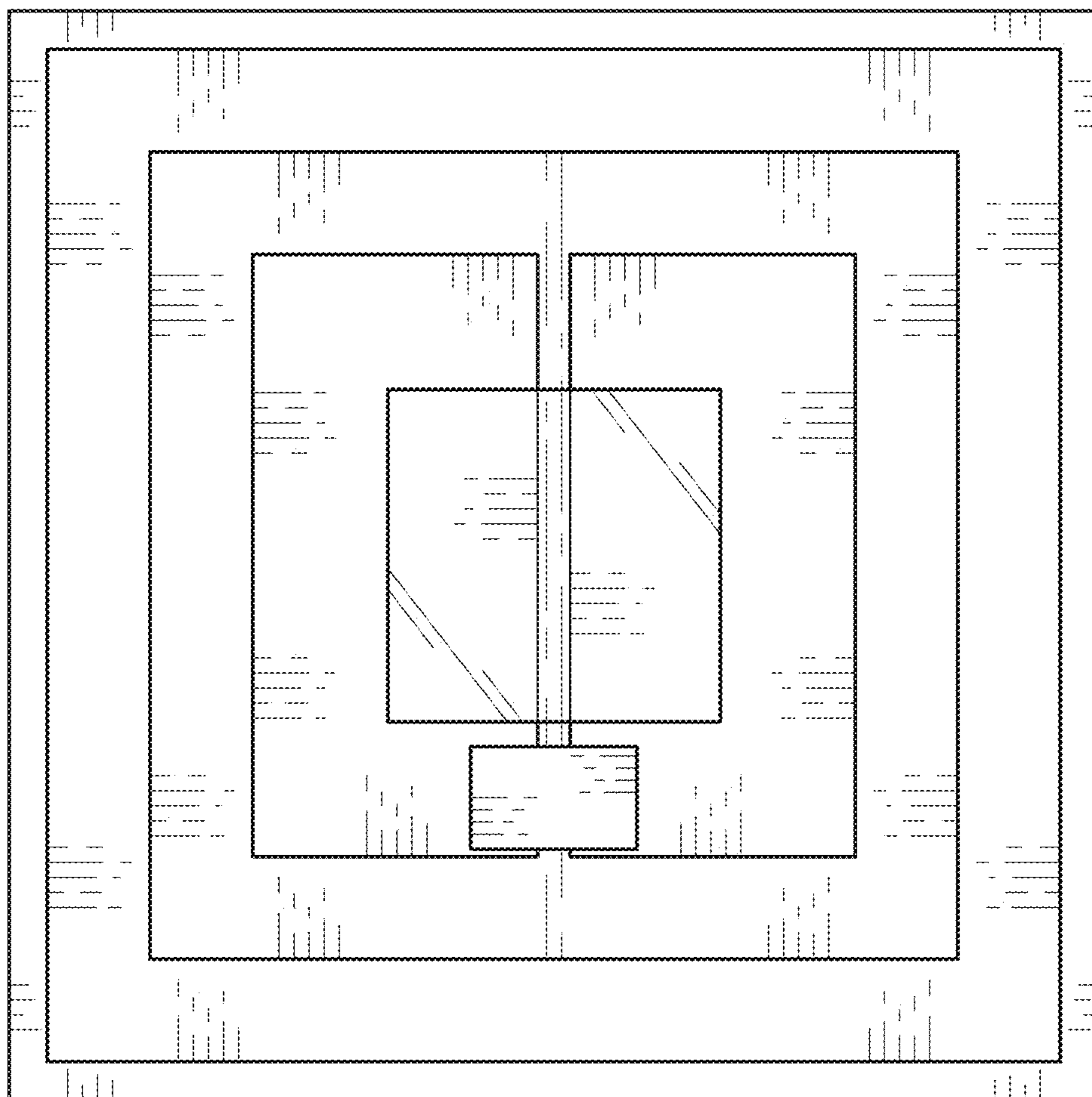


FIG. 10

