



US00D914690S

(12) **United States Design Patent** (10) **Patent No.:** **US D914,690 S**
Wengreen (45) **Date of Patent:** **** Mar. 30, 2021**

(54) **MOUNT FOR ELECTRONIC DEVICES**

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(**) Term: **15 Years**

(21) Appl. No.: **29/728,623**

(22) Filed: **Mar. 19, 2020**

Related U.S. Application Data

(63) Continuation-in-part of application No. 29/690,707, filed on May 9, 2019, now Pat. No. Des. 905,068, and a continuation of application No. 29/690,706, filed on May 9, 2019, now Pat. No. Des. 905,067.

(51) **LOC (13) Cl.** **14-02**

(52) **U.S. Cl.**
USPC **D14/447**

(58) **Field of Classification Search**

USPC D14/447, 434, 432, 439, 440, 451, 452,
D14/457, 239; D8/363, 373, 380;
D6/406.3, 406.4, 406.5, 406.6; D13/184,
D13/199

CPC A47B 21/04; A47B 2097/006; A47B
2097/005; A47B 2023/049; A45C
2011/002; A45C 2011/003; F16M
2200/00; F16M 13/00

See application file for complete search history.

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(57) **CLAIM**

The ornamental design for a mount for electronic devices, as shown and described.

DESCRIPTION

FIG. 1 is a front, first side, and top perspective view of a mount for electronic devices embodying the new design; FIG. 2 is a back, second side, and bottom perspective view thereof;

FIG. 3 is a top, first side, and front perspective view thereof; FIG. 4 is a front view thereof;

FIG. 5 is a back view thereof;

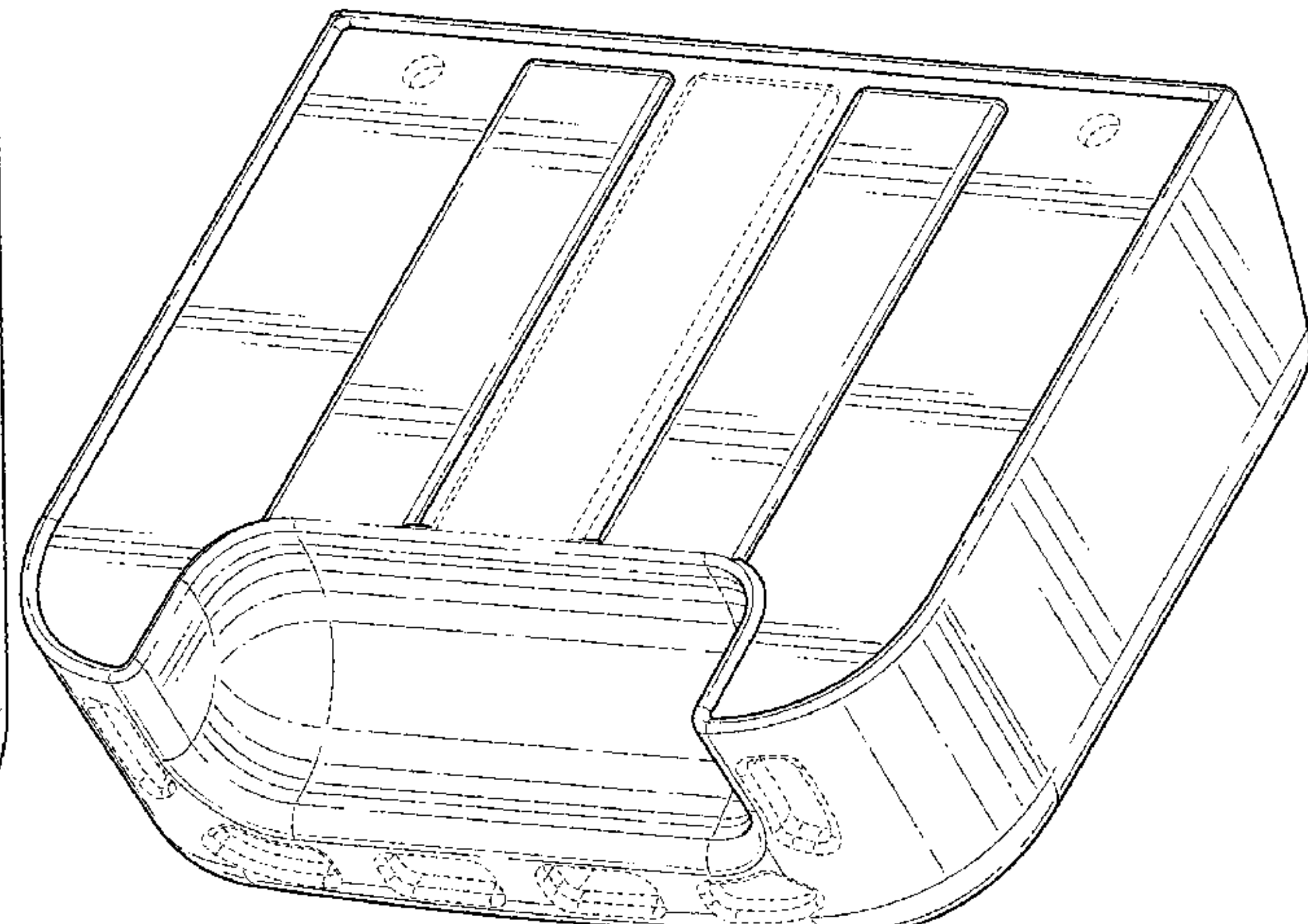
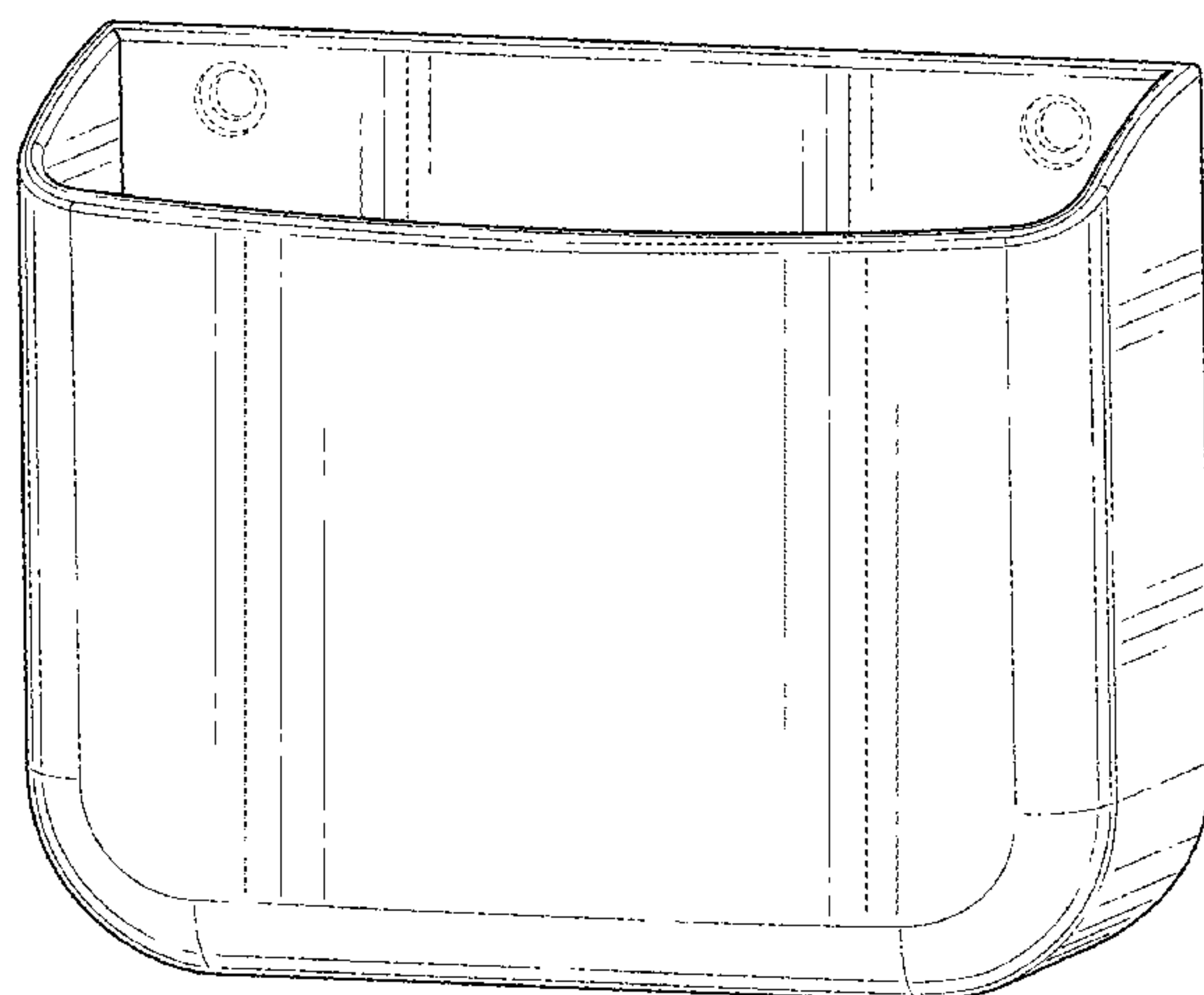
FIG. 6 is a second side view thereof, the first side view being a mirror image;

FIG. 7 is a top view thereof; and,

FIG. 8 is a bottom view thereof.

The broken lines in the drawings illustrate portions of a mount for electronic devices which form no part of the claimed design.

1 Claim, 8 Drawing Sheets



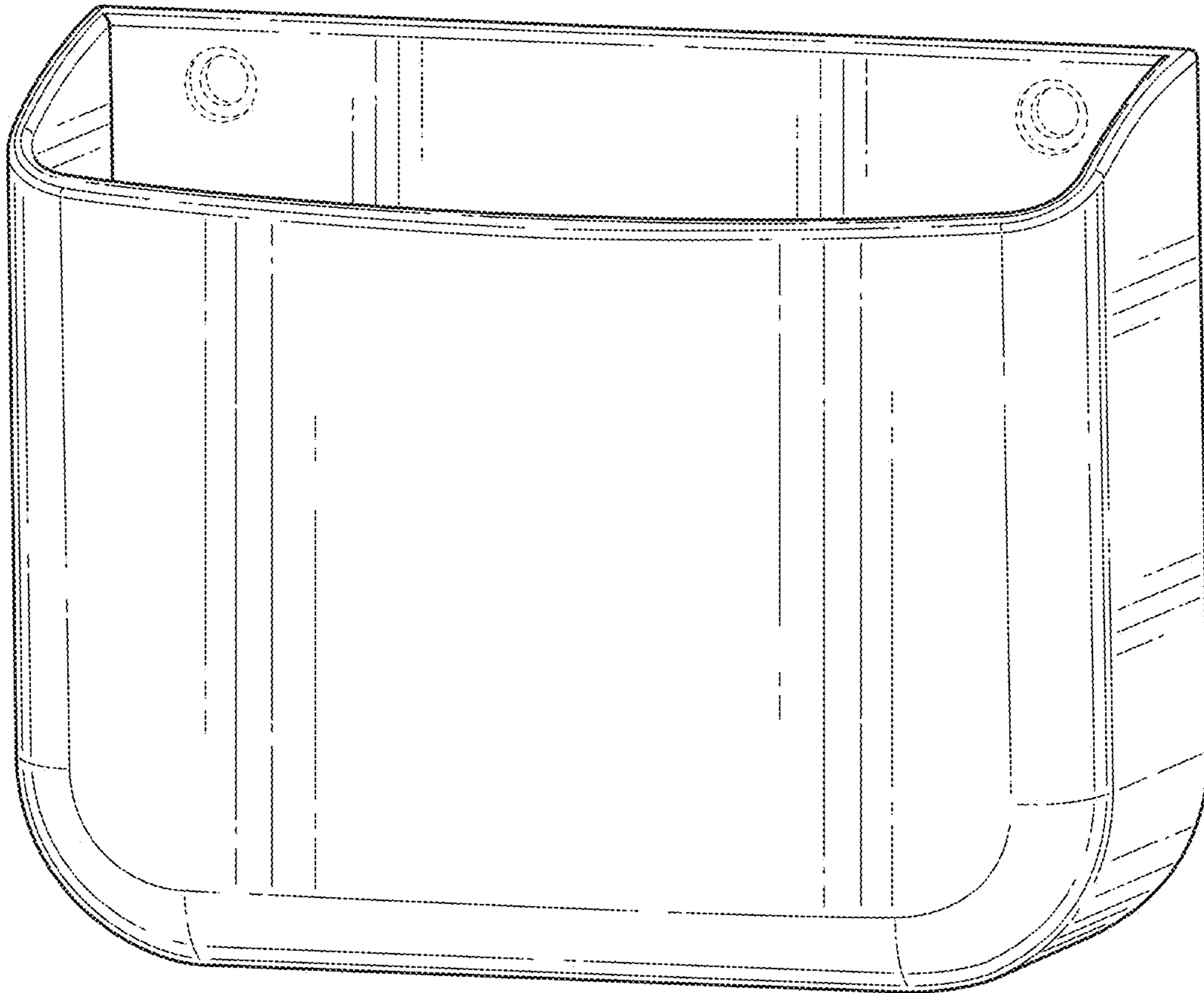


FIG. 1

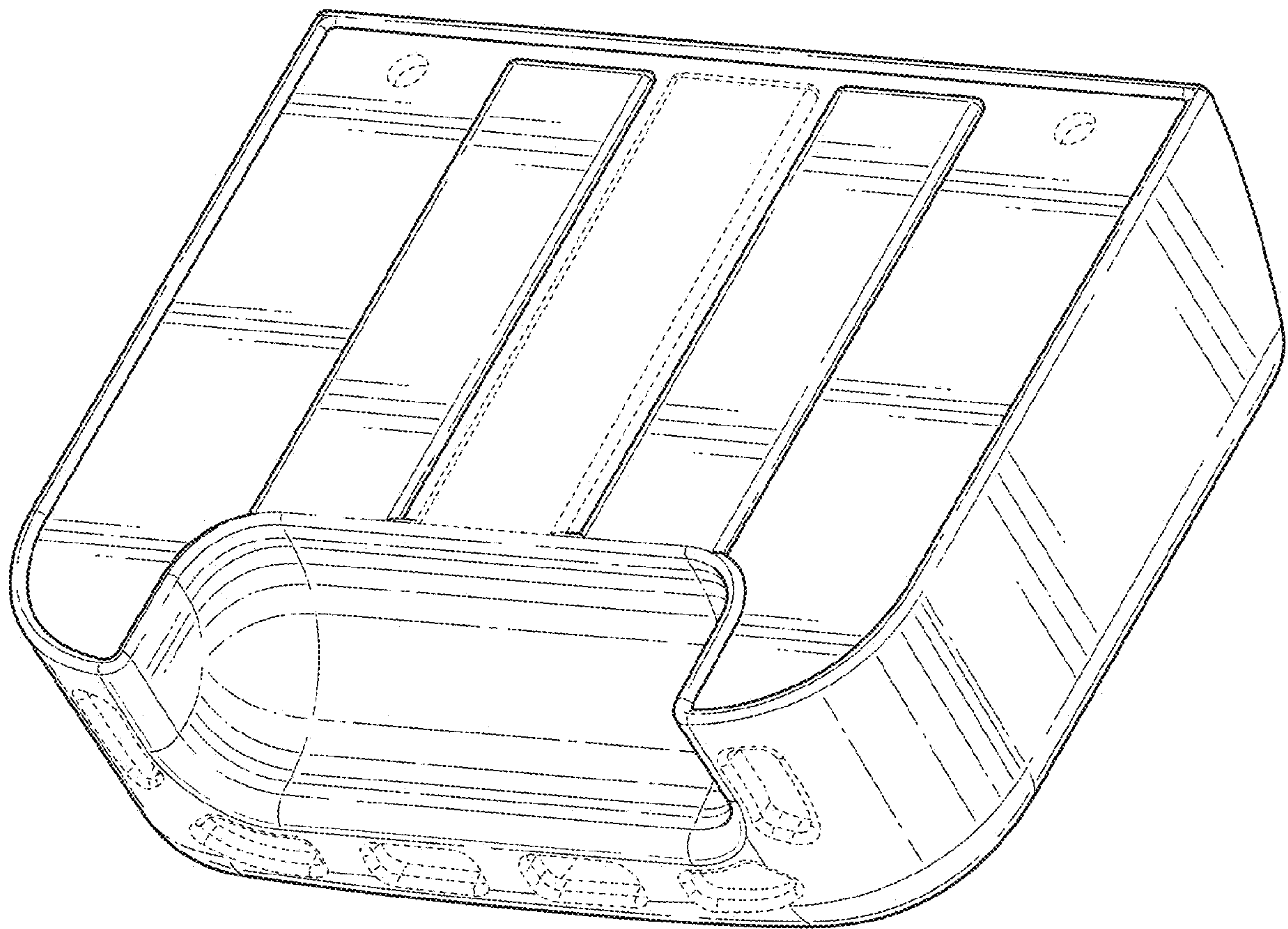


FIG. 2

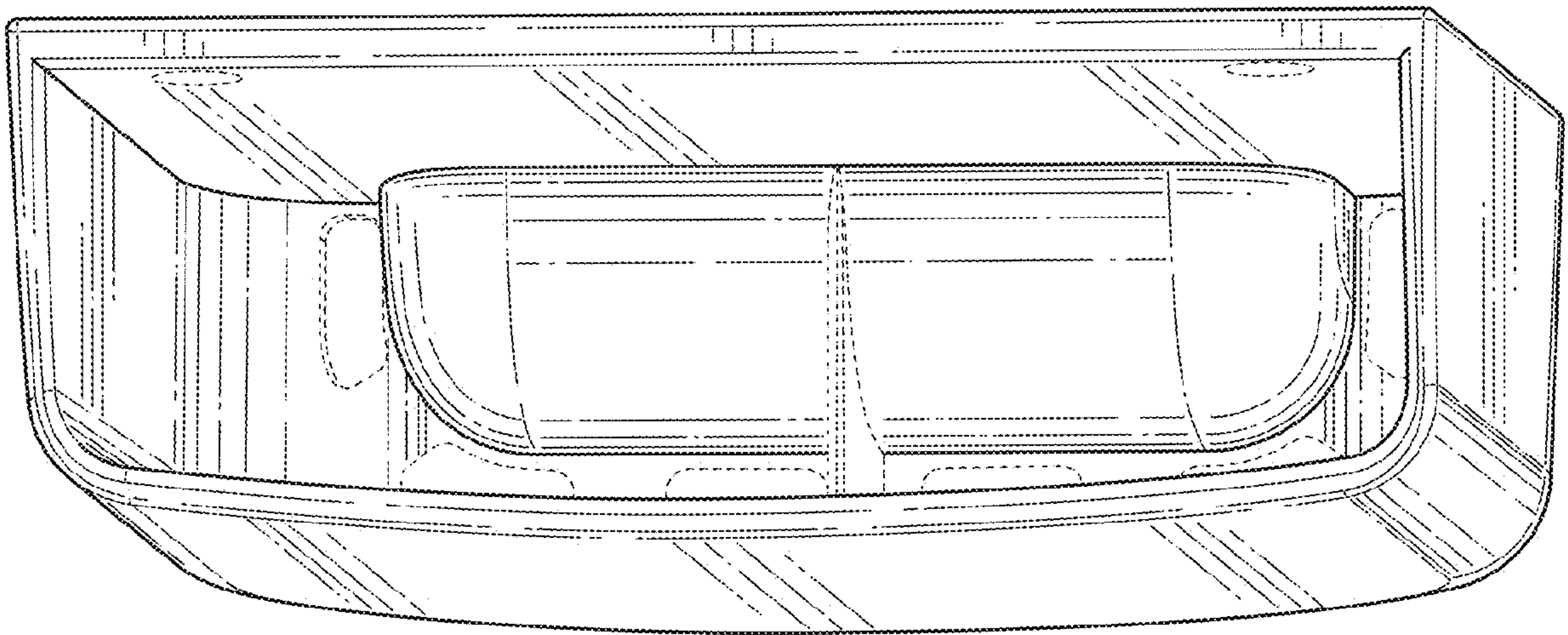


FIG. 3

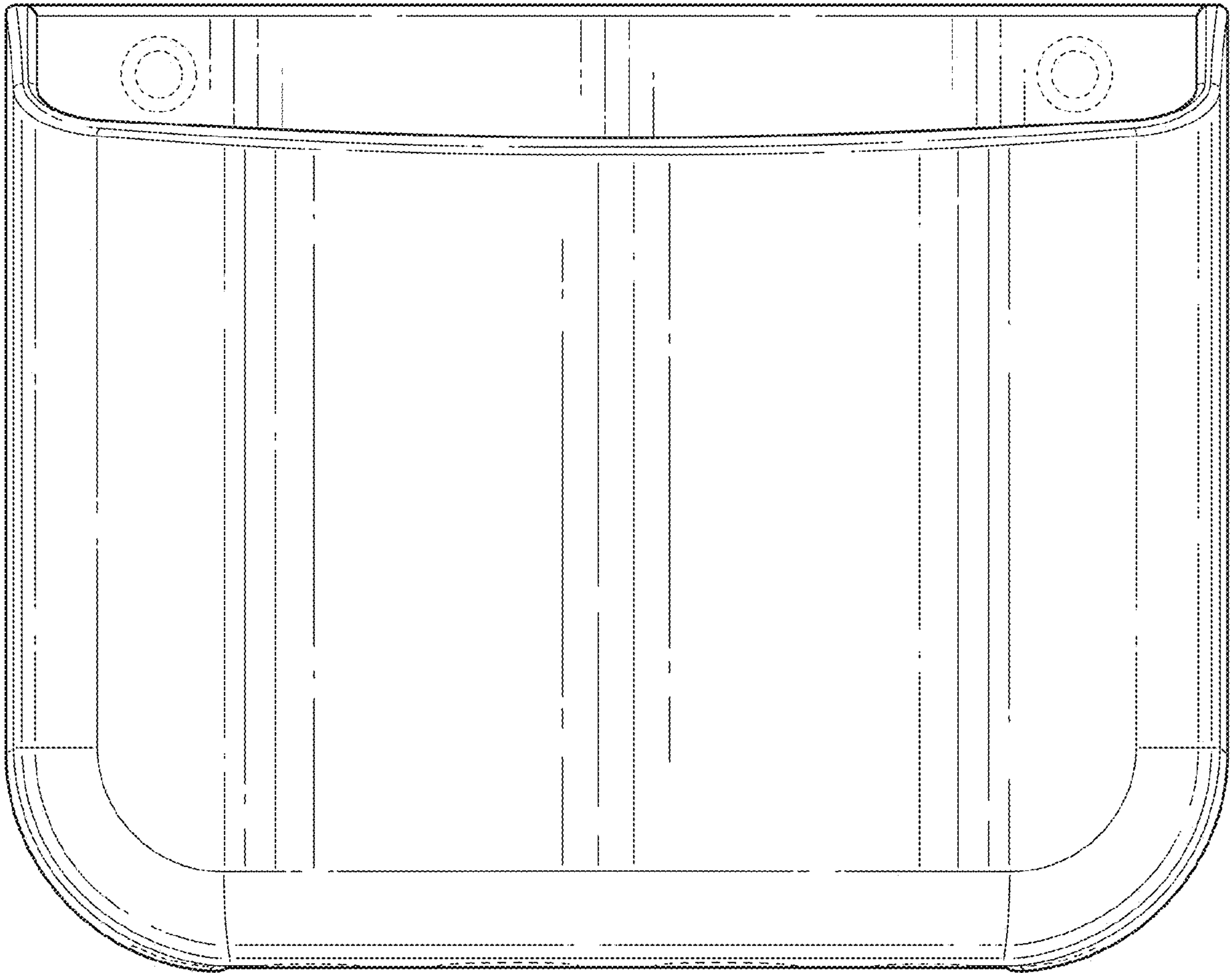


FIG. 4

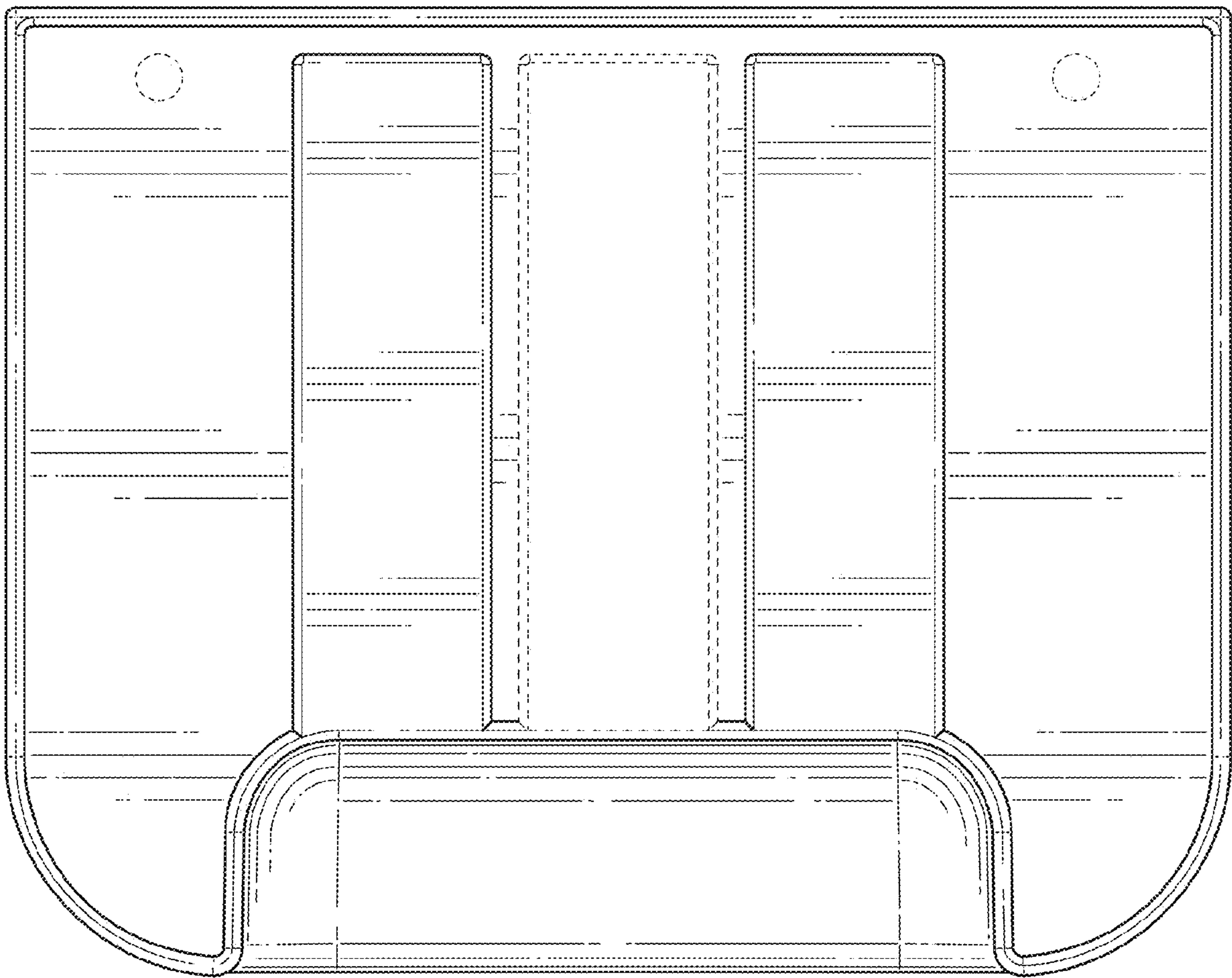


FIG. 5

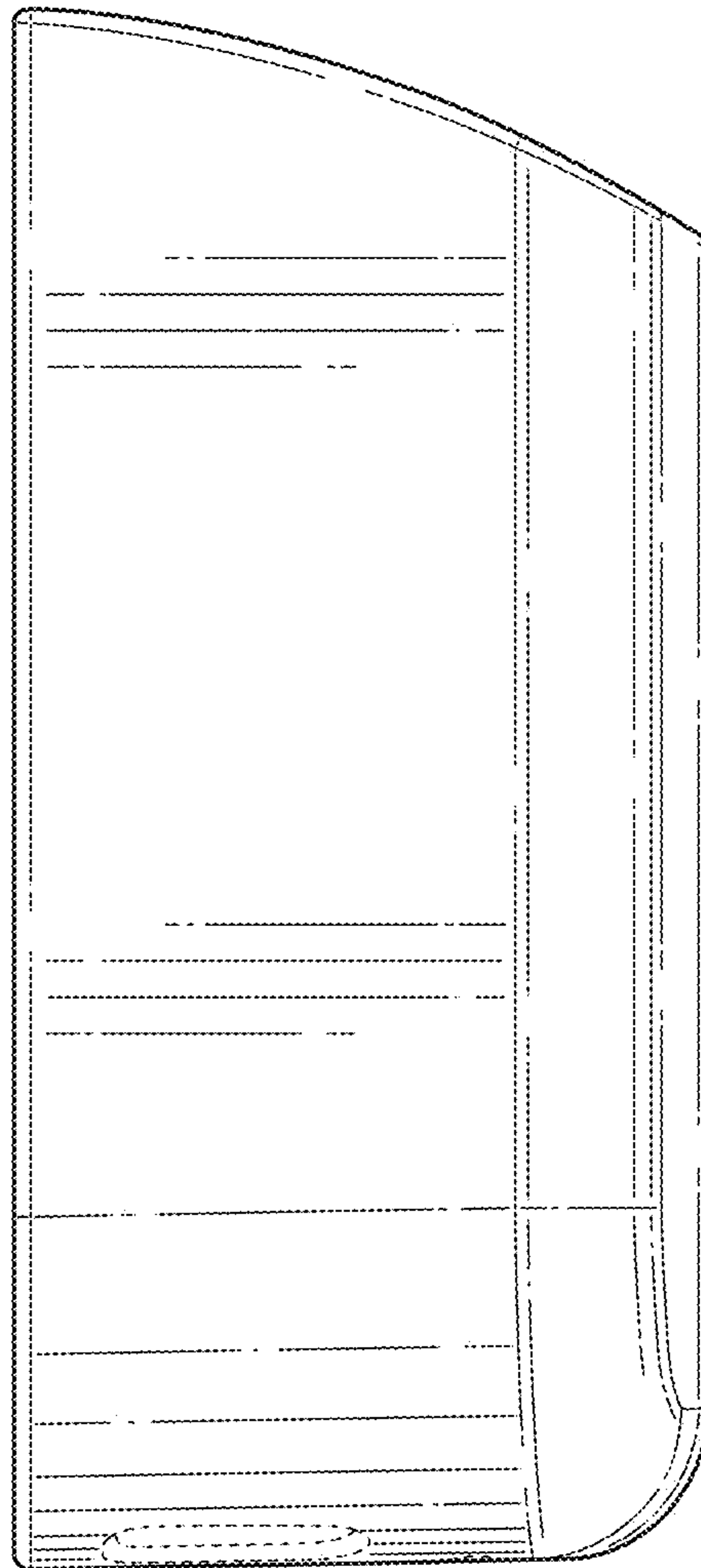


FIG. 6

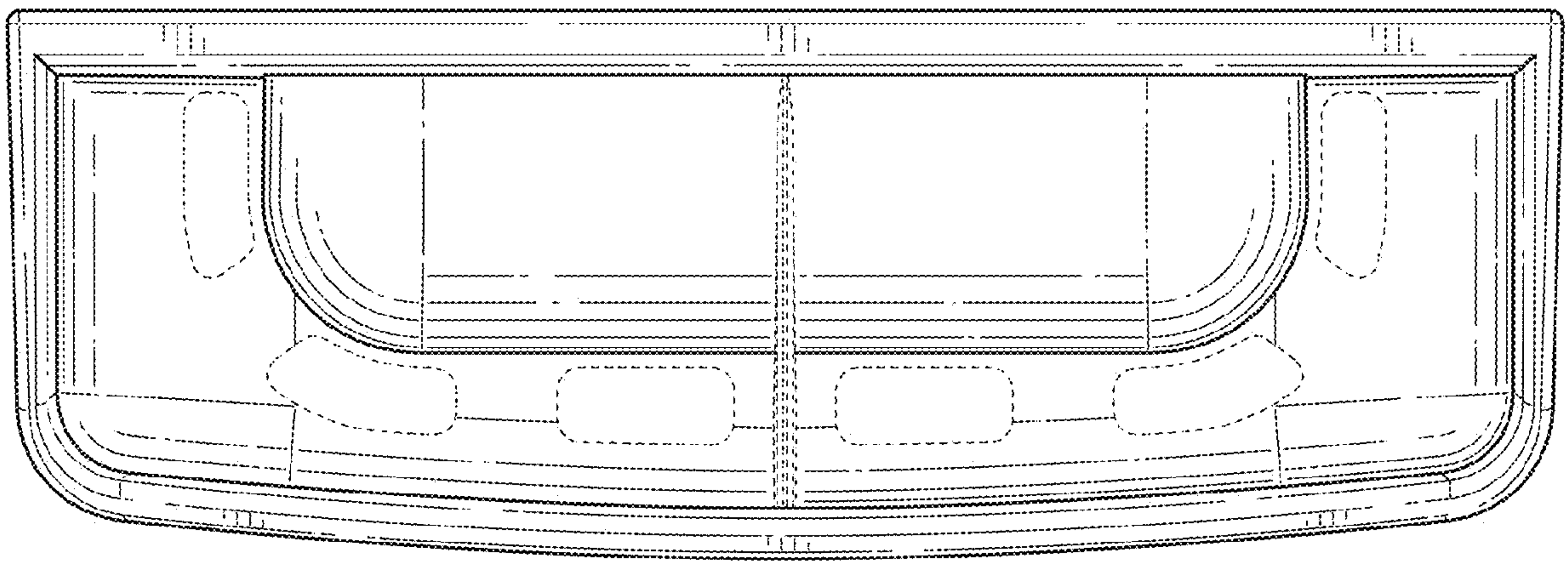


FIG. 7

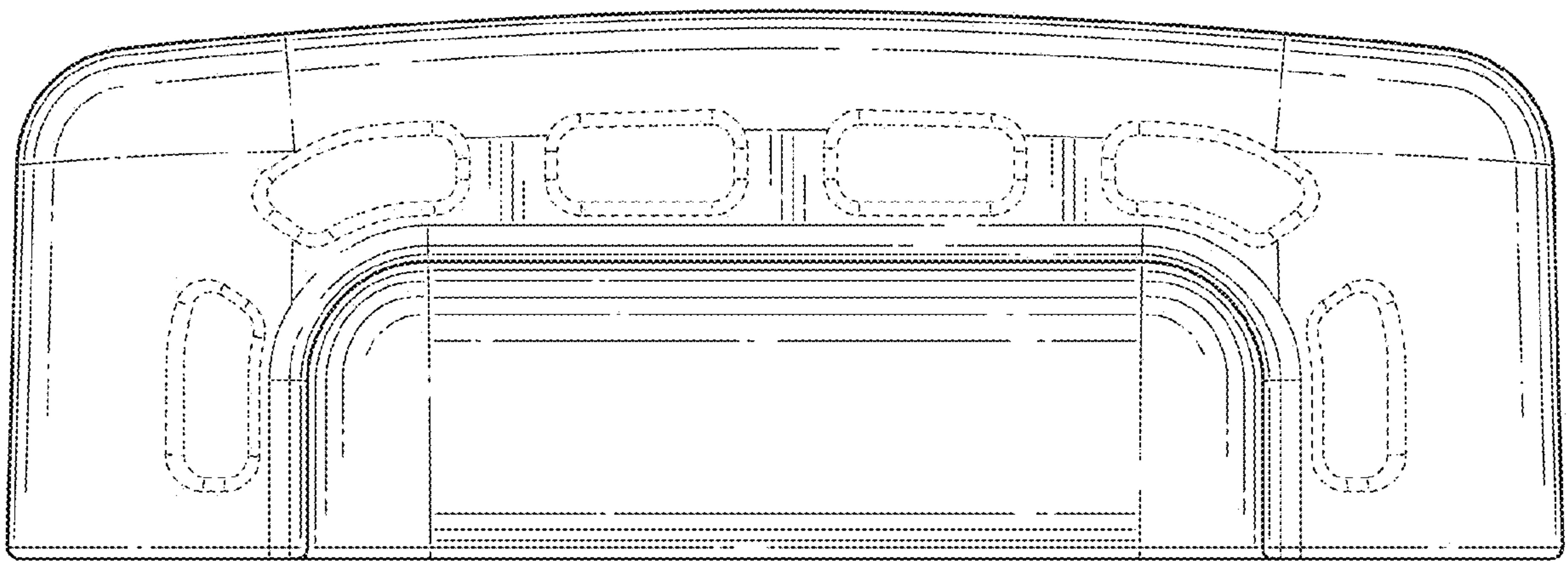


FIG. 8