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(12) **United States Design Patent**
Furutani

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(54) **SEMICONDUCTOR MODULE**

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(**) Term: **15 Years**

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(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/103, 110, 118, 121, 147, 154, 173,
D13/182, 184, 199; D14/356, 432
CPC H01L 23/00; H01L 23/14; H01L 21/4814
See application file for complete search history.

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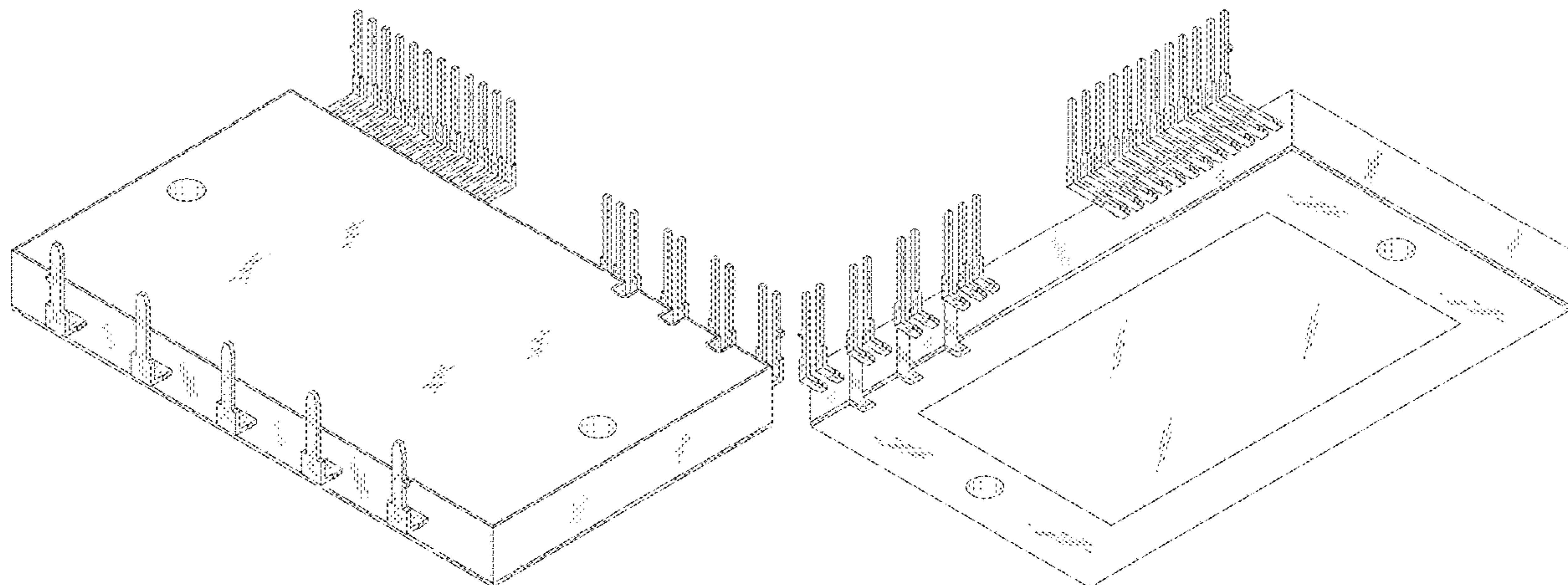
(57) **CLAIM**

The ornamental design for a semiconductor module, as shown and described.

DESCRIPTION

FIG. 1 is a front, top, right side perspective view of a semiconductor module showing my new design; FIG. 2 is a rear, bottom, left side perspective view thereof; FIG. 3 is a front elevation view thereof; FIG. 4 is a rear elevation view thereof; FIG. 5 is a top plan view thereof; FIG. 6 is a bottom plan view thereof; FIG. 7 is a right side elevation view thereof; FIG. 8 is a left side elevation view thereof; and, FIG. 9 is a cross sectional front elevation view thereof, taken in the direction of line 9-9 in FIG. 5. In FIGS. 2 and 6, the oblique line shading shown on the bottom side indicates a translucent surface.

1 Claim, 7 Drawing Sheets



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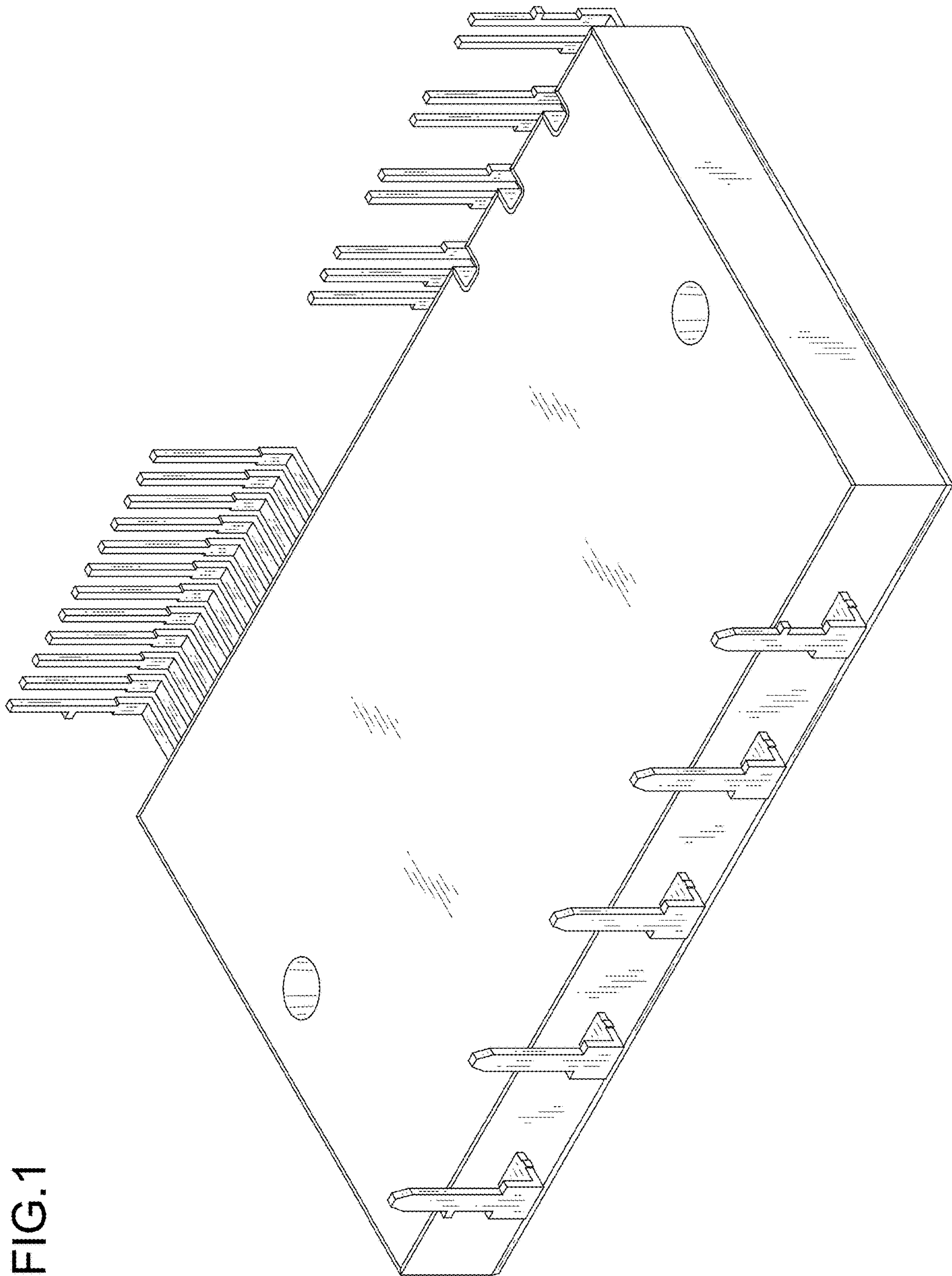


FIG. 1

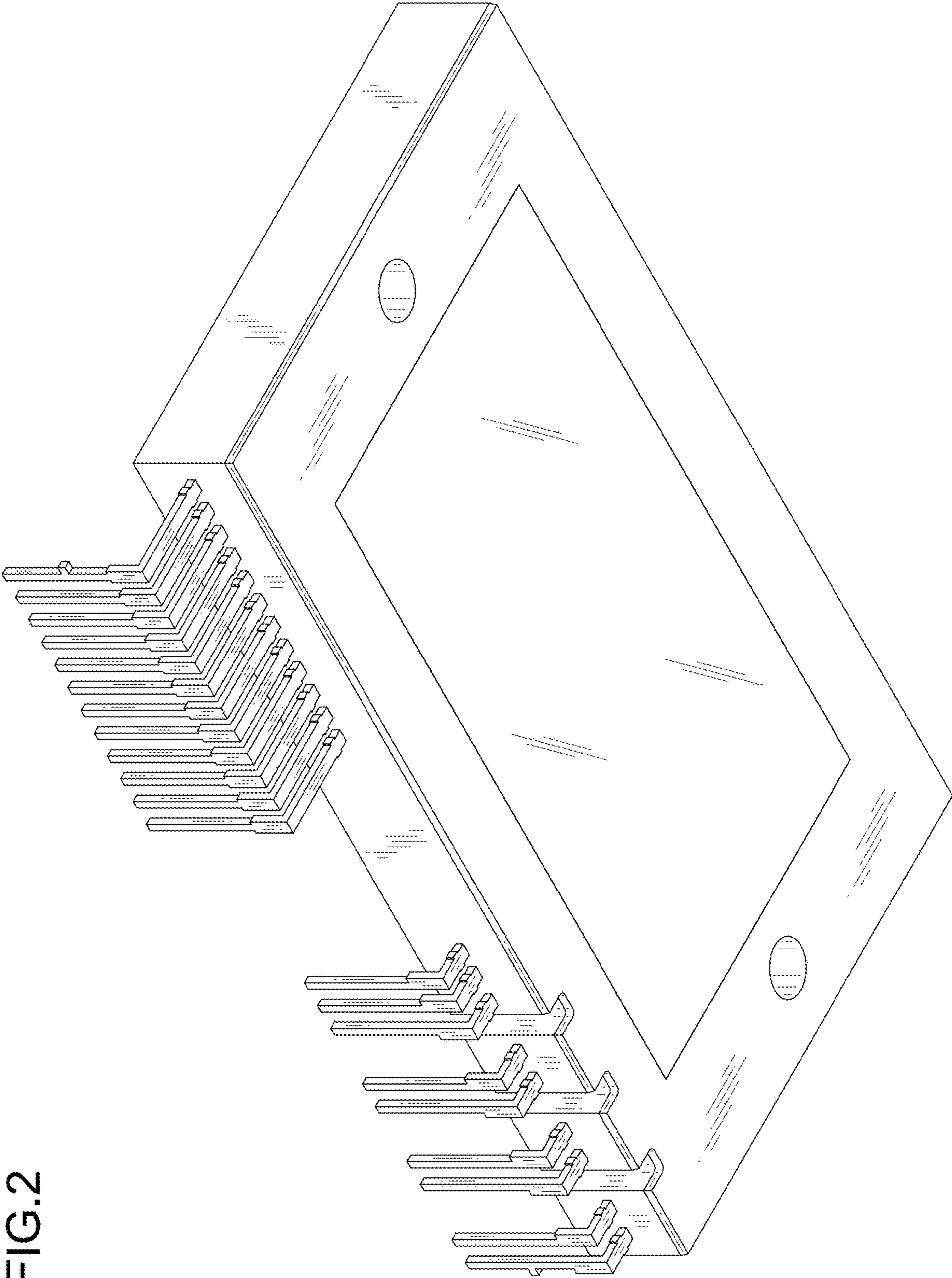


FIG.2

FIG.3

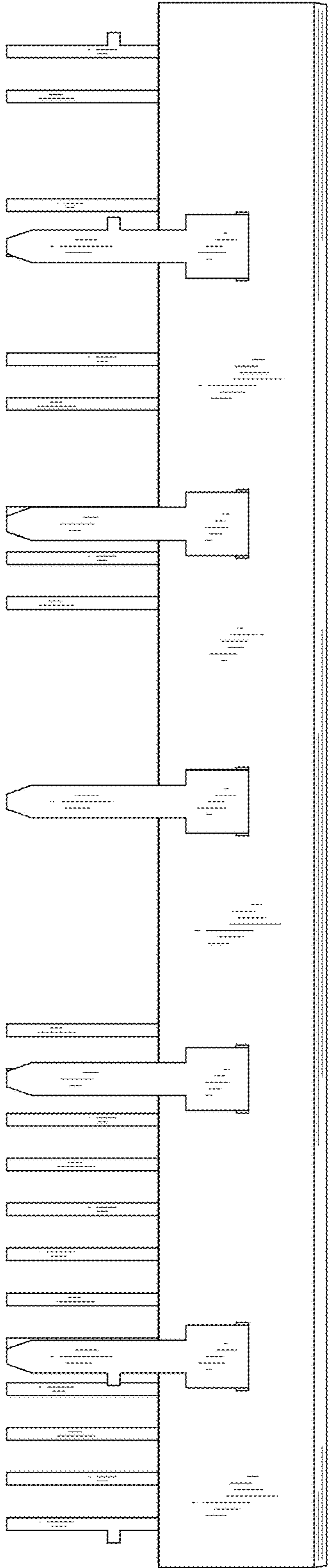


FIG.4

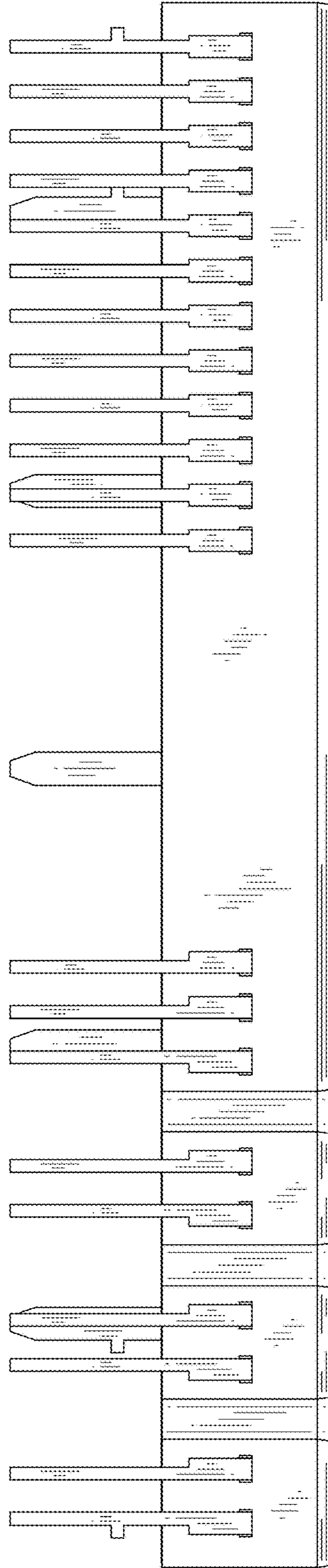


FIG. 5

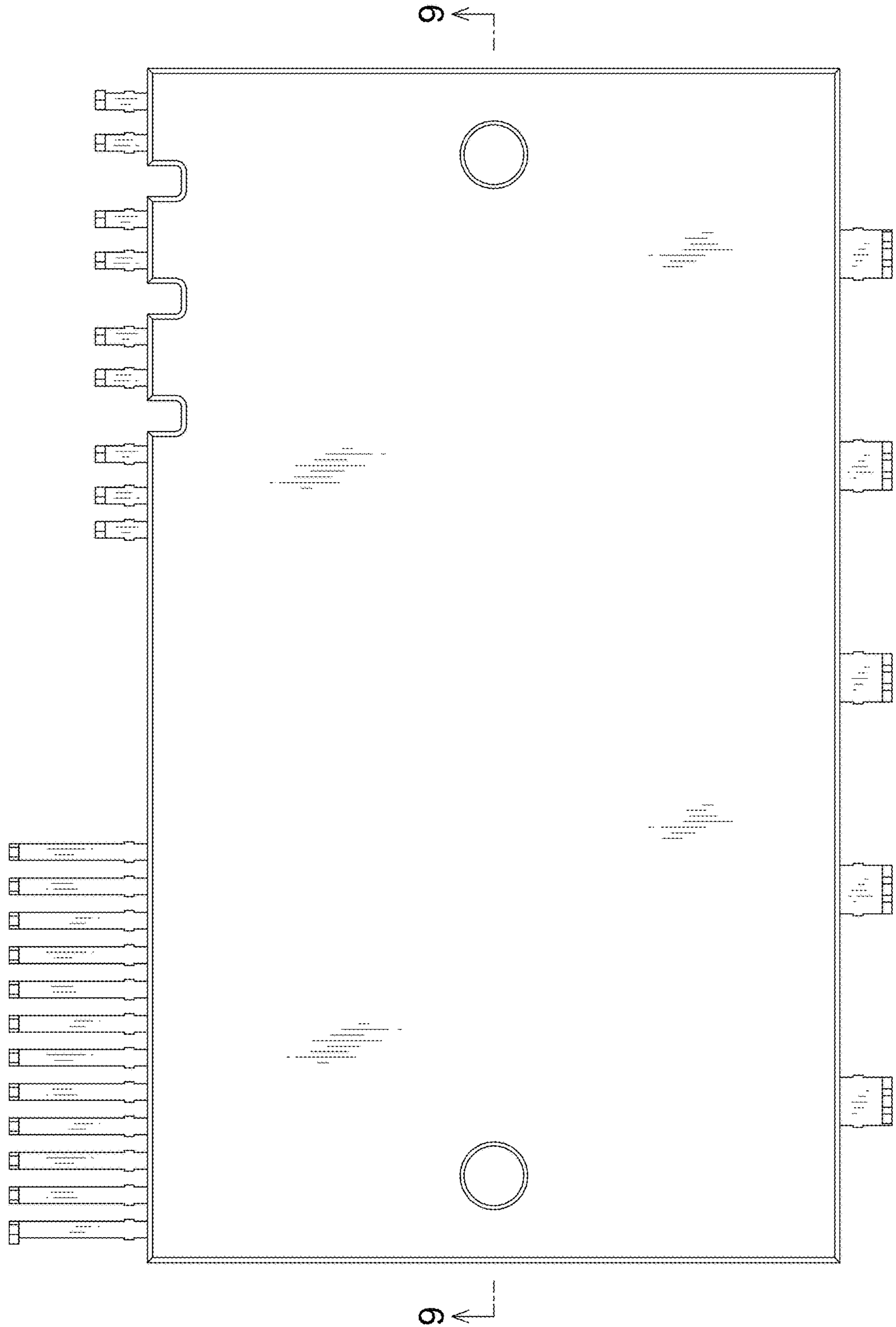


FIG.6

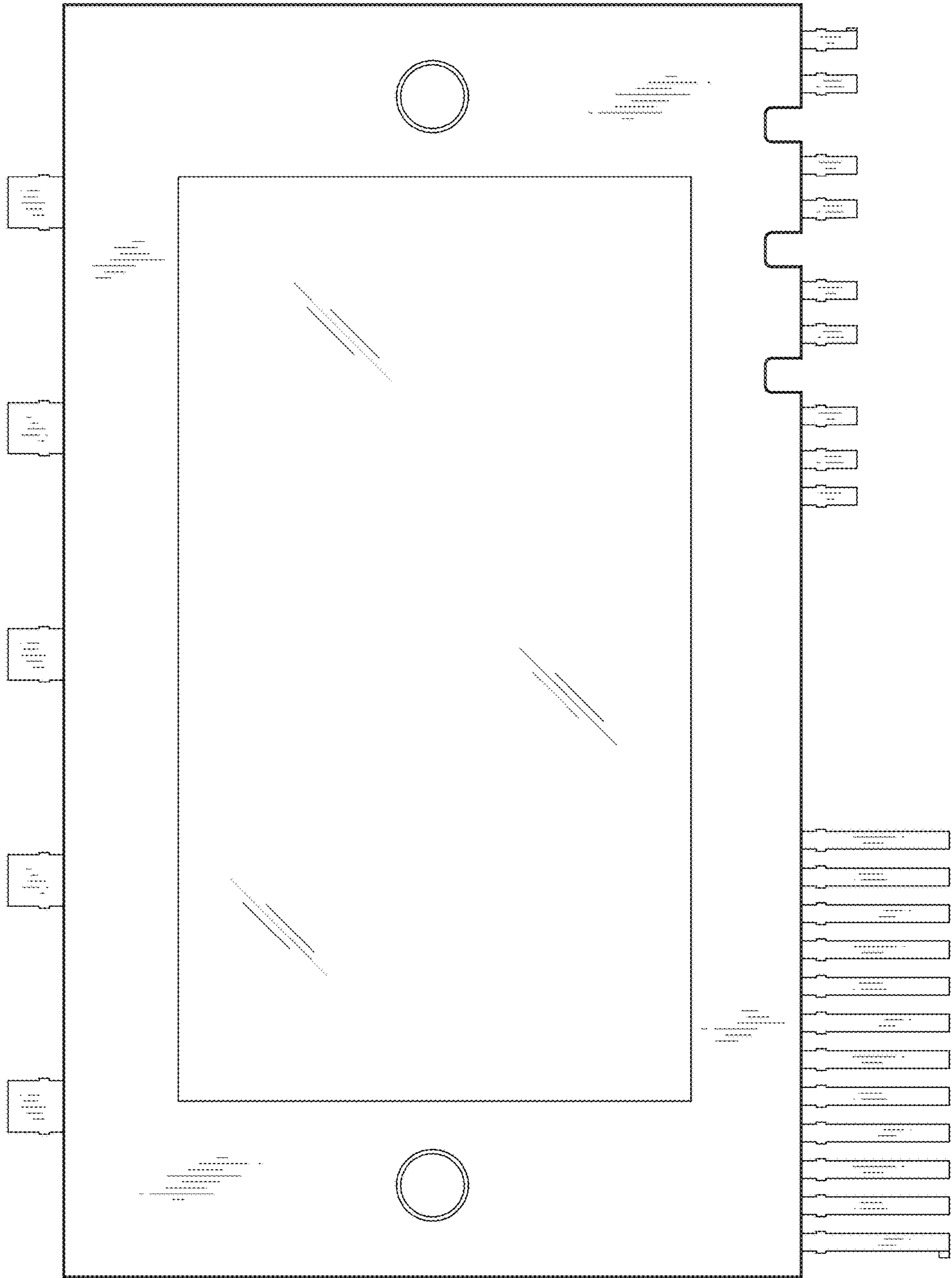


Fig.7

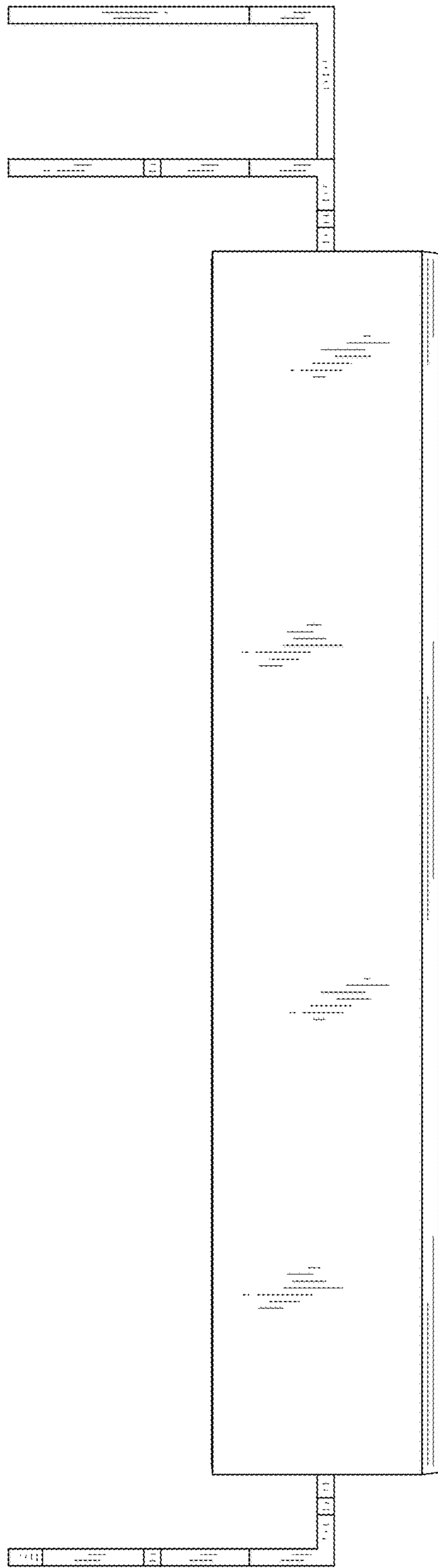


Fig.8

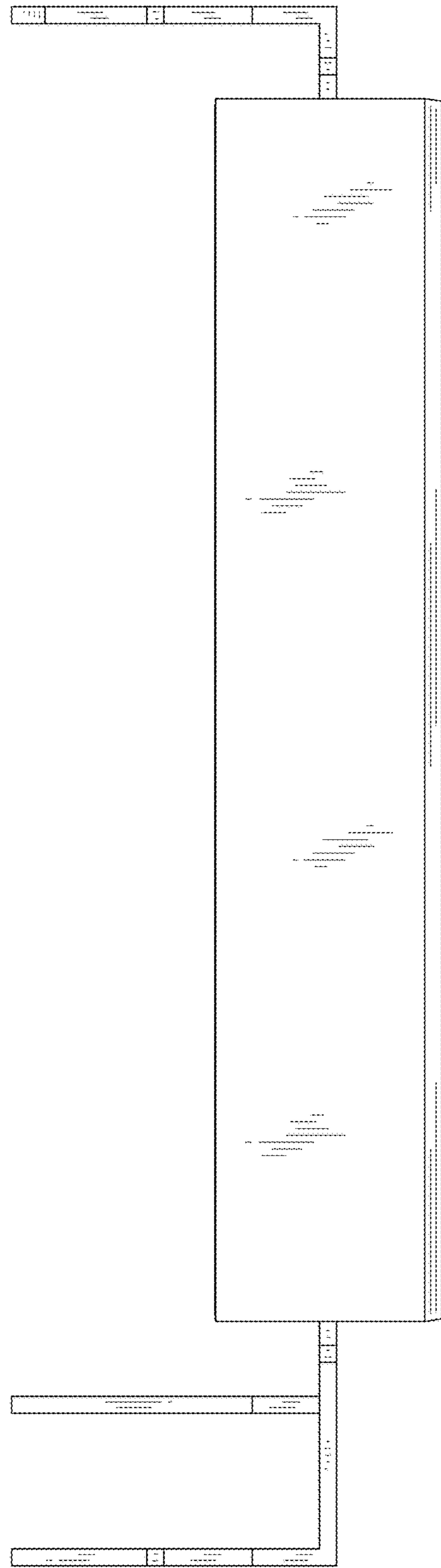


Fig. 9

