



US00D913256S

(12) **United States Design Patent**
Smith

(10) **Patent No.:** **US D913,256 S**
(45) **Date of Patent:** **** Mar. 16, 2021**

(54) **ANTENNA PATTERN FOR A SEMICONDUCTIVE SUBSTRATE CARRIER**

(71) Applicant: **Eryn Smith**, Pleasanton, CA (US)

(72) Inventor: **Eryn Smith**, Pleasanton, CA (US)

(**) Term: **15 Years**

(21) Appl. No.: **29/700,100**

(22) Filed: **Jul. 31, 2019**

(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**
USPC D13/182; D15/144, 144.1; 235/492, 235/487; 340/572.7

CPC . H01Q 7/00; H01Q 9/00; H01Q 11/00; H01Q 1/2225; H01Q 1/2283; H01Q 1/22; H01Q 1/12; Y10T 29/49018; Y10T 29/49016; H01F 41/041

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D254,687 S *	4/1980	Fadler	D13/182
D318,461 S *	7/1991	Hasegawa	D13/182
D319,045 S *	8/1991	Hasegawa	D13/182
D319,629 S *	9/1991	Hasegawa	D13/182
D319,814 S *	9/1991	Hasegawa	D13/182
D320,361 S *	10/1991	Karasawa	D10/103
5,491,364 A *	2/1996	Brandenburg	H05K 1/111 257/786
D376,744 S *	12/1996	Eisenblatter	D8/70
6,720,924 B2 *	4/2004	Tomomatsu	H01Q 1/2283 343/700 MS
D546,784 S *	7/2007	Hayashi	D13/182
D548,200 S *	8/2007	Hayashi	D13/182
D570,310 S *	6/2008	Sasaki	D13/182
D599,307 S *	9/2009	Blumberg, Jr.	D13/182

D599,308 S *	9/2009	Blumberg, Jr.	D13/182
D639,757 S *	6/2011	Root	D13/182
8,018,392 B2 *	9/2011	Fujii	H01Q 1/2283 343/853
8,348,170 B2 *	1/2013	Brod	H01Q 7/00 235/492
D689,835 S	9/2013	Smith	
8,528,827 B2 *	9/2013	Aoki	G06K 19/077 235/492
8,570,222 B2 *	10/2013	Alexopoulos	H01Q 1/36 343/700 MS
D716,742 S *	11/2014	Jang	D13/182
D723,077 S *	2/2015	Sakata	D15/140
D724,553 S *	3/2015	Choi	D13/182
9,059,503 B2 *	6/2015	Plettemeier	H01L 23/66
D741,921 S *	10/2015	Jarvius	D15/136
9,246,415 B2	1/2016	Smith	
9,754,809 B2	9/2017	Smith	
9,966,294 B2	5/2018	Smith	
9,984,913 B2	5/2018	Smith	

(Continued)

Primary Examiner — Elizabeth J Oswecki

(57) **CLAIM**

The ornamental design for an antenna pattern for a semi-conductive substrate carrier, as shown and described.

DESCRIPTION

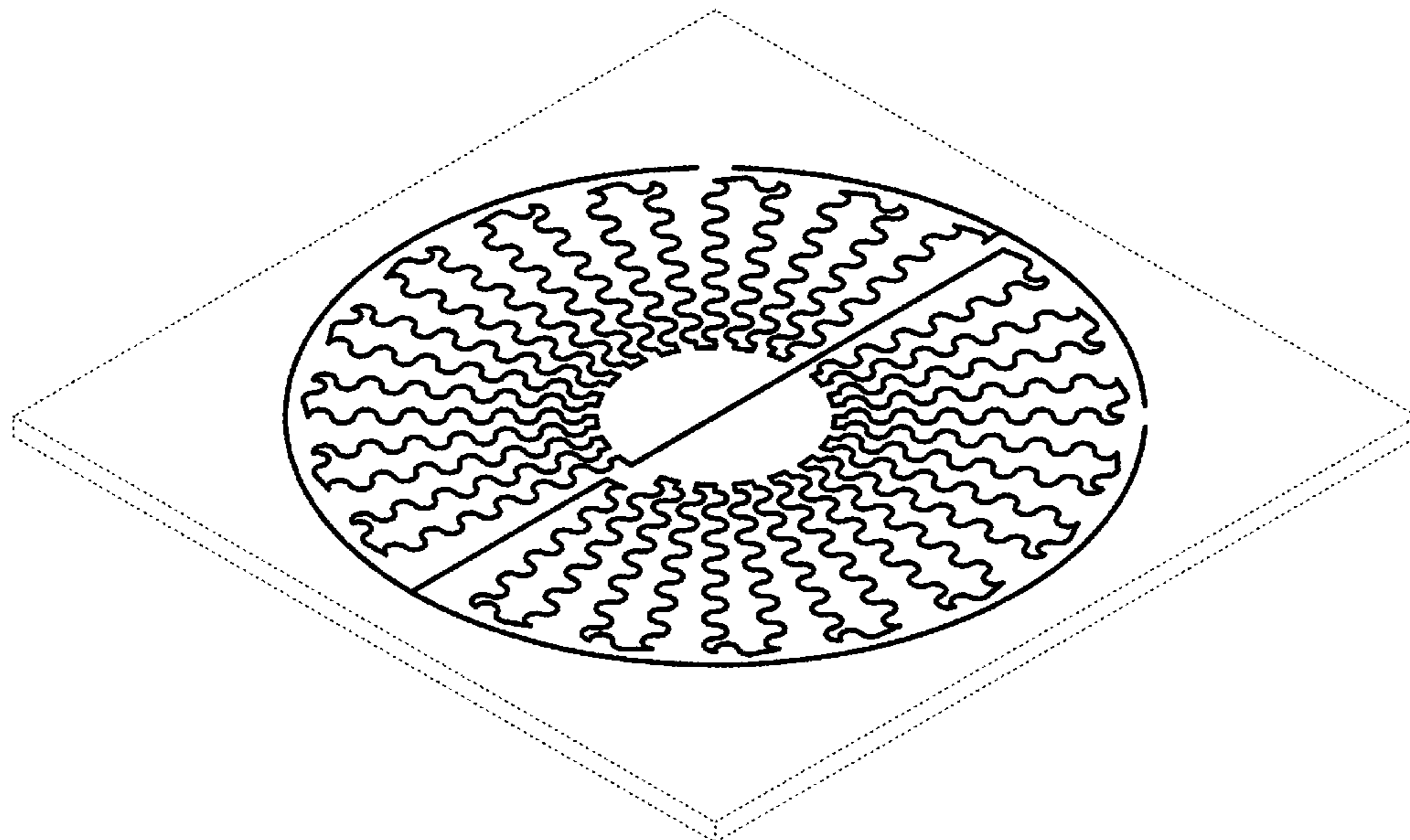
FIG. 1 is a top, front, left perspective view of an antenna pattern for a semiconductive substrate carrier showing my new design;

FIG. 2 is a top, rear, right perspective view thereof; and, FIG. 3 is a top plan view thereof.

The square broken line showing a platform portion of a semiconductive substrate carrier in FIGS. 1 through 3 is included for the purpose of illustrating environmental structure and forms no part of the claimed design.

The claim is directed to only the solid lines that form the antenna pattern and not the surrounding surface area.

1 Claim, 3 Drawing Sheets



(56)

References Cited

U.S. PATENT DOCUMENTS

D826,185 S *	8/2018	Kosugi	D13/182
10,236,202 B2	3/2019	Smith		
D895,587 S *	9/2020	Atojoko	D14/230
2006/0221539 A1 *	10/2006	Morita	H01L 21/67103
				361/234
2011/0111593 A1 *	5/2011	Kanno	G03F 7/0002
				438/689
2018/0151403 A1	5/2018	Smith		

* cited by examiner

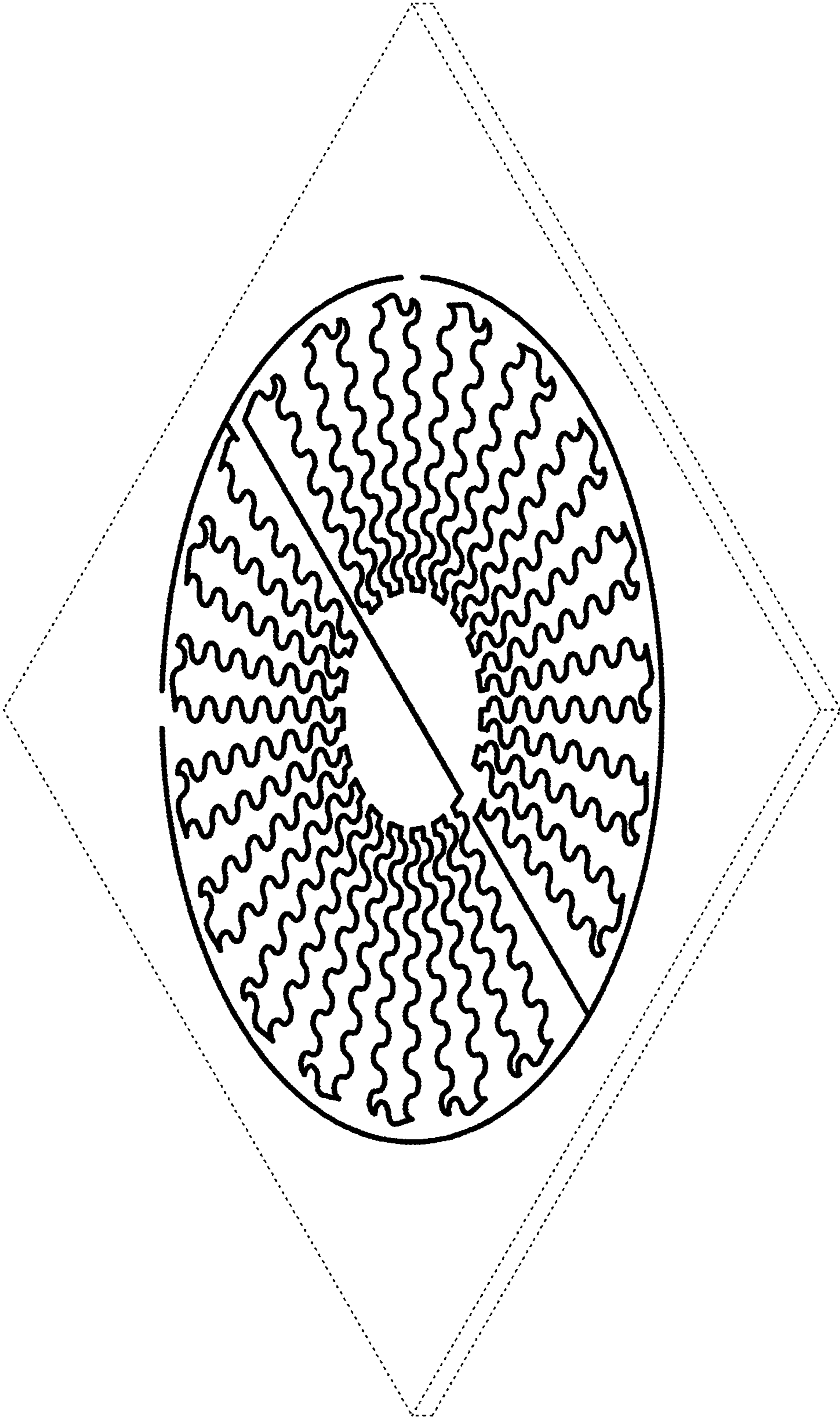


FIG. 1

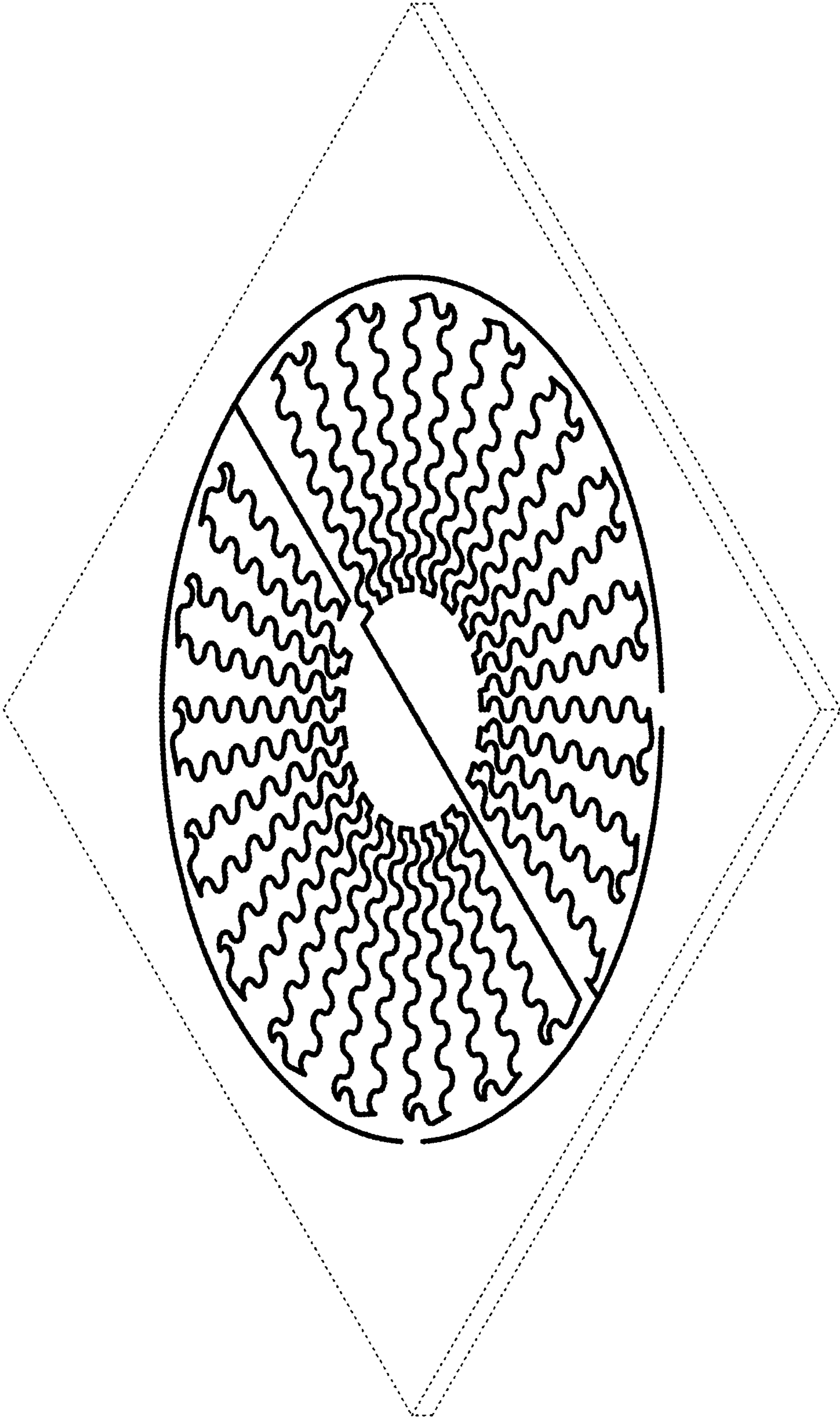


FIG. 2

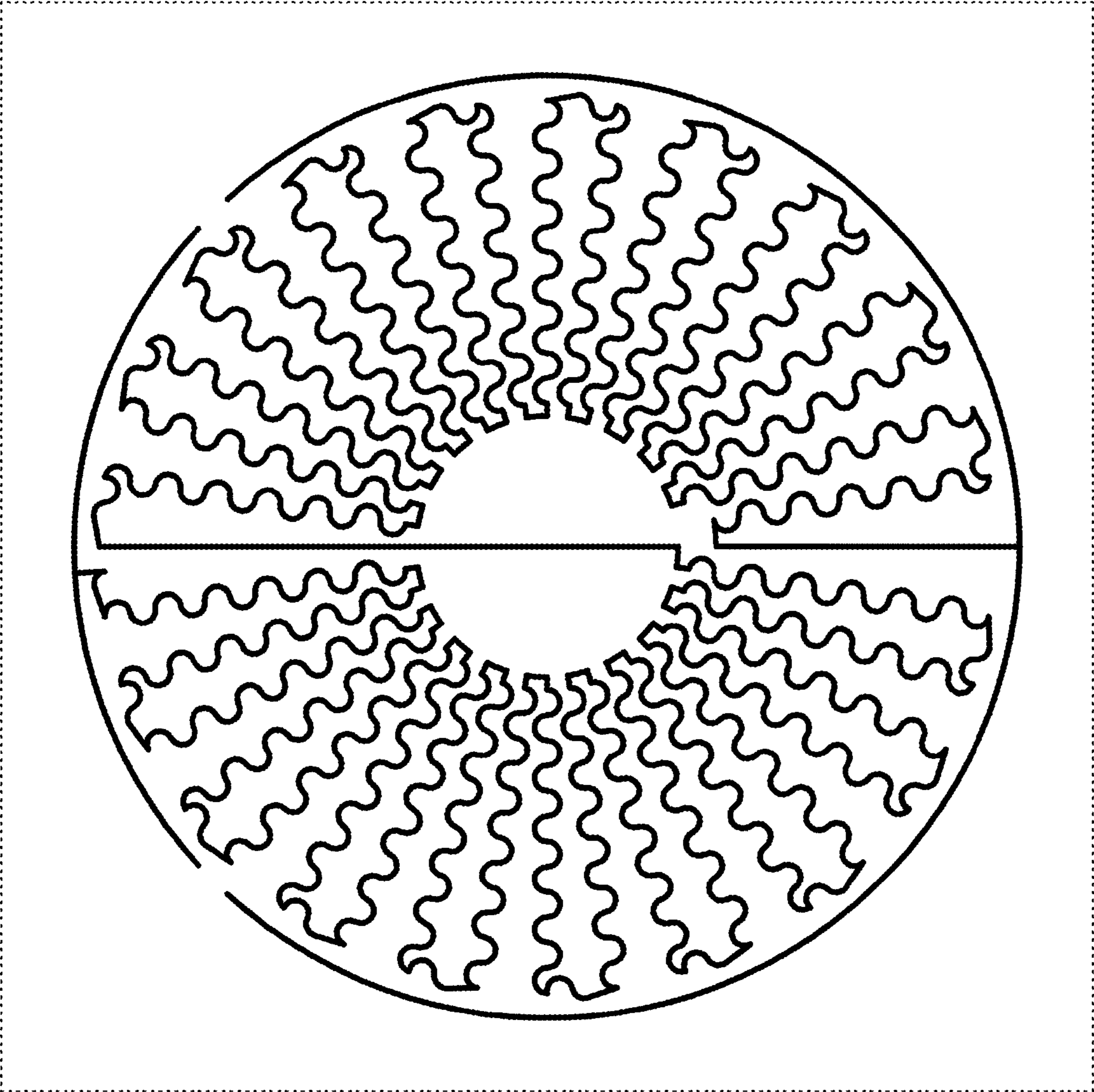


FIG. 3