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(12) **United States Design Patent**
Matthews et al.

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(54) **ELECTRONICS ENCLOSURE**

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(73) Assignee: **Applied Ideation, LLC**, San Diego, CA (US)

(*) Notice: This patent is subject to a terminal disclaimer.

(**) Term: **15 Years**

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(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/184**

(58) **Field of Classification Search**
USPC D13/102–110, 118–119, 184, 199;
D14/356, 432
CPC Y02E 60/10; Y02E 60/12; Y02E 60/122;
Y02E 60/124; Y02E 60/50; H01M 2/02;
H01M 2/022; H01M 2/0202; H01M
2/0207; H01M 2/0212; H01M 2/1061;
H01M 2/1022; H01M 2/1055; H01M
2/1066; H01M 2/105
See application file for complete search history.

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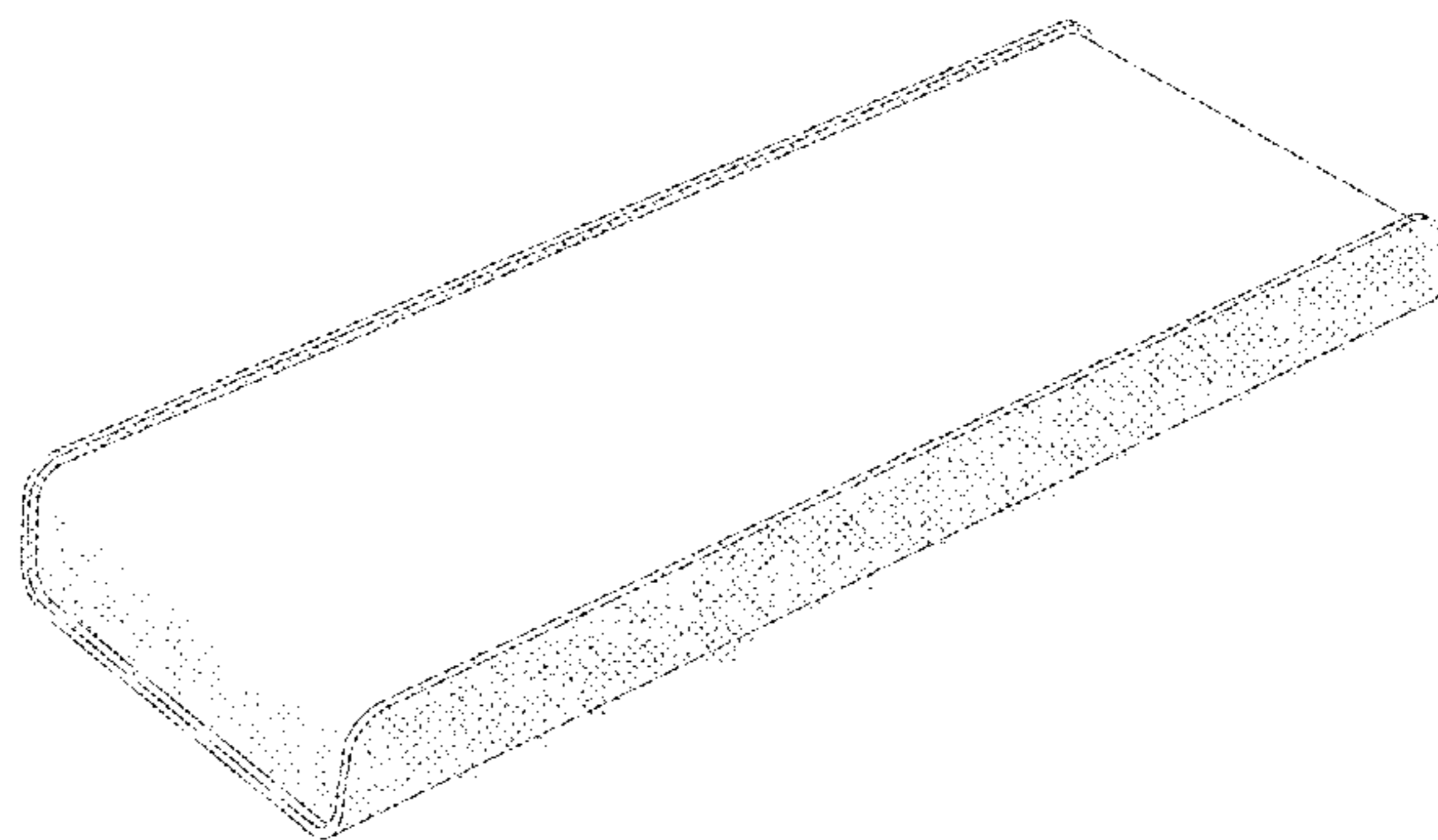
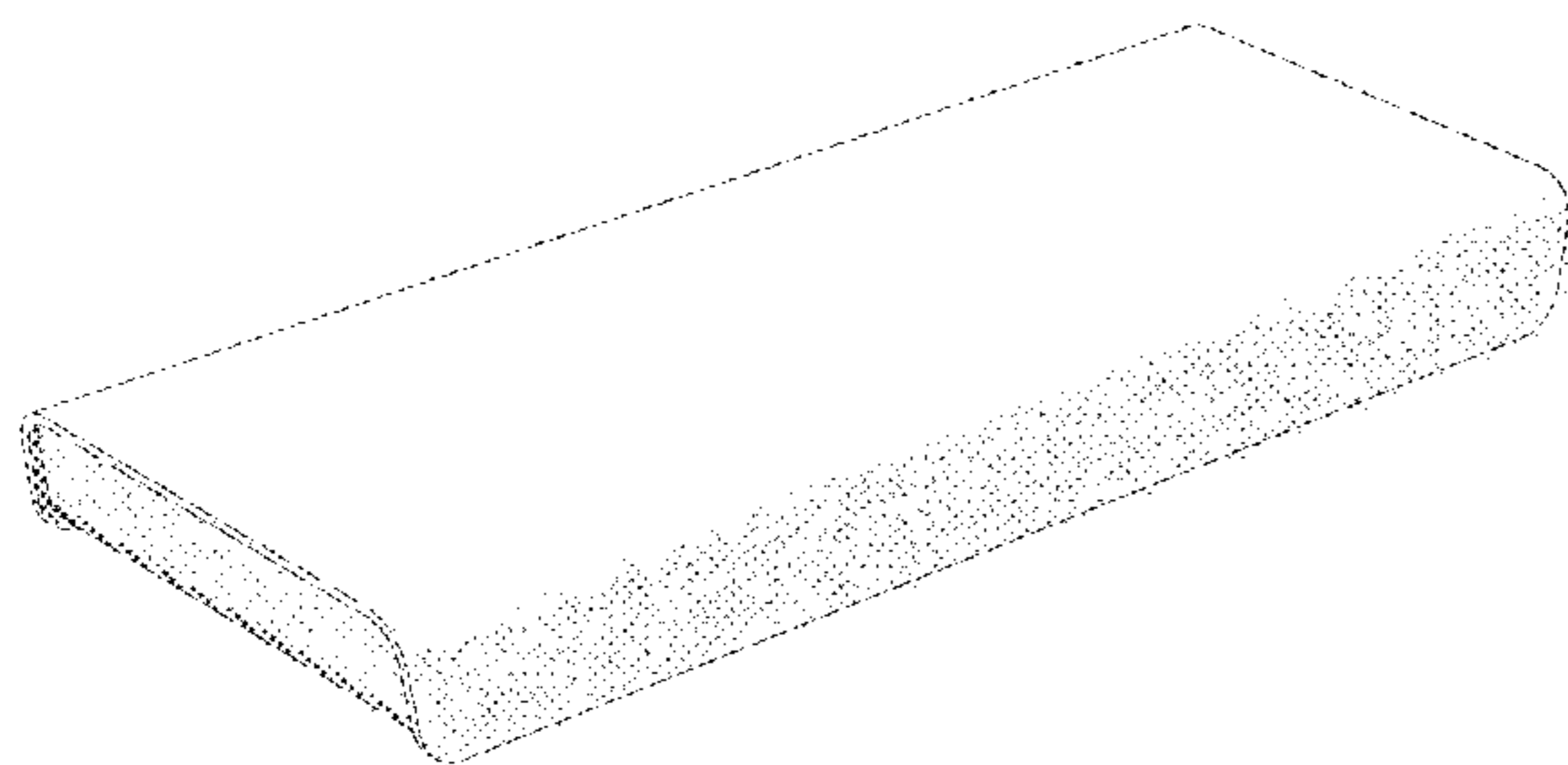
(57) **CLAIM**

We claim the ornamental design for an electronics enclosure, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view showing a top, front, and left side of our new design for the electronics enclosure;
FIG. 2 is a perspective view showing a bottom, rear, and right side of our new design for the electronics enclosure;
FIG. 3 is a front elevational view thereof, which is identical to the rear elevational view;
FIG. 4 is a right elevational view thereof, being an identical to the left elevational view;
FIG. 5 is a bottom view thereof; and,
FIG. 6 is a top view thereof.

1 Claim, 3 Drawing Sheets



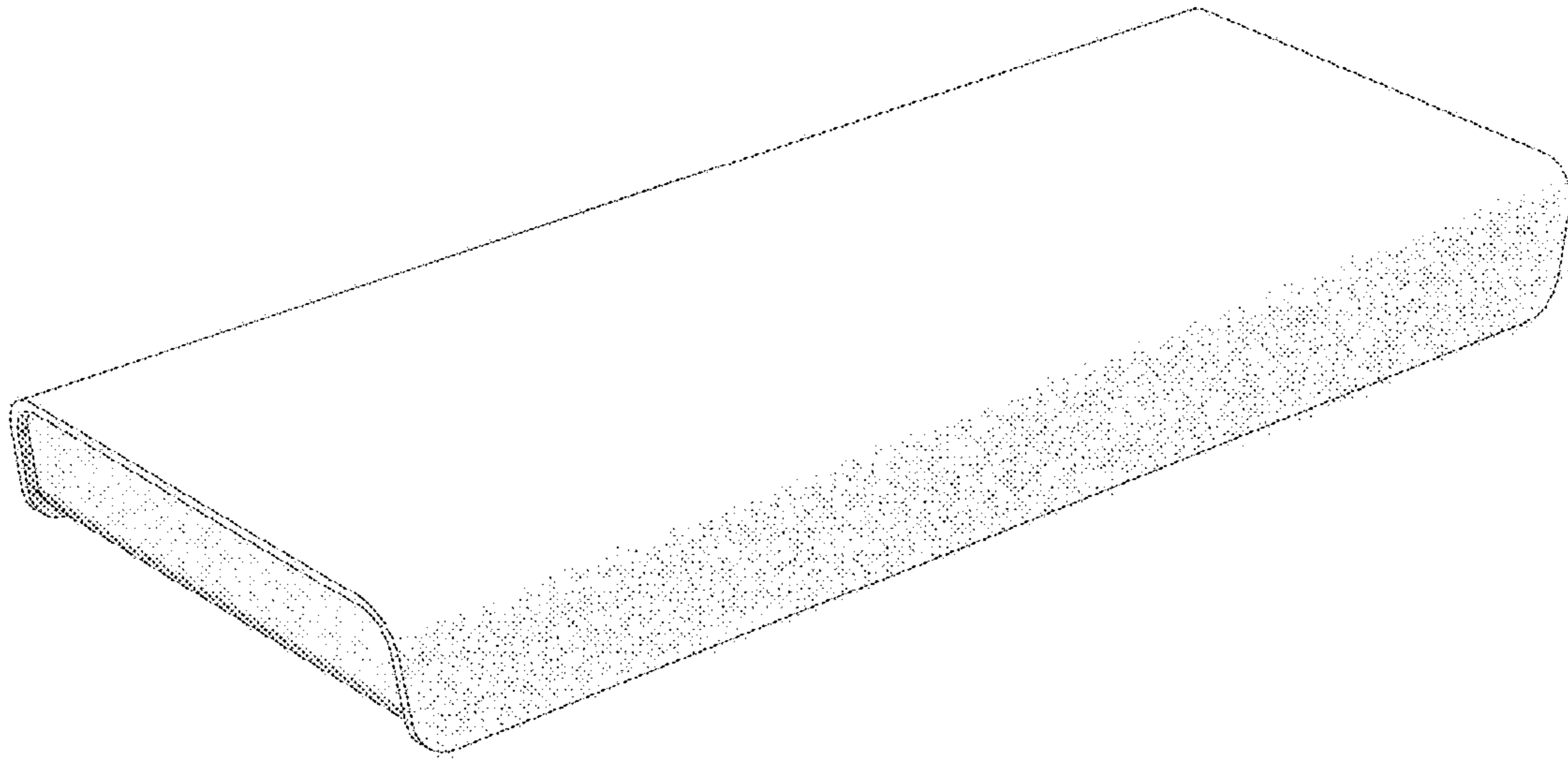


FIG. 1

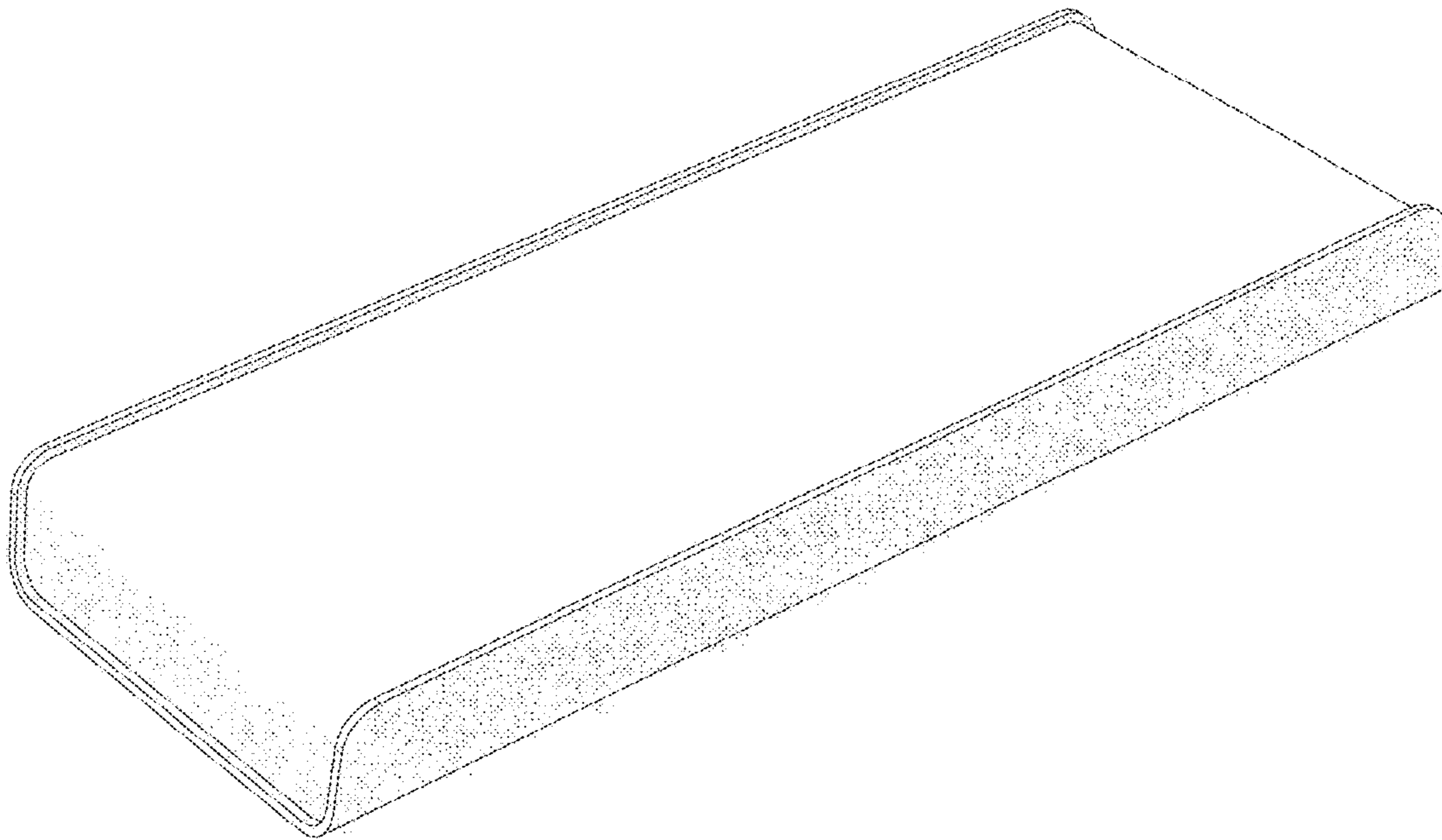


FIG. 2

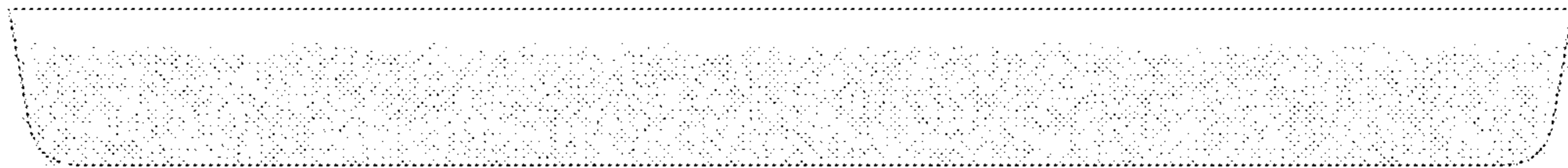


FIG. 3

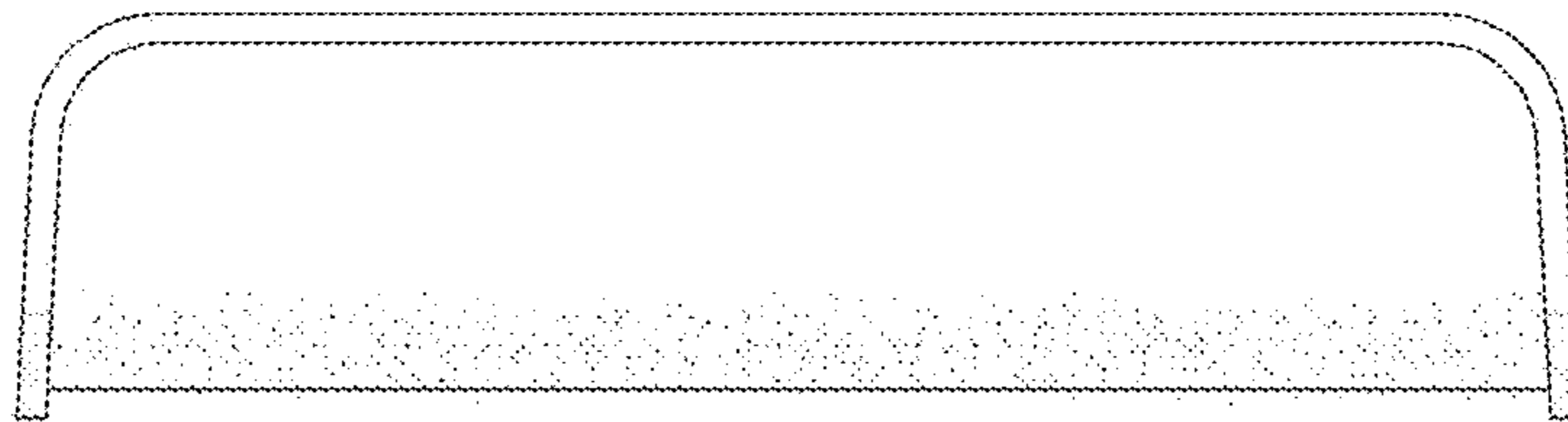


FIG. 4

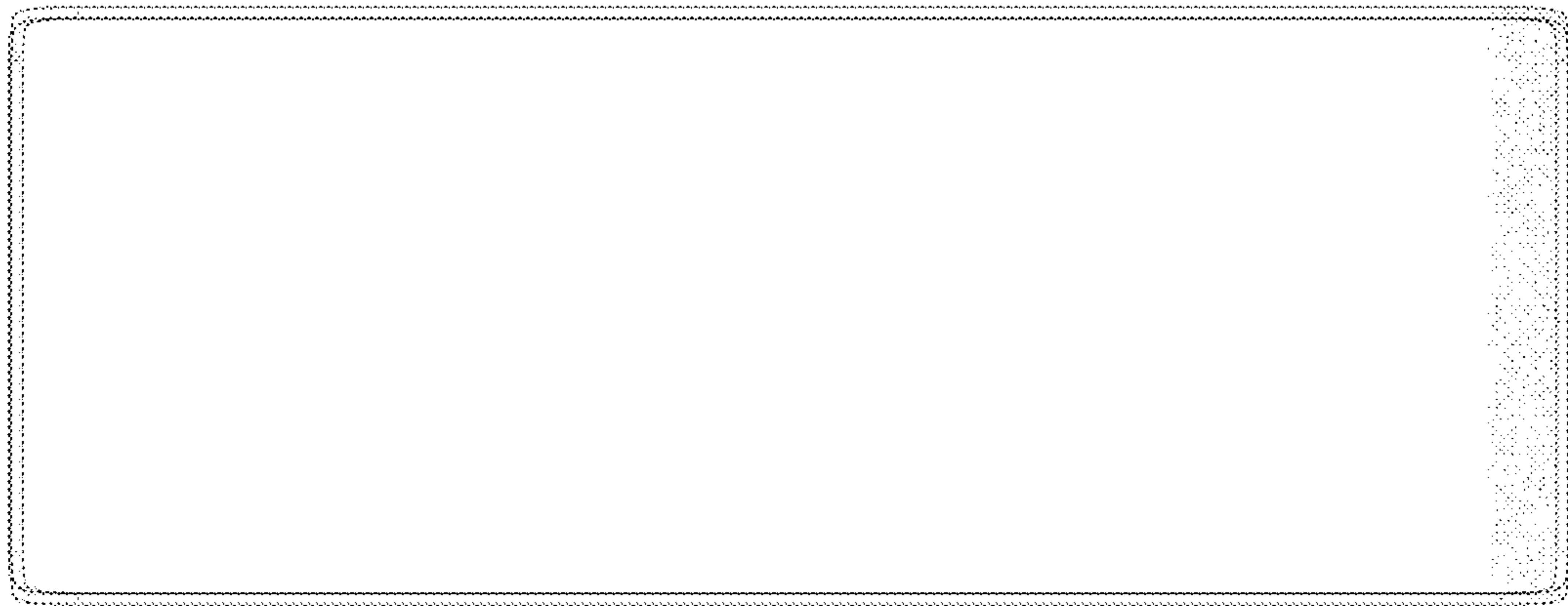


FIG. 5



FIG. 6