

US00D909319S

(12) **United States Design Patent** (10) **Patent No.:** **US D909,319 S**
Nordeen (45) **Date of Patent:** **** Feb. 2, 2021**

(54) **CIRCUIT BOARD**

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(73) Assignee: **Impact IP, LLC**, Rocklin, CA (US)

(**) Term: **15 Years**

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(51) **LOC (13) Cl.** **13-03**

(52) **U.S. Cl.**
USPC **D13/182**

(58) **Field of Classification Search**

USPC D13/182; 361/748, 752, 760, 761, 807,
361/808, 736, 719, 720; 174/250, 253,
174/255, 68.1; 363/144, 147

CPC H05K 3/30; H05K 3/301; H05K 3/284;
H05K 3/303; H05K 1/18; H05K 1/181;
H05K 1/182; H05K 1/183; H05K 1/185;
H05K 1/00; H05K 7/1417; H05K 7/02;
H05K 7/12; H05K 7/06; H01L 2924/14

See application file for complete search history.

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(57)

CLAIM

The ornamental design for a circuit board, as shown and described.

DESCRIPTION

FIG. 1 is a top and front isometric view of a circuit board, showing my new design.

FIG. 2 is a top and rear isometric view thereof.

FIG. 3 is a bottom and rear isometric view thereof.

FIG. 4 is a bottom and front isometric view thereof.

FIG. 5 is a top view thereof.

FIG. 6 is a bottom view thereof.

FIG. 7 is a rear view thereof.

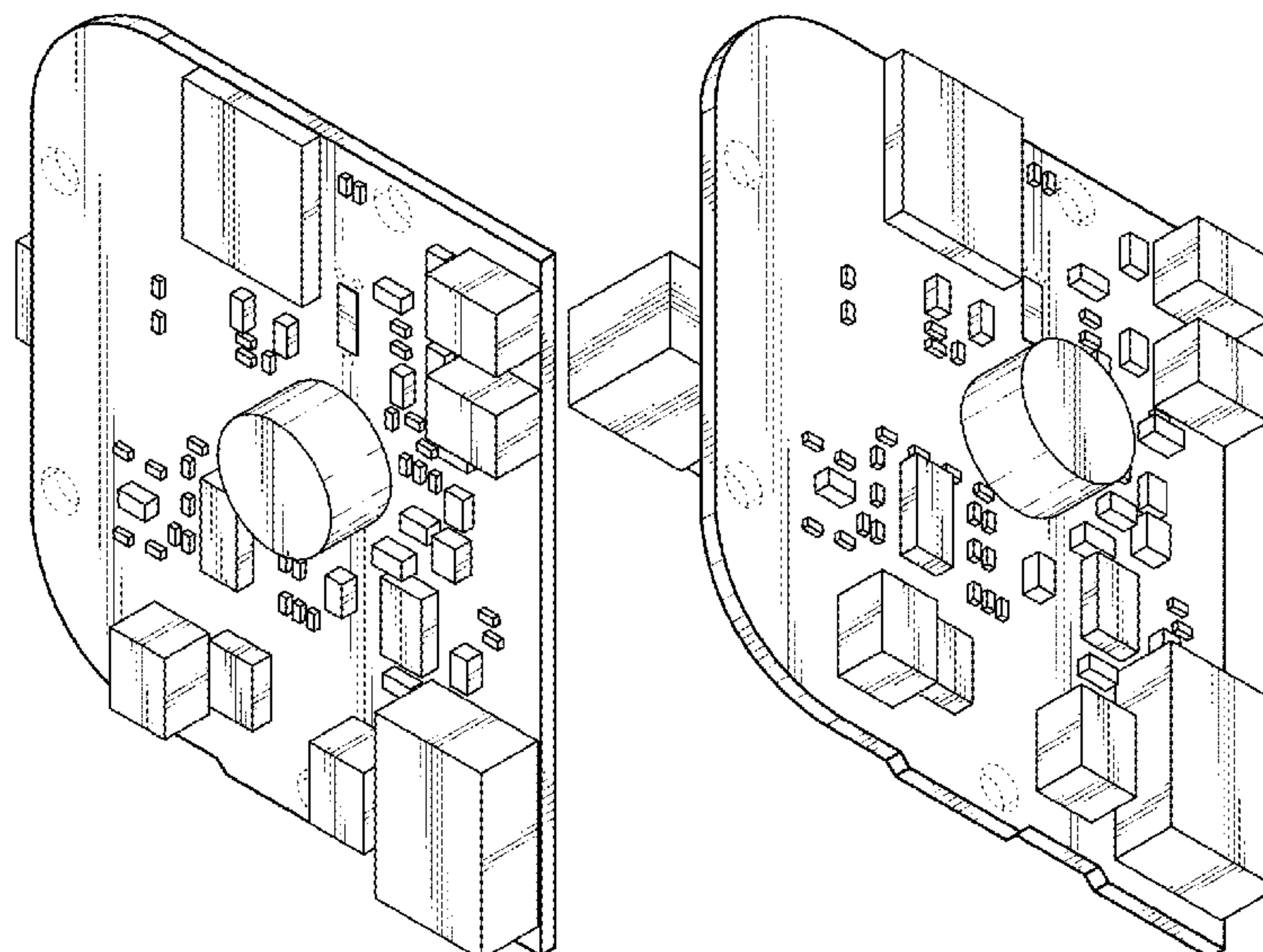
FIG. 8 is a front view thereof.

FIG. 9 is a right view thereof; and,

FIG. 10 is a left view thereof.

The broken lines form no part of the claimed design.

1 Claim, 4 Drawing Sheets



(56)

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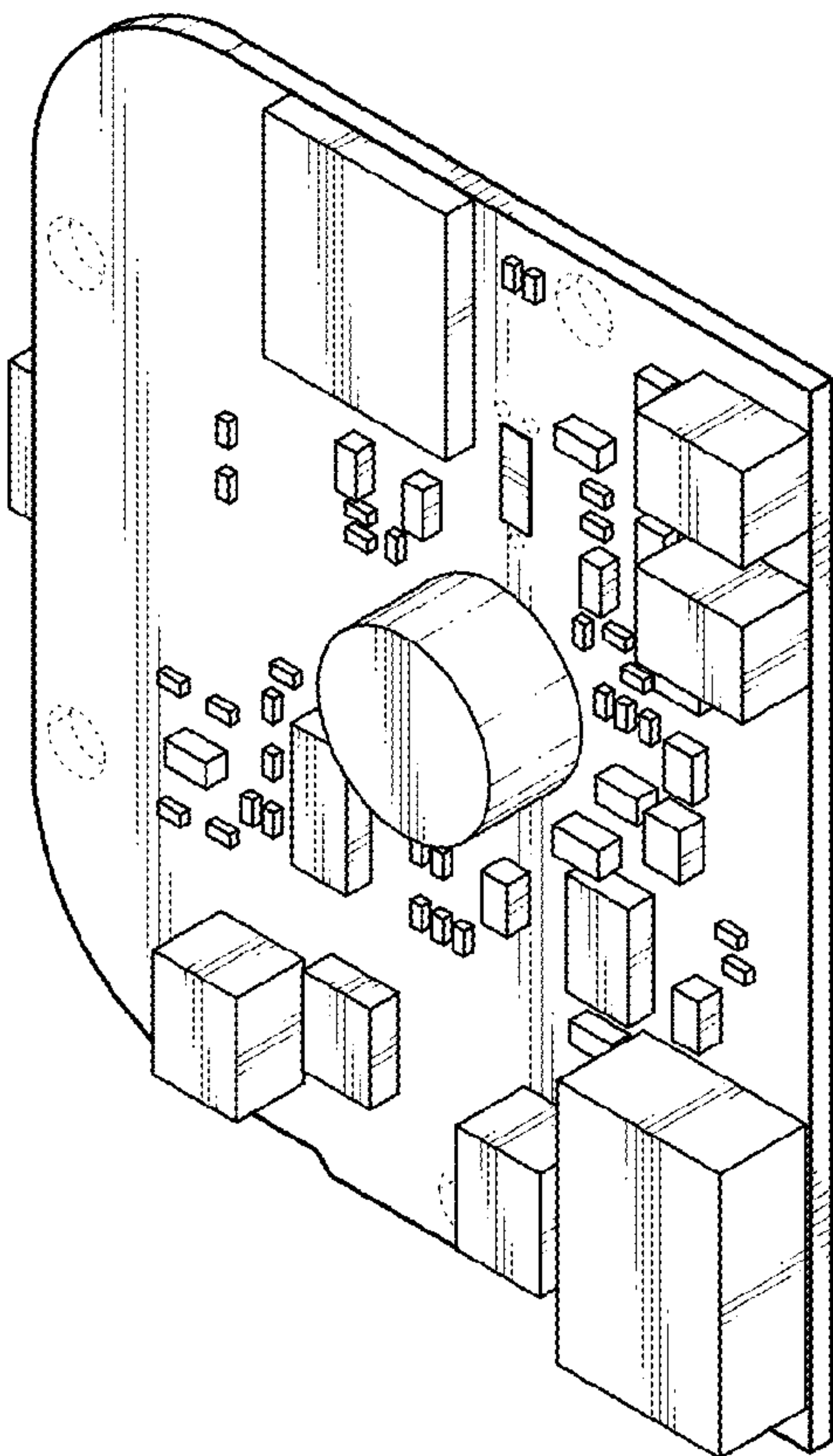


FIG. 1

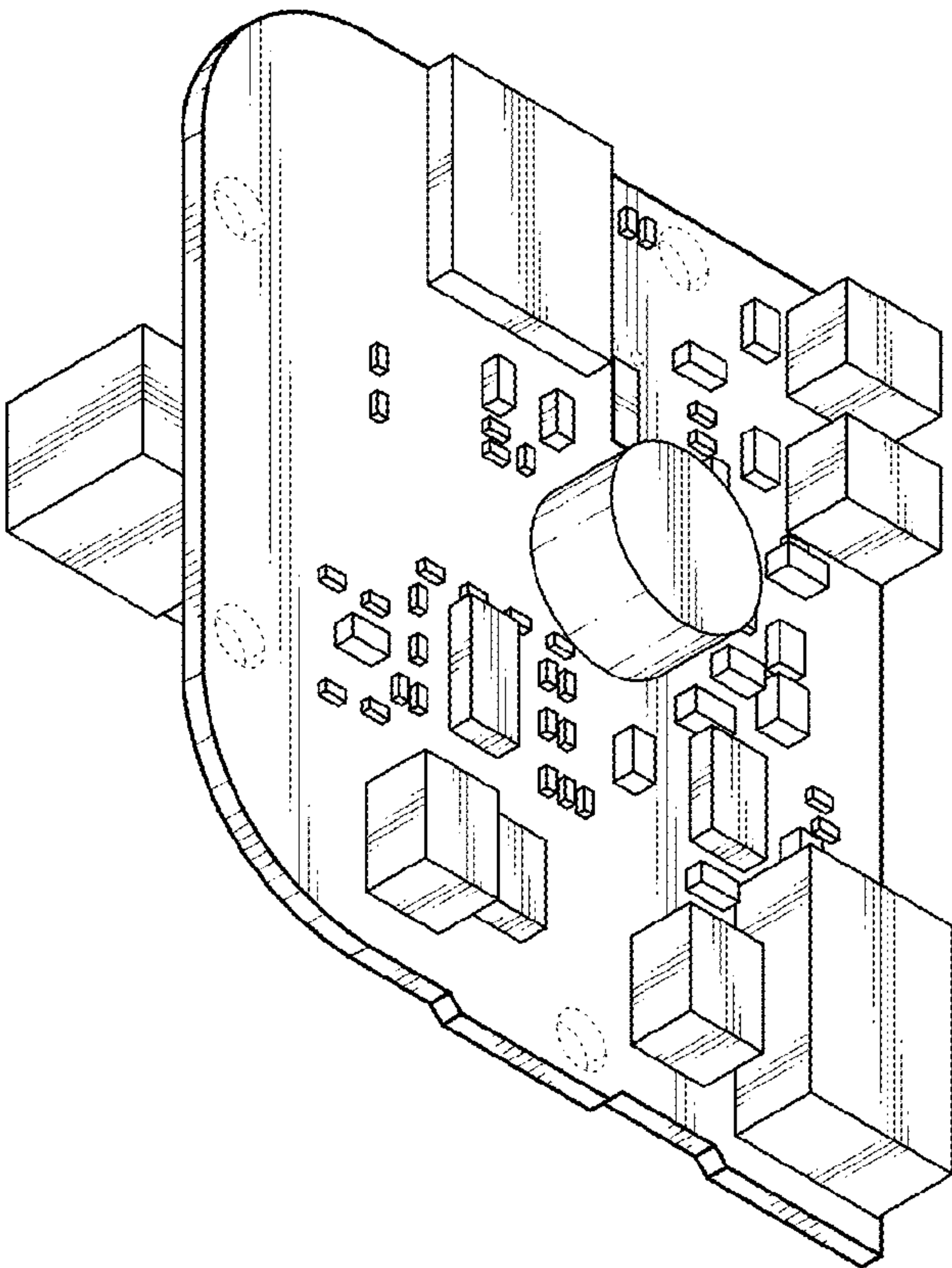


FIG. 2

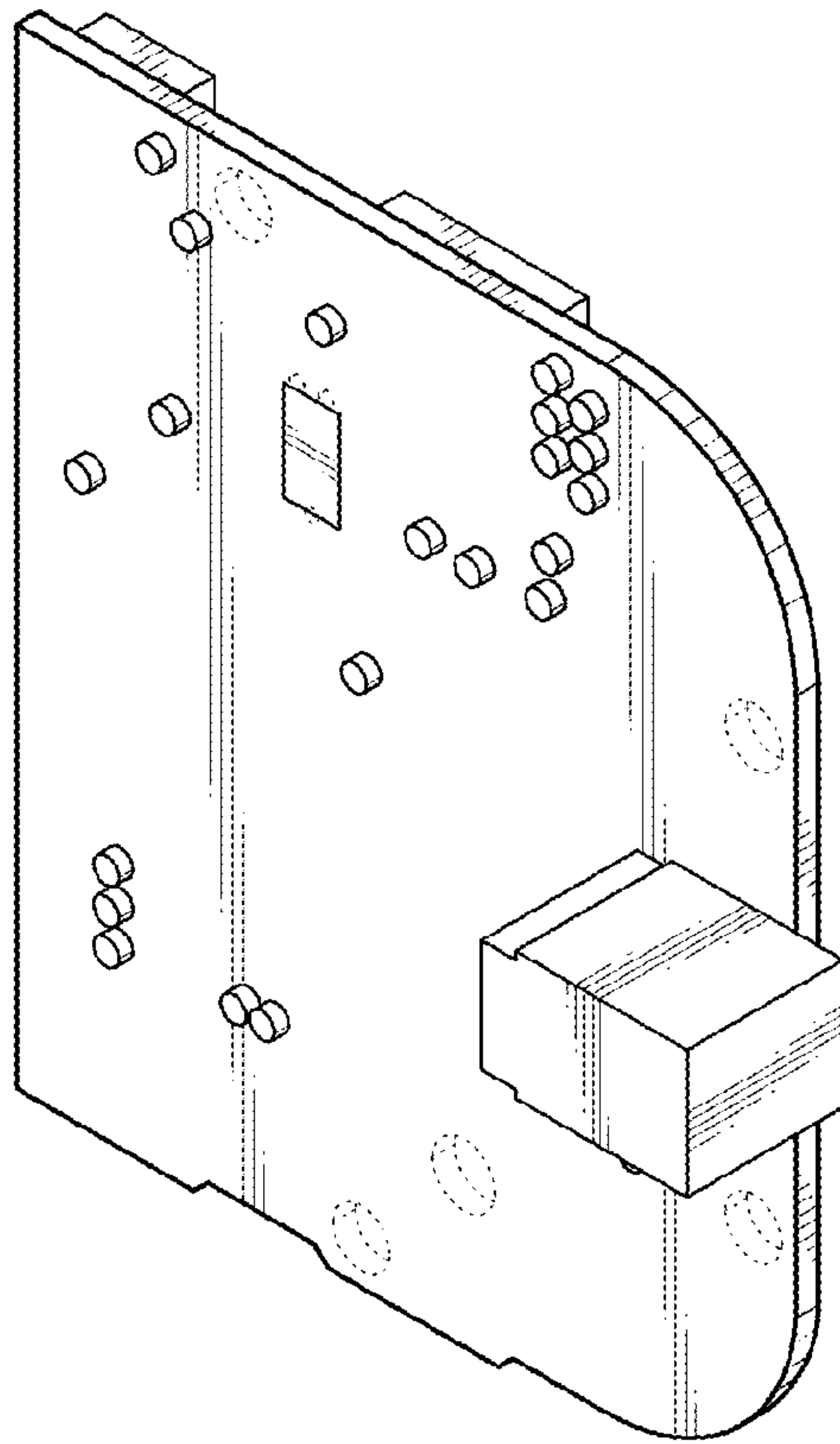


FIG. 3

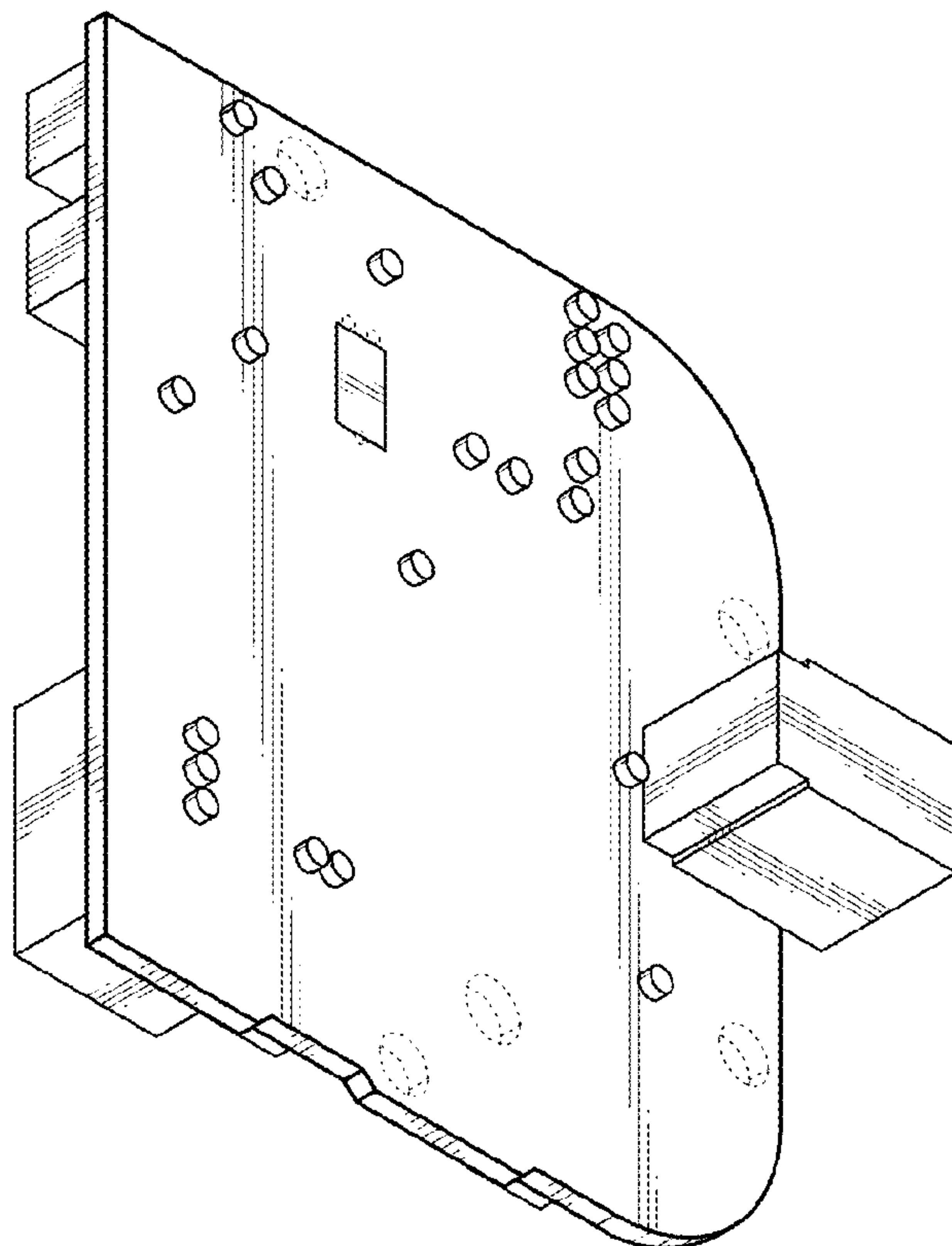


FIG. 4

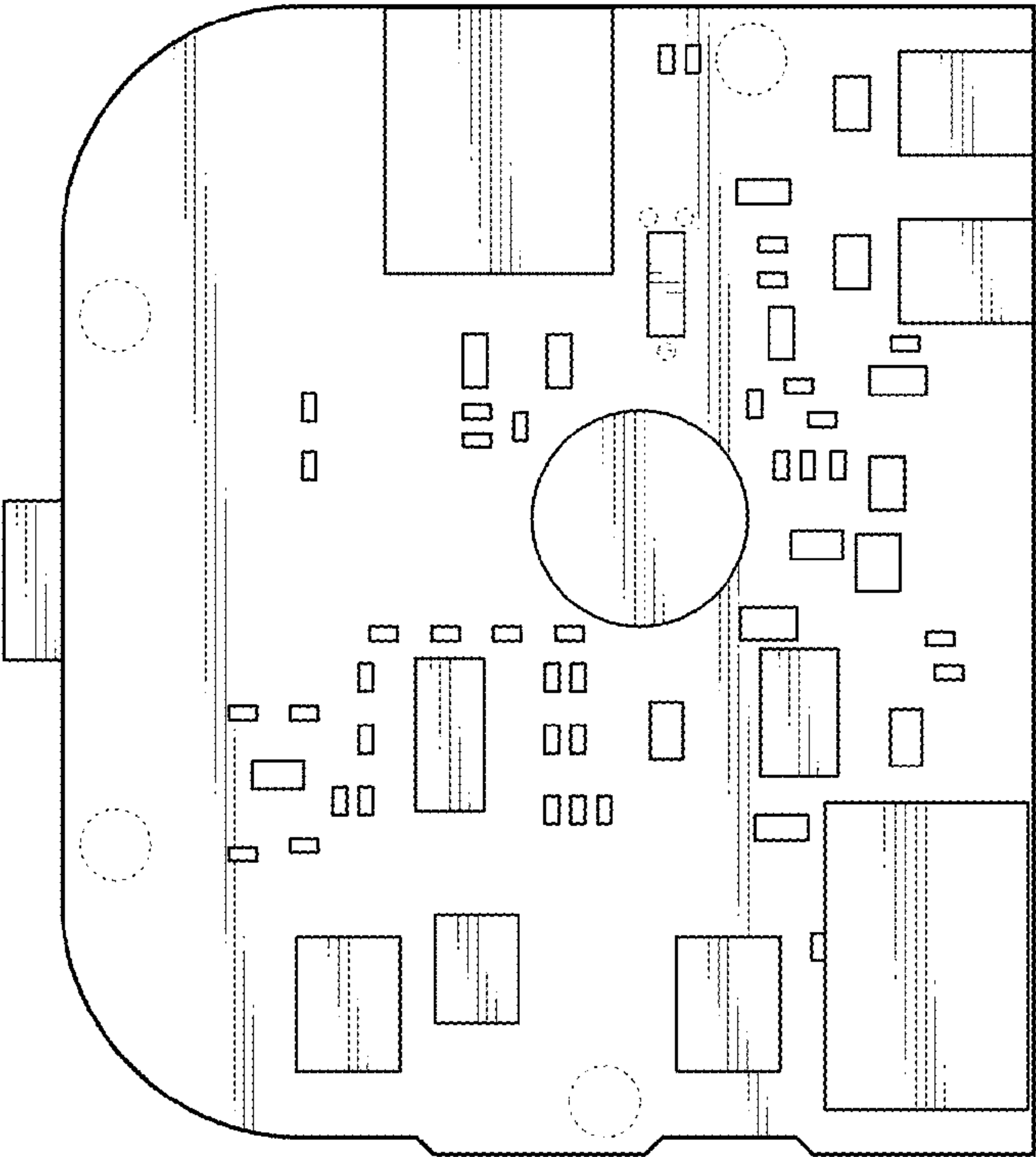


FIG. 5

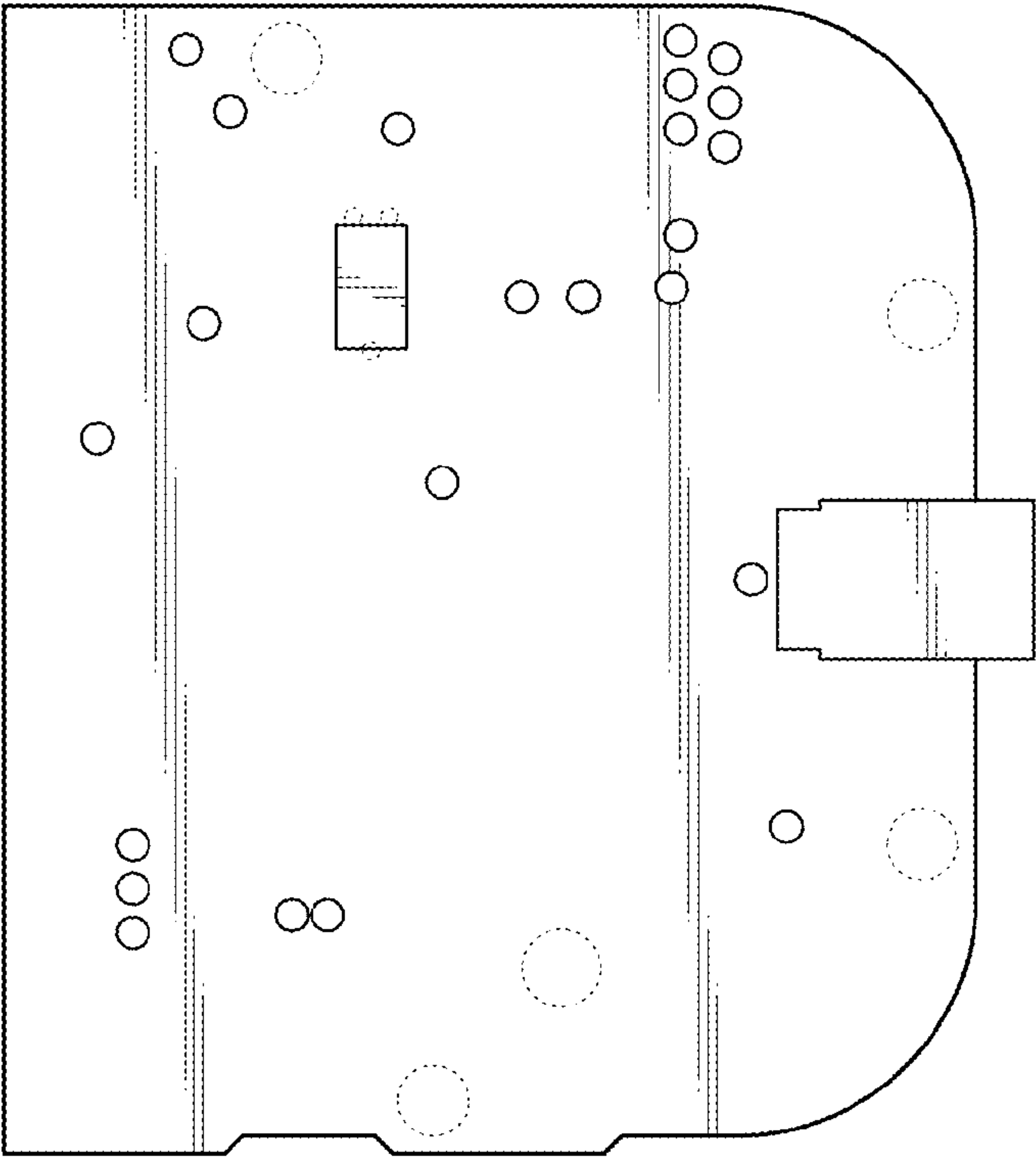


FIG. 6

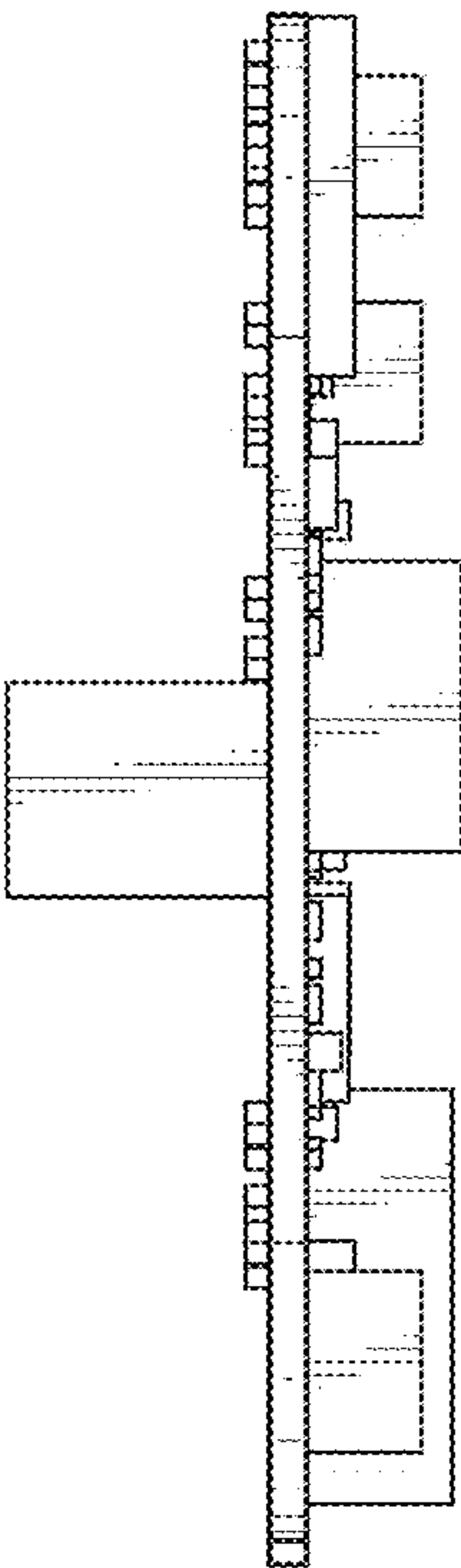


FIG. 7

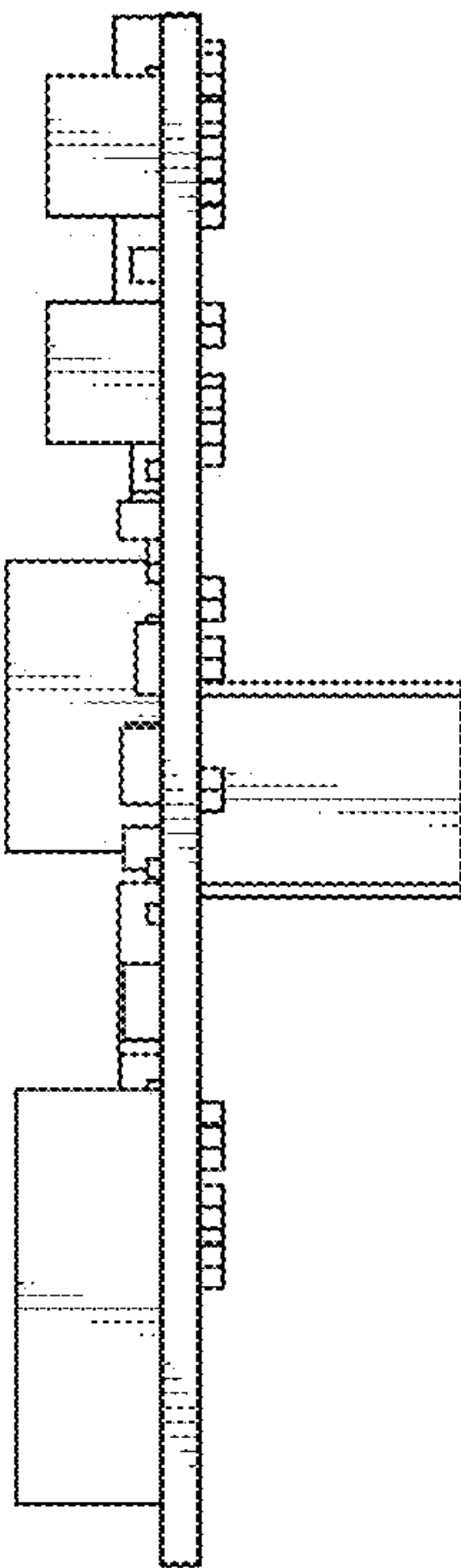


FIG. 8

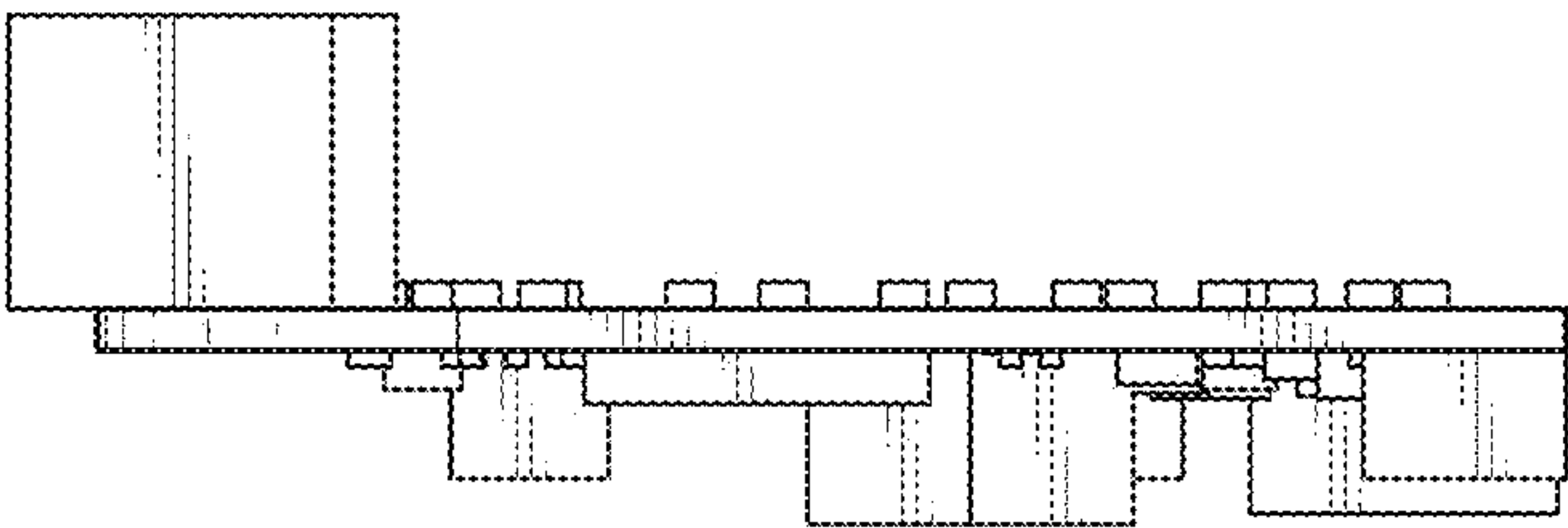


FIG. 9

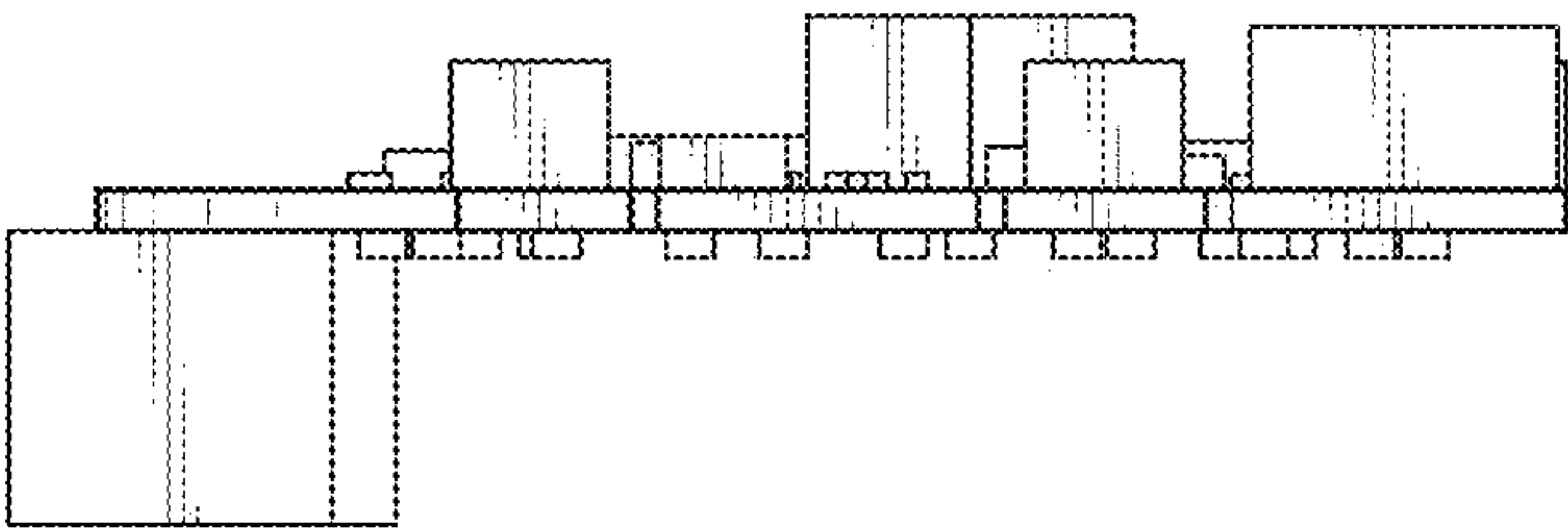


FIG. 10