



US00D908711S

(12) **United States Design Patent**  
**Zhou**

(10) **Patent No.:** **US D908,711 S**

(45) **Date of Patent:** **\*\* Jan. 26, 2021**

(54) **DATA MEMORY DRIVE**

(71) Applicant: **Shenzhen Longsys Electronics Co., Ltd.**, Shenzhen (CN)

(72) Inventor: **Fu Zhou**, Shenzhen (CN)

(73) Assignee: **SHENZHEN LONGSYS ELECTRONICS CO., LTD.**, Shenzhen (CN)

(\*\*) Term: **15 Years**

(21) Appl. No.: **29/705,267**

(22) Filed: **Sep. 11, 2019**

(30) **Foreign Application Priority Data**

Jul. 22, 2019 (CN) ..... 2019 3 0391328

(51) **LOC (13) Cl.** ..... **14-03**

(52) **U.S. Cl.**  
USPC ..... **D14/480.7**

(58) **Field of Classification Search**  
USPC ..... D14/480.1–480.7, 435.1, 411, 433, 436,  
D14/484.1, 137, 155, 203.1–203.8, 240,  
D14/242; D13/133, 154  
CPC ..... H05K 5/0278; G06K 19/07732  
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D494,969 S \* 8/2004 Lin ..... D14/480.7  
7,092,256 B1 \* 8/2006 Salazar ..... G06F 13/378  
361/679.32  
D542,789 S \* 5/2007 Depay ..... D14/435.1  
D546,328 S \* 7/2007 Kobayakawa ..... D14/480.4

D601,141 S \* 9/2009 Moshes ..... D14/480.7  
D624,012 S \* 9/2010 de Medeiros ..... D13/103  
D639,303 S \* 6/2011 Ni ..... D14/480.7  
8,411,428 B2 \* 4/2013 Lu ..... H05K 5/0278  
361/679.32  
D685,809 S \* 7/2013 Billington ..... D14/480.7  
D688,255 S \* 8/2013 Daniel ..... D14/480.4  
D707,690 S \* 6/2014 Billington ..... D14/480.7  
D759,661 S \* 6/2016 Liao ..... D14/480.7  
2009/0042433 A1 \* 2/2009 Bushby ..... H01R 13/6397  
439/352

\* cited by examiner

Primary Examiner — Angela J Lee

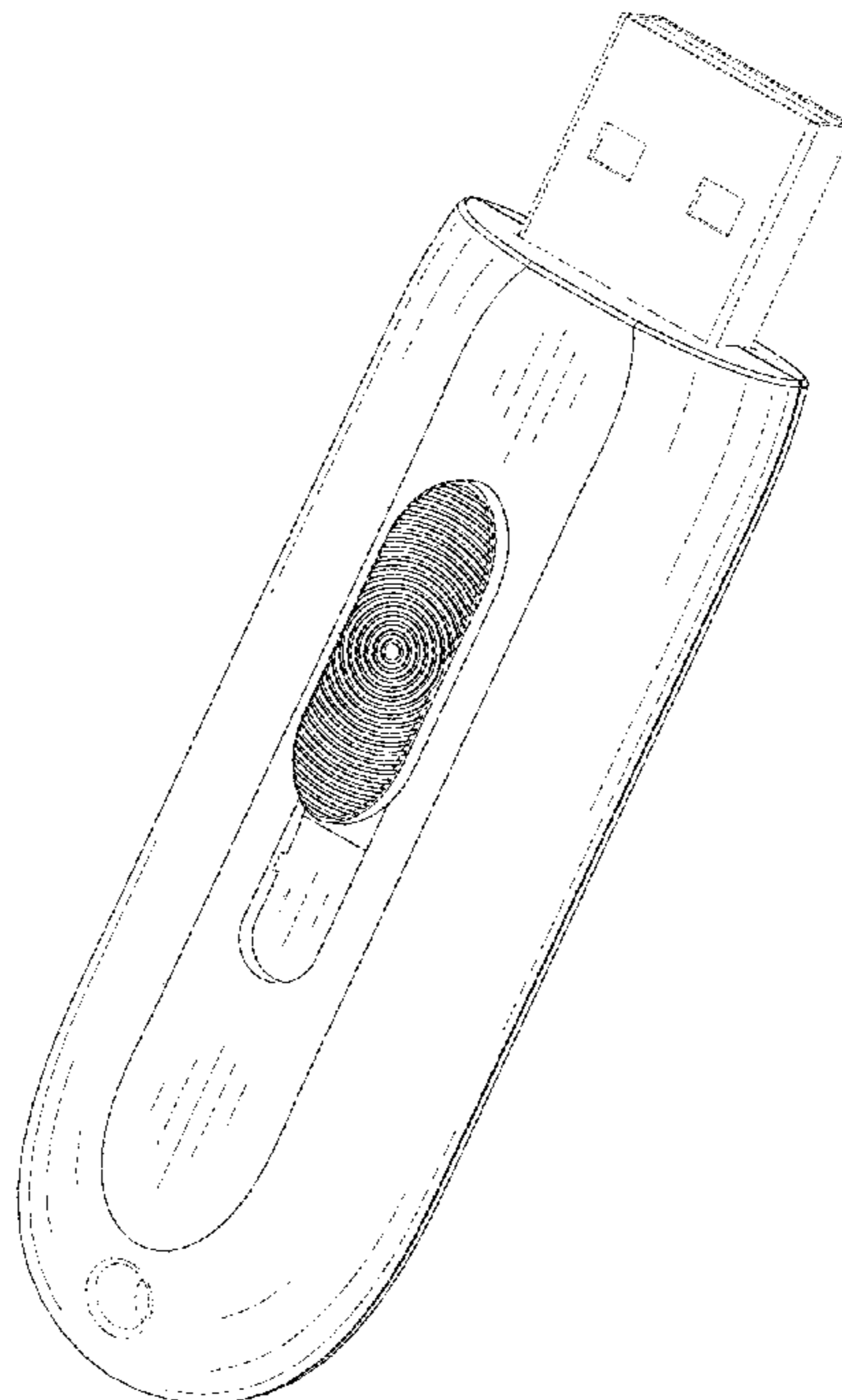
(57) **CLAIM**

The ornamental design for a data memory drive, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of a data memory according to an embodiment of the design;  
FIG. 2 is another perspective view thereof;  
FIG. 3 is a front elevational view thereof;  
FIG. 4 is a rear elevational view thereof;  
FIG. 5 is a left side elevational view thereof;  
FIG. 6 is a right side elevational view thereof;  
FIG. 7 is a top plan view thereof; and,  
FIG. 8 is a bottom plan view thereof.  
The broken lines in the drawings illustrate portions of the data memory drive that form no part of the claimed design. The shade lines in the figures show contour and not surface ornamentation.  
The dot-dash-dot broken lines are for the purpose of illustrating the boundaries of the claim, and form no part of the claimed design.

**1 Claim, 7 Drawing Sheets**



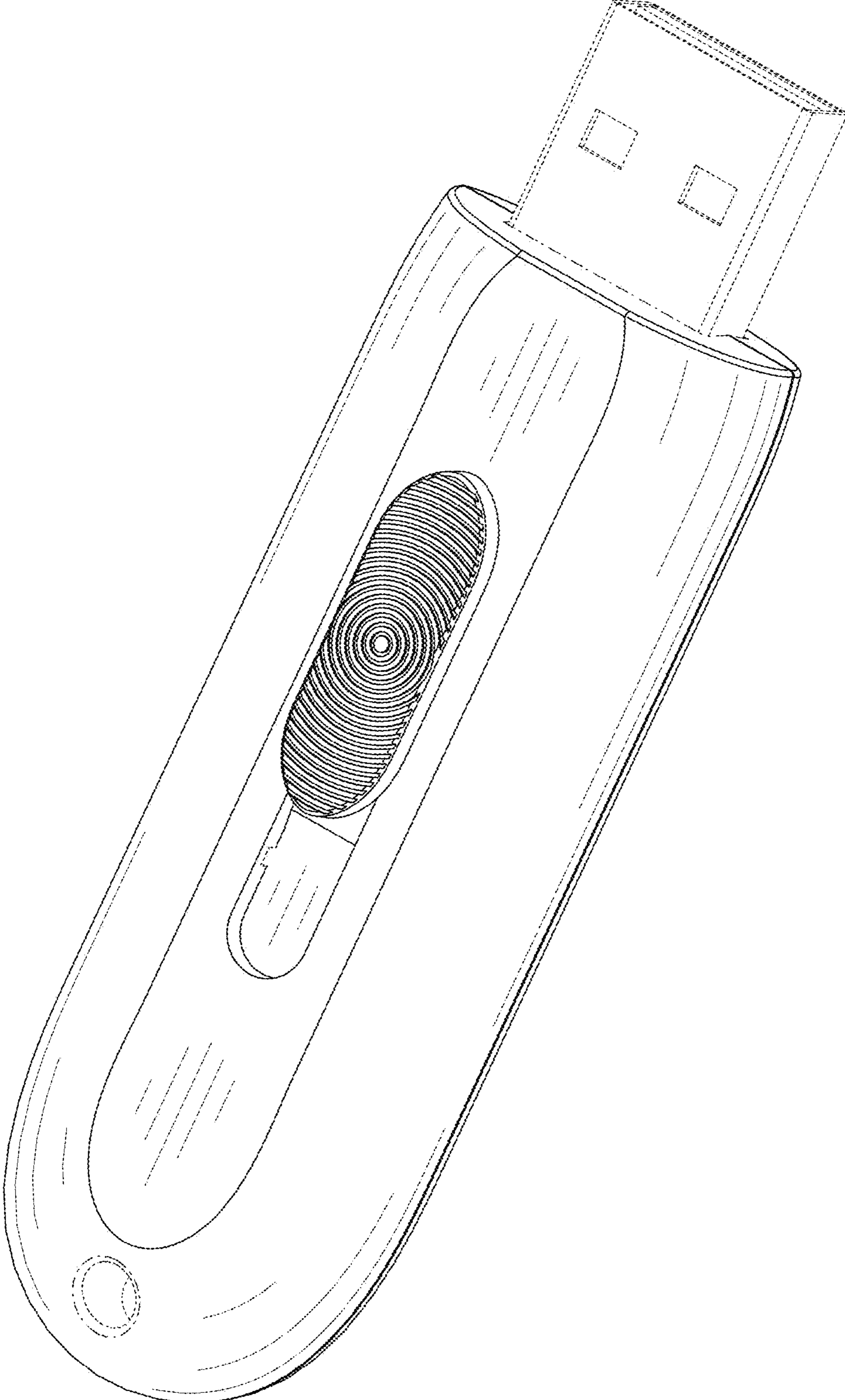


FIG. 1

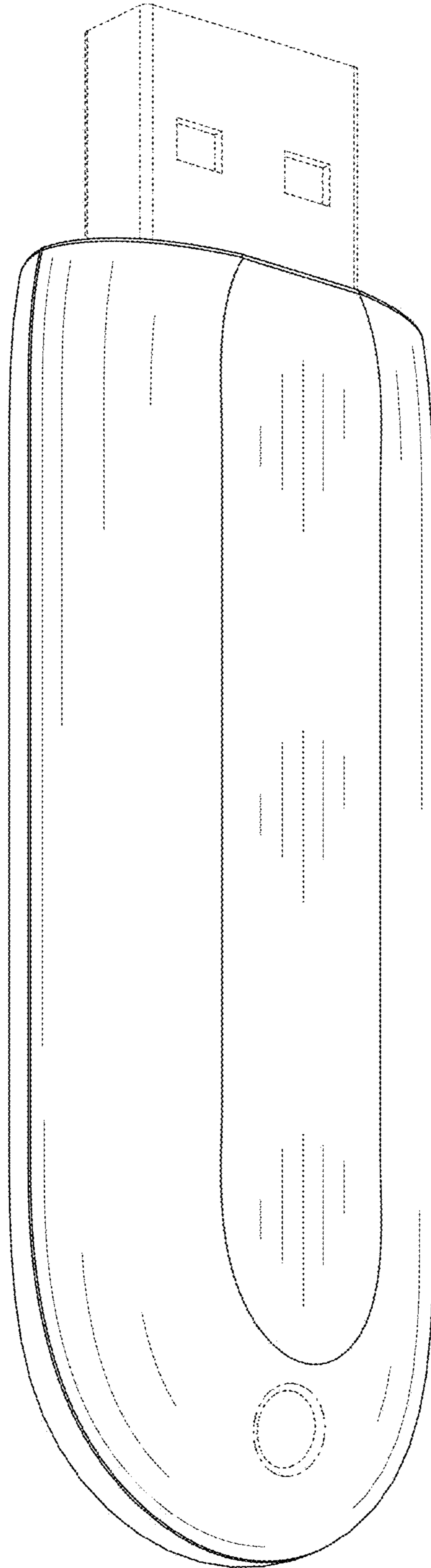


FIG.2

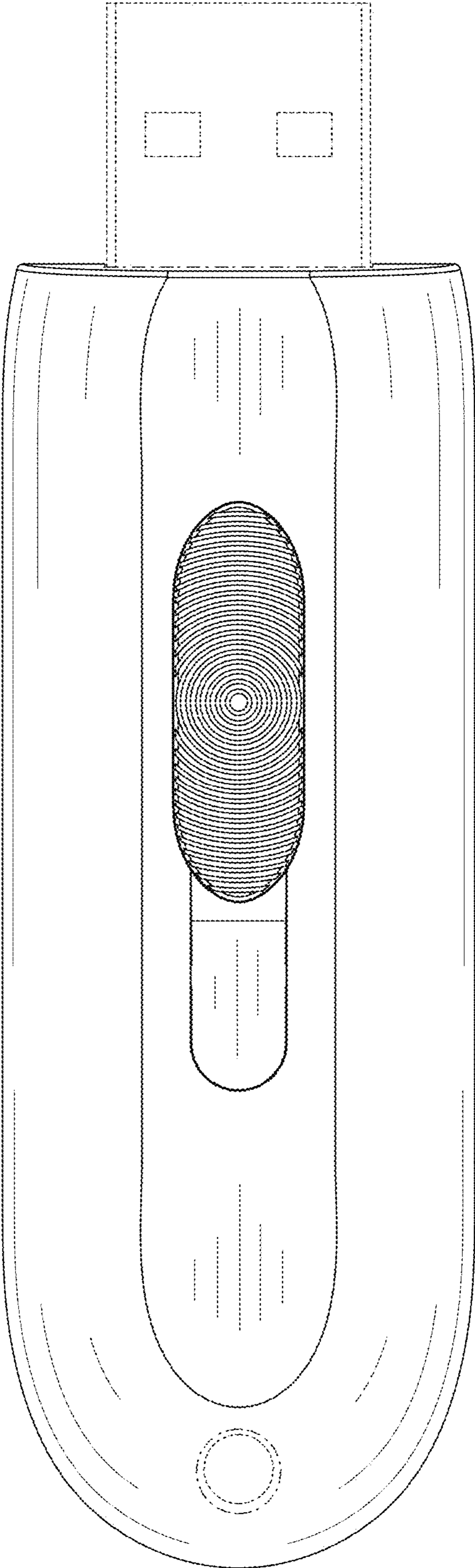


FIG.3

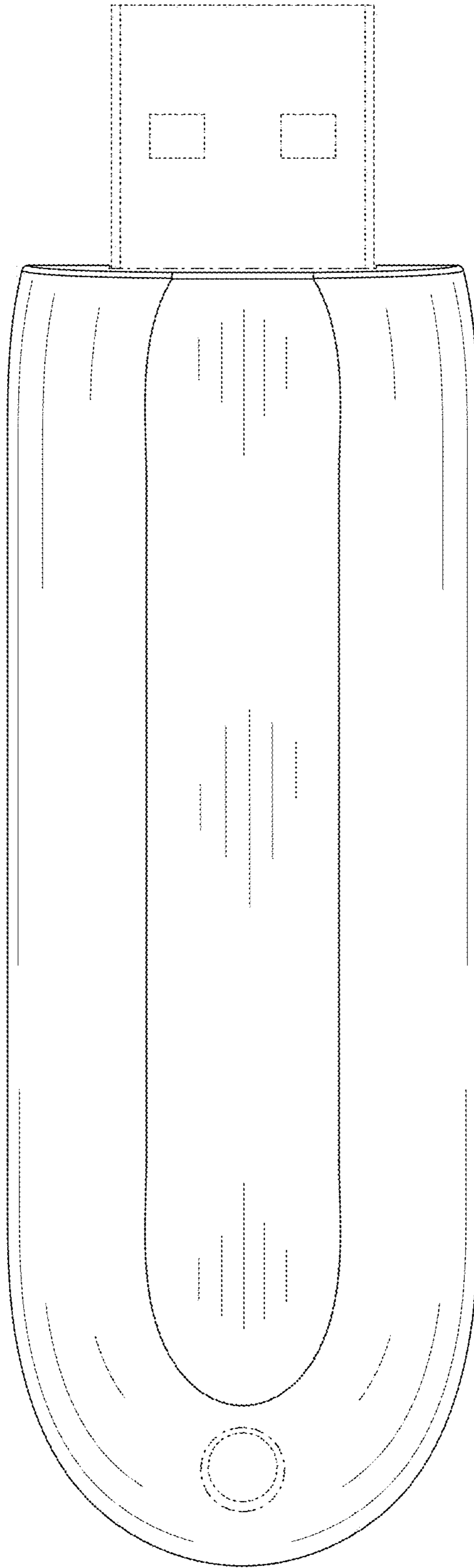


FIG.4

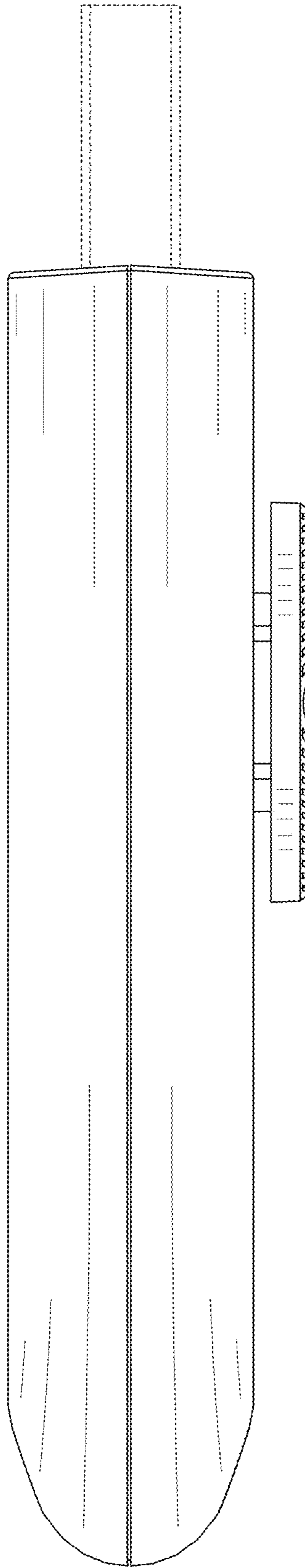


FIG.5

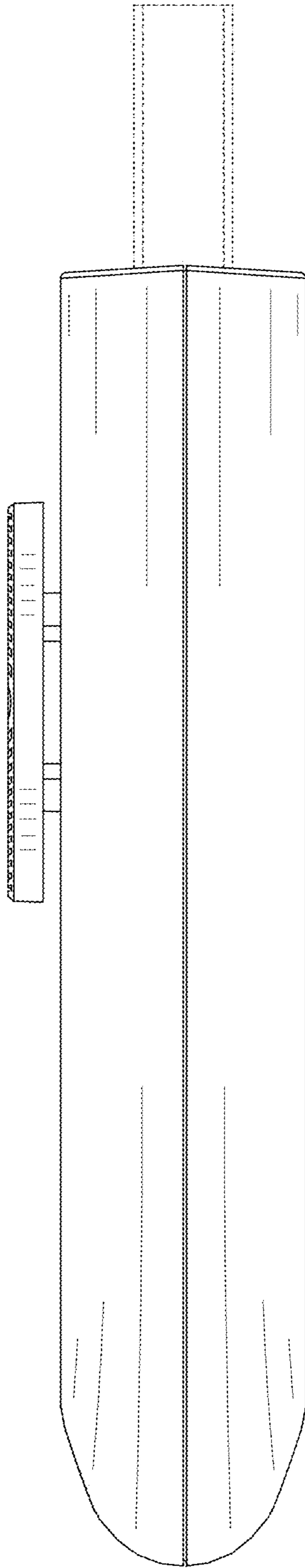


FIG.6

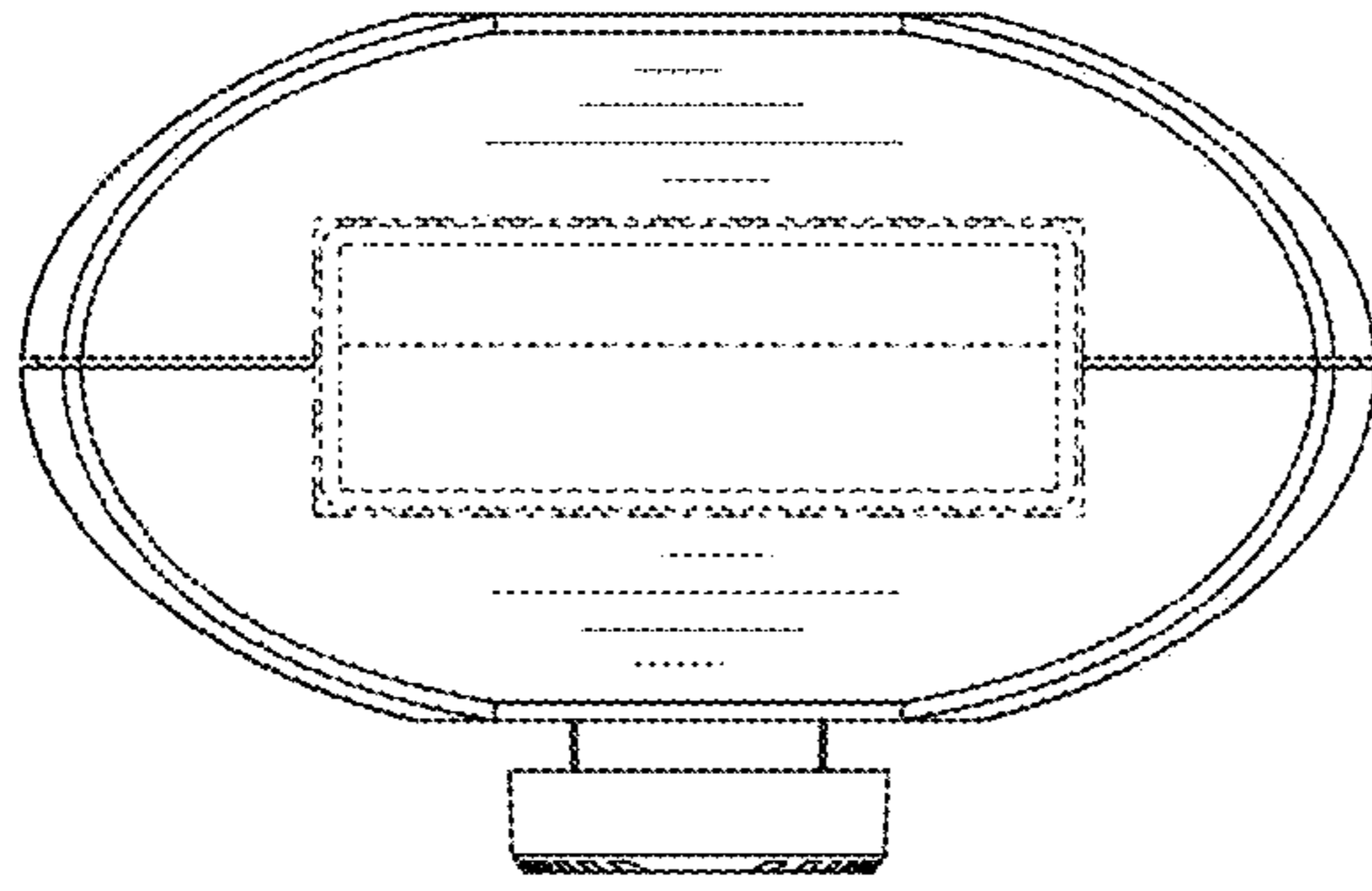


FIG. 7

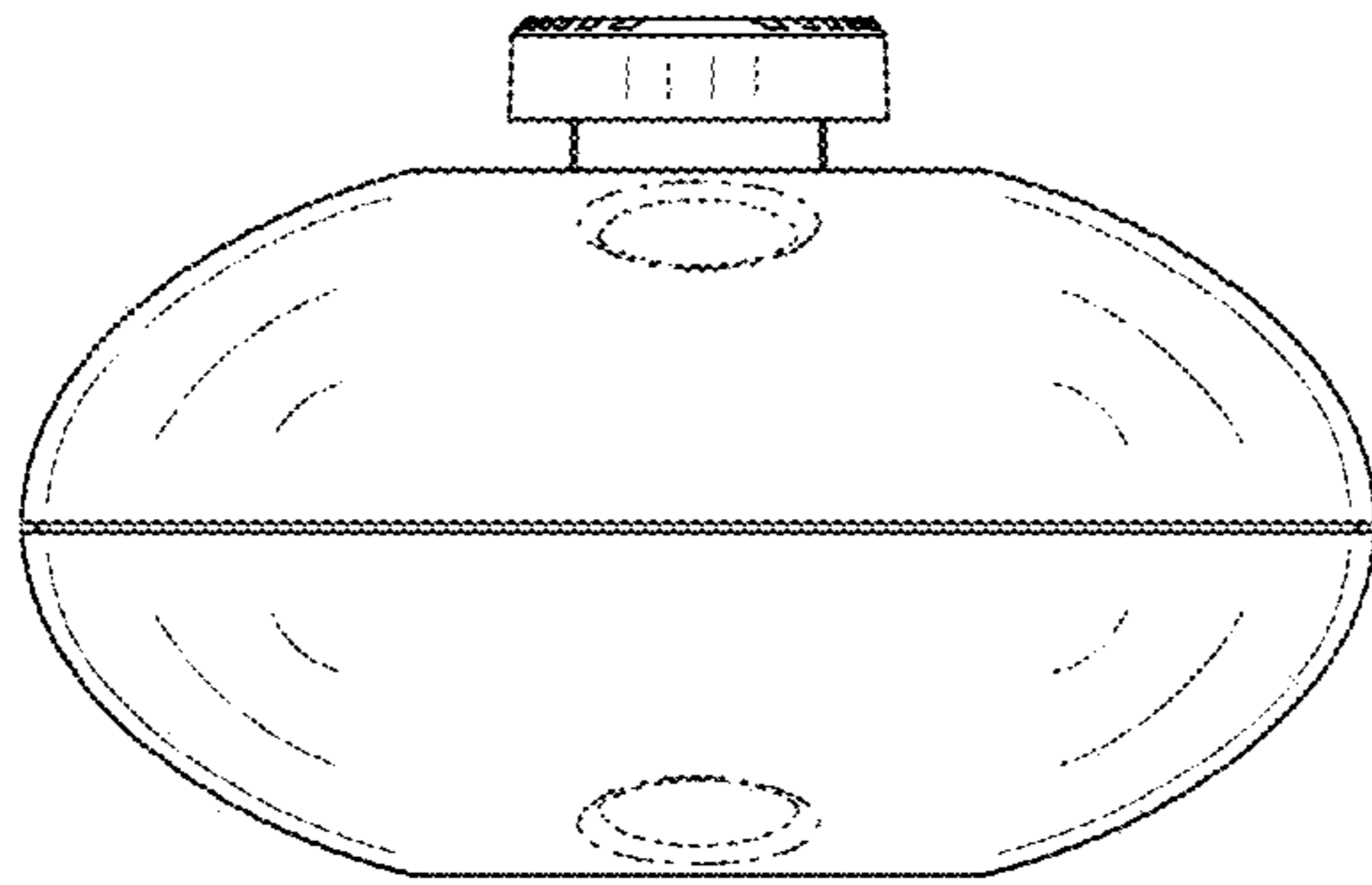


FIG. 8