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(12) **United States Design Patent** (10) **Patent No.:** **US D907,213 S**
Vosch et al. (45) **Date of Patent:** **** *Jan. 5, 2021**

(54) **PATCH WITH ELECTRODE ARRAY**

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(*) Notice: This patent is subject to a terminal disclaimer.

(Continued)

(**) Term: **15 Years**

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(51) **LOC (13) Cl.** **24-02**

(57) **CLAIM**

(52) **U.S. Cl.**

The ornamental design for a patch with electrode array, as shown and described.

USPC **D24/187**

(58) **Field of Classification Search**

USPC D24/107, 168, 186, 187, 189, 200

CPC A61B 5/0002; A61B 5/042; A61B 5/0404;

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A61N 1/046; A61N 1/0472; A61N

1/0476; A61N 1/0492

See application file for complete search history.

DESCRIPTION

FIG. 1 is a top perspective view of an patch with electrode array showing our new design;

FIG. 2 is a bottom perspective view thereof;

FIG. 3 is a bottom view thereof;

FIG. 4 is a top view thereof;

FIG. 5 is a side view thereof;

FIG. 6 is a side view thereof;

FIG. 7 is a side view that opposes the side view of FIG. 6; and,

FIG. 8 is a side view that opposes the side view of FIG. 5.

The dot-dot-dash lines in the figures depict the boundaries of the claim and form no part thereof. The evenly spaced broken lines in FIGS. 2 and 3 depict portions of the patch with electrode array that form no part of the claimed design.

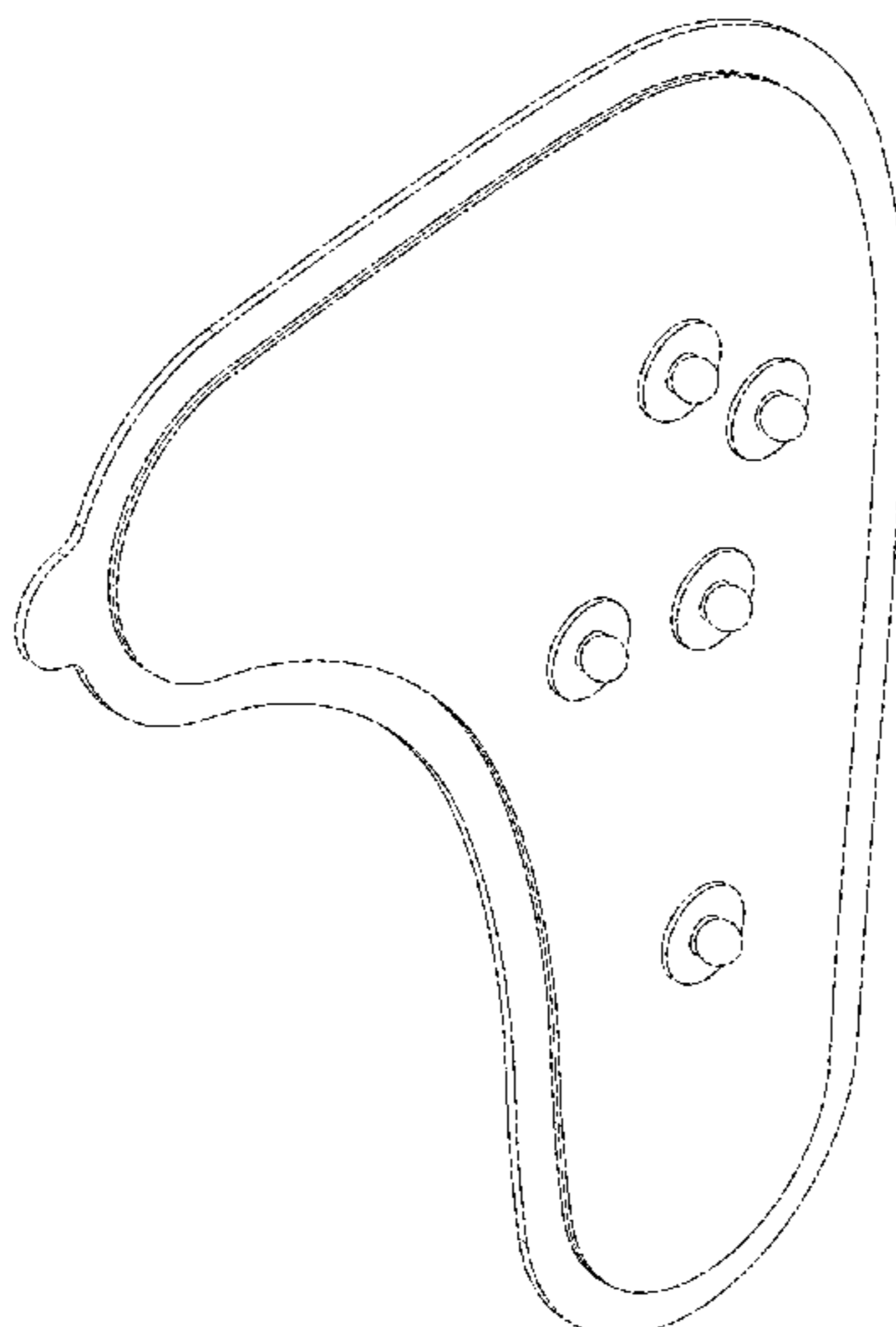
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1 Claim, 5 Drawing Sheets



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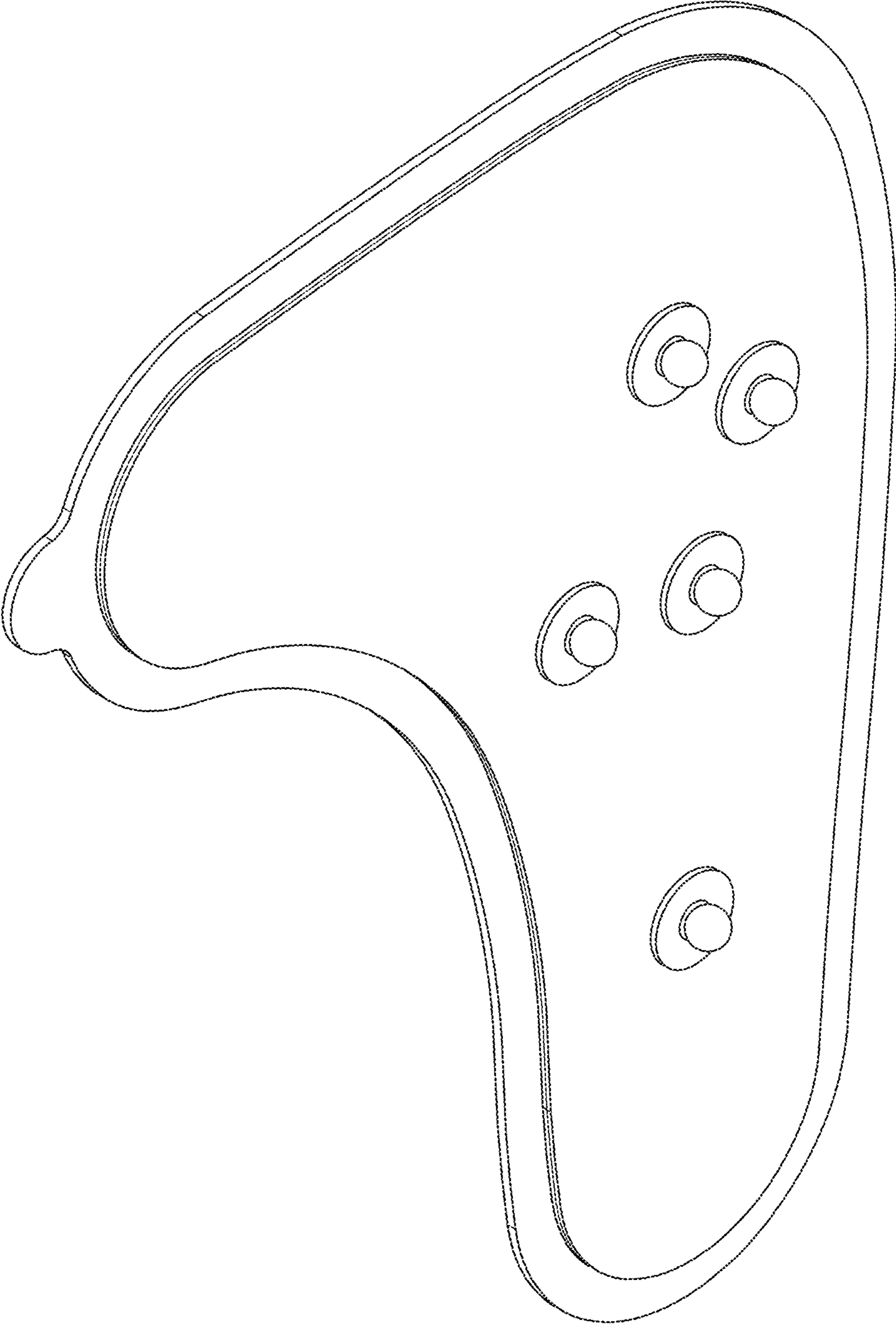


FIG. 1

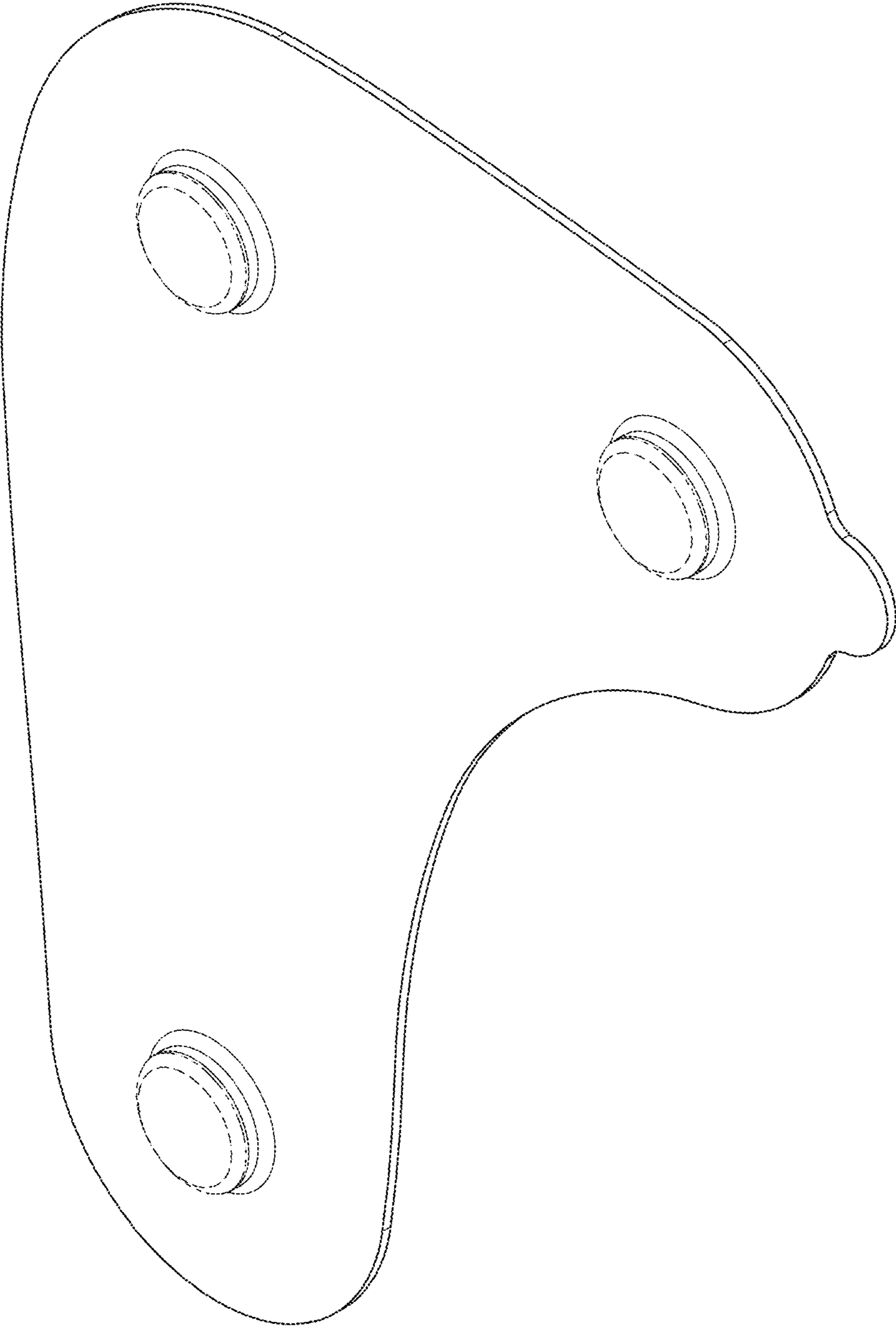


FIG. 2

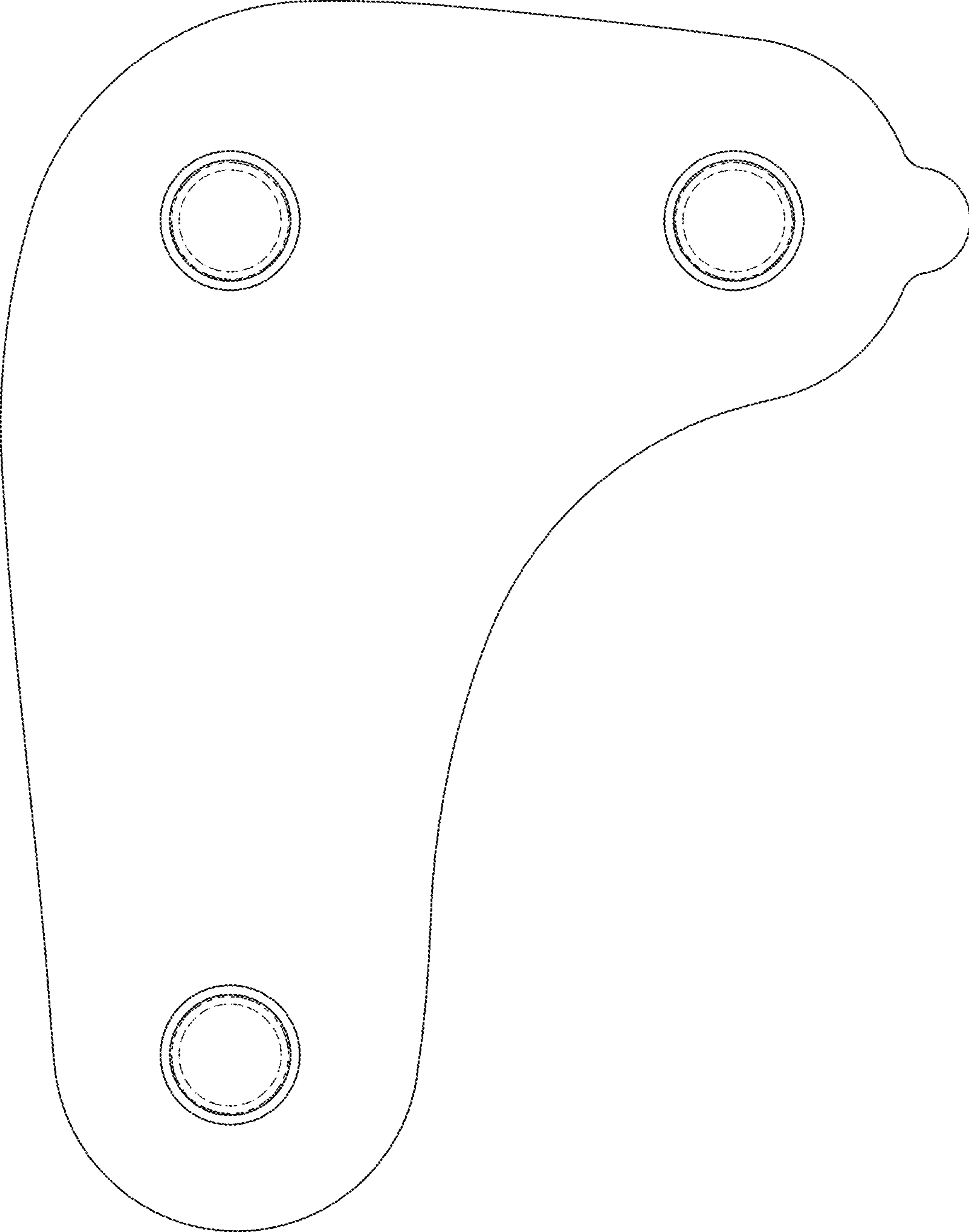


FIG. 3

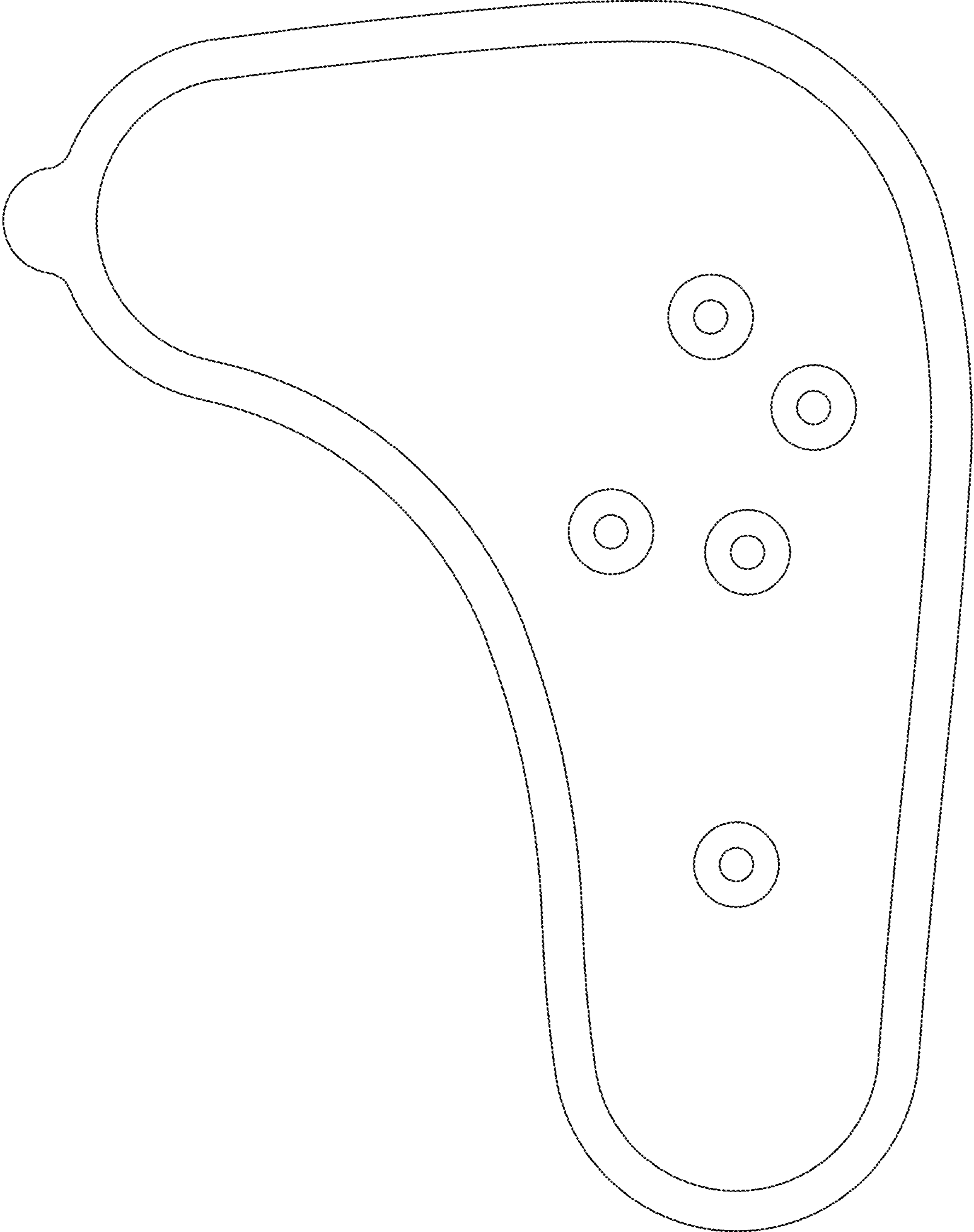


FIG. 4

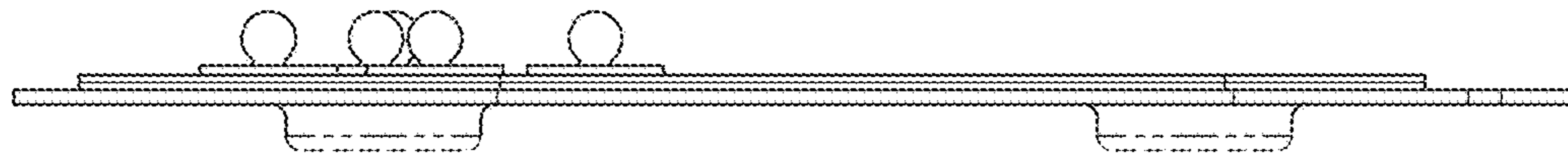


FIG. 5

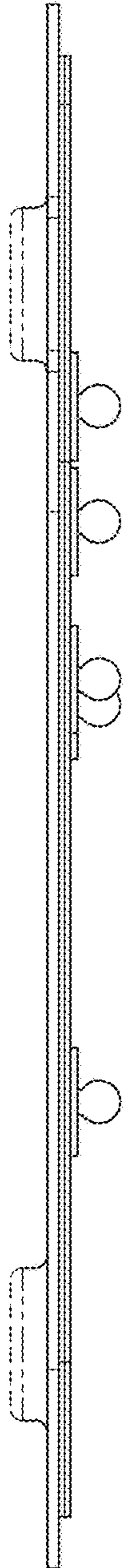


FIG. 6

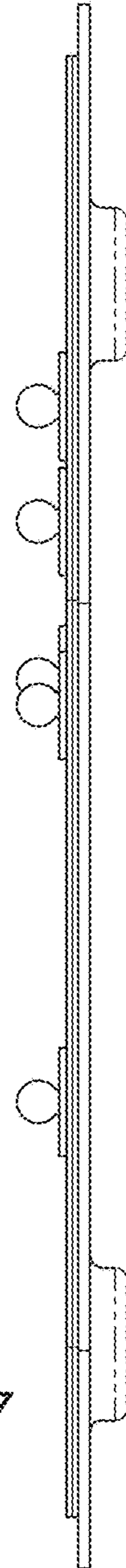


FIG. 7

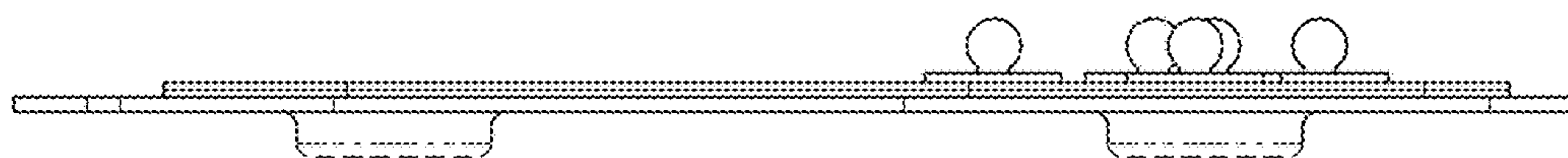


FIG. 8