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(12) **United States Design Patent** (10) **Patent No.:** **US D902,448 S**  
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(54) **LIGHT EMITTING DIODE PACKAGE**

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(\*\*) Term: **15 Years**

(57) **CLAIM**

(21) Appl. No.: **29/661,900**

The ornamental design for a light emitting diode package, as shown and described.

(22) Filed: **Aug. 31, 2018**

**DESCRIPTION**

(51) **LOC (12) Cl.** ..... **26-02**

(52) **U.S. Cl.**  
USPC ..... **D26/24**

(58) **Field of Classification Search**  
USPC ..... D26/1, 24, 51, 72, 104, 80  
CPC ..... F21L 4/00; F21L 7/00; F21L 15/16; F21S 8/09; F21V 7/09; F21V 21/30  
See application file for complete search history.

FIG. 1 is a top perspective view of a light emitting diode chip showing our design;  
FIG. 2 is a top view thereof;  
FIG. 3 is a bottom view thereof;  
FIG. 4 is an end view thereof;  
FIG. 5 is an opposing end view thereof;  
FIG. 6 is a side view thereof;  
FIG. 7 is an opposing side view thereof;  
FIG. 8 is a top perspective view of a light emitting diode chip showing our design;  
FIG. 9 is a top view thereof;  
FIG. 10 is a bottom view thereof;  
FIG. 11 is an end view thereof;  
FIG. 12 is an opposing end view thereof;  
FIG. 13 is a side view thereof; and,  
FIG. 14 is an opposing side view thereof.

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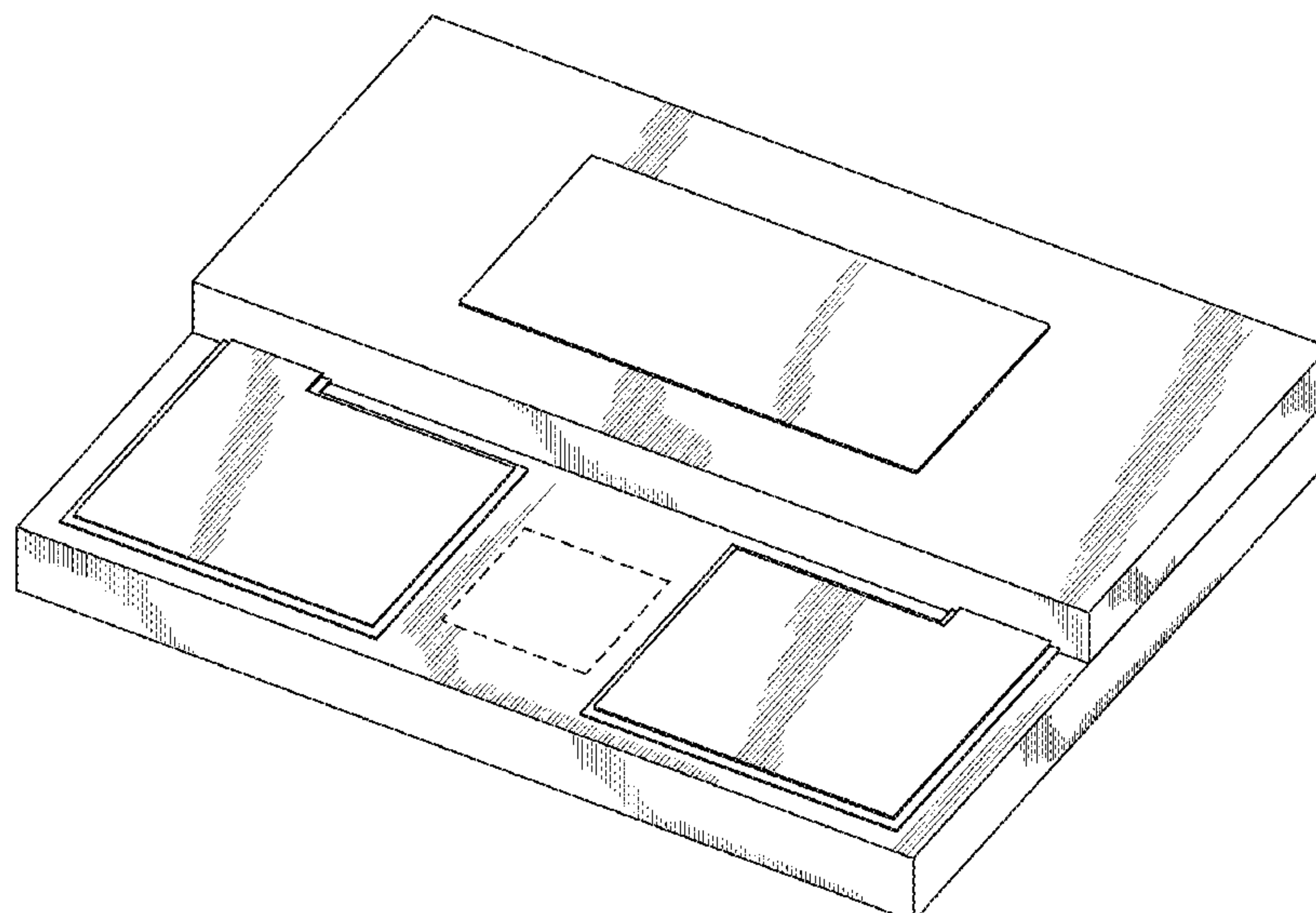
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The broken lines shown in the drawings depict portions of the light emitting diode chip that form no part of the claimed design.

The break lines in some of the figures indicate that the portions shown within the break lines form no part of the claimed design.

**1 Claim, 10 Drawing Sheets**





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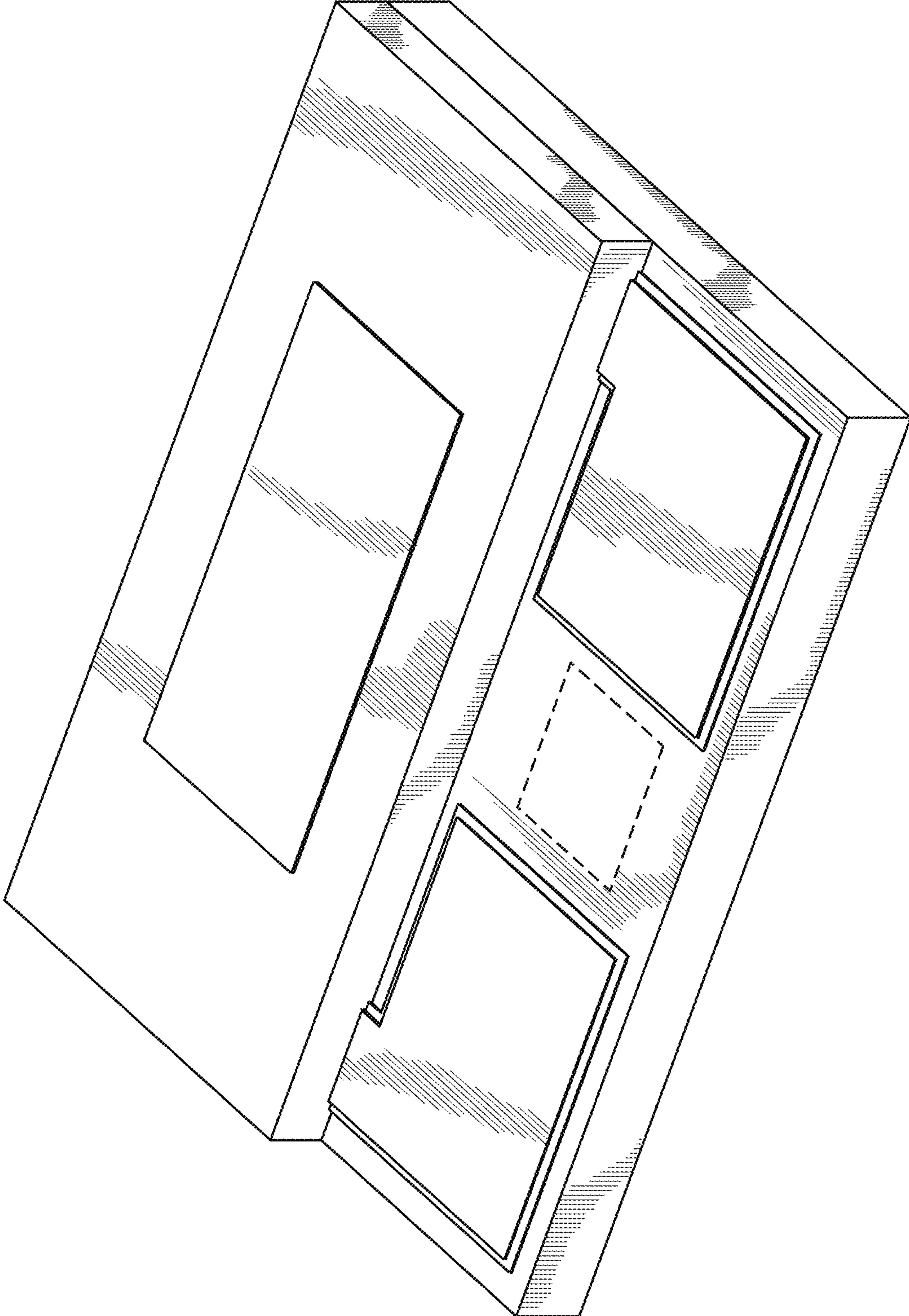


FIG. 1

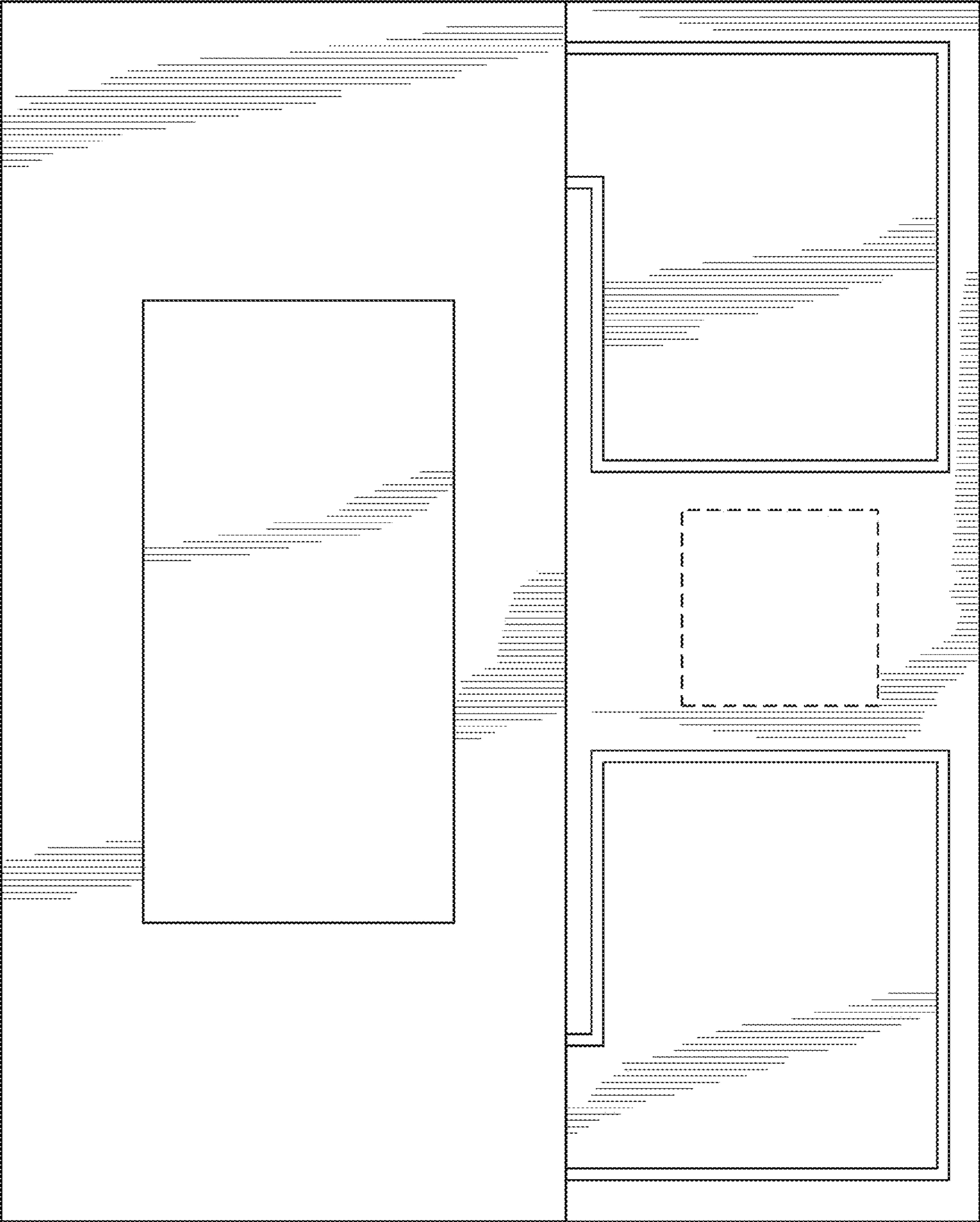
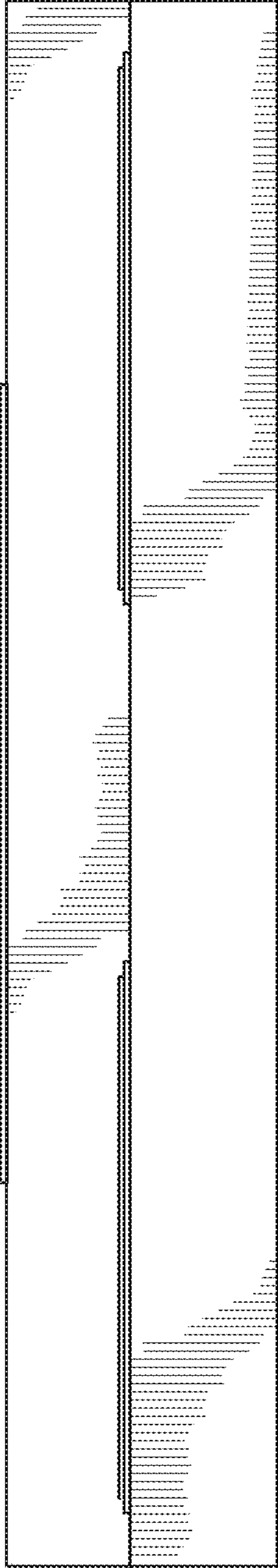


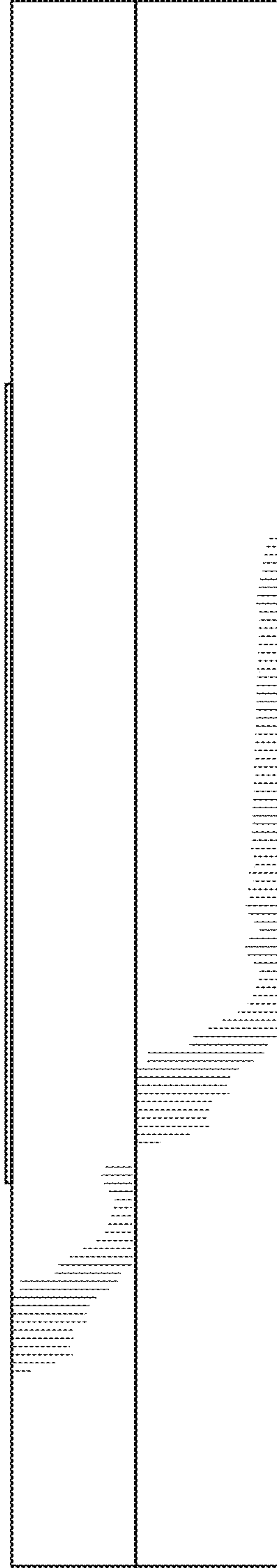
FIG. 2



FIG. 3



**FIG. 4**



**FIG. 5**

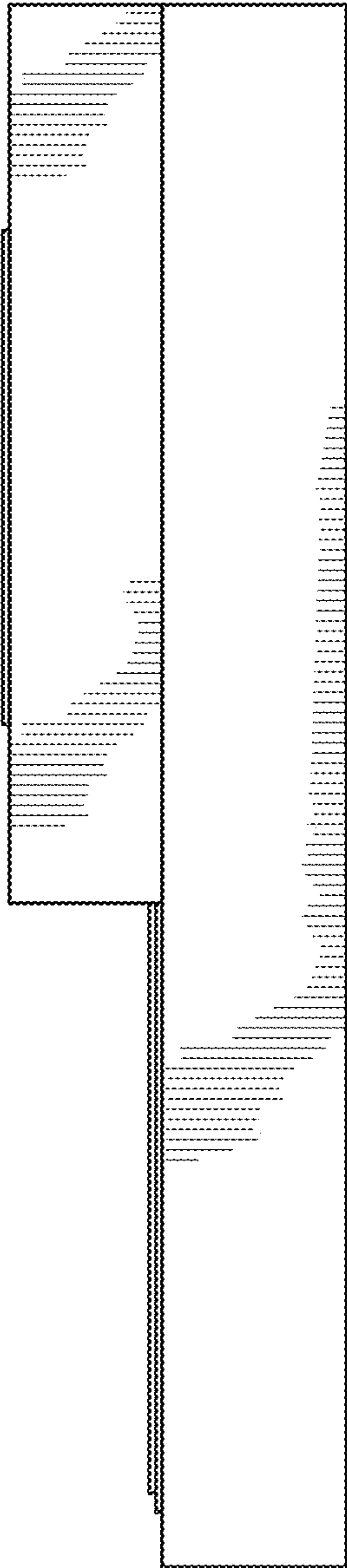


FIG. 6

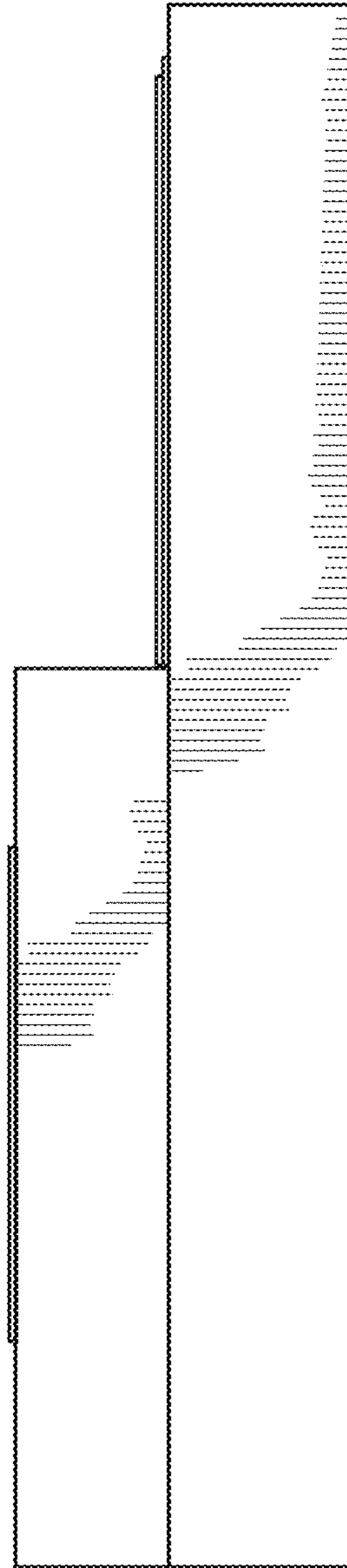


FIG. 7



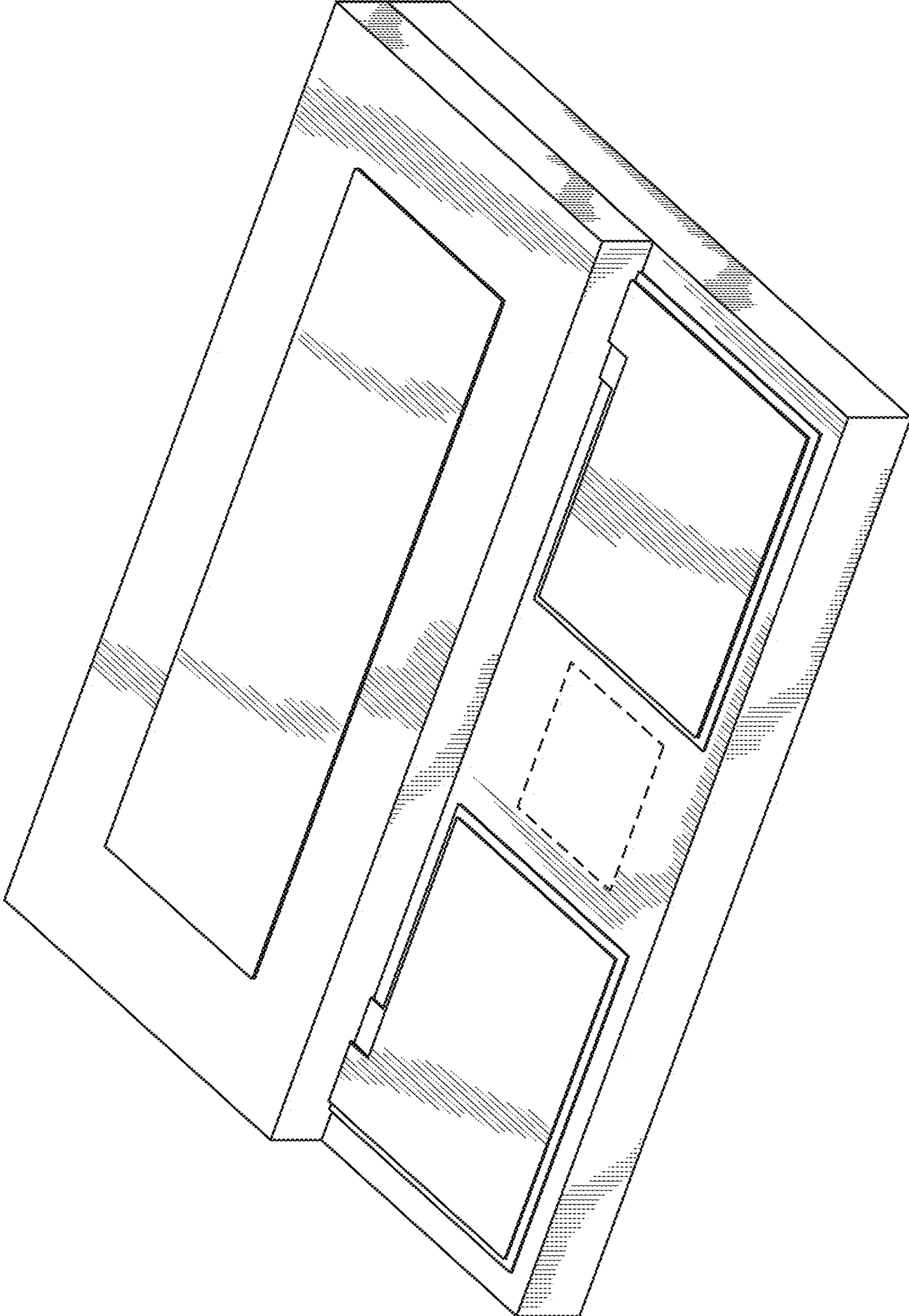


FIG. 8

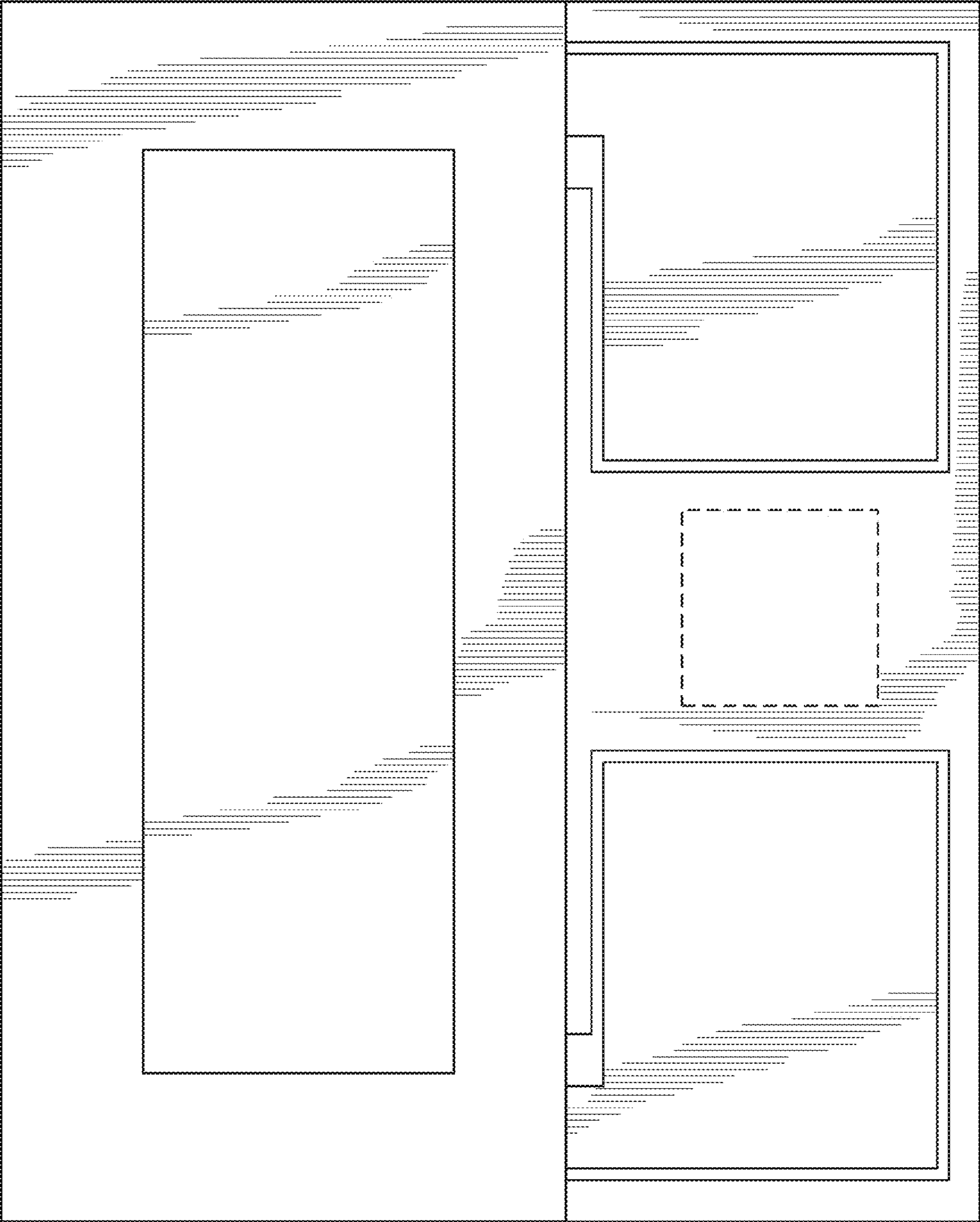


FIG. 9



FIG. 10

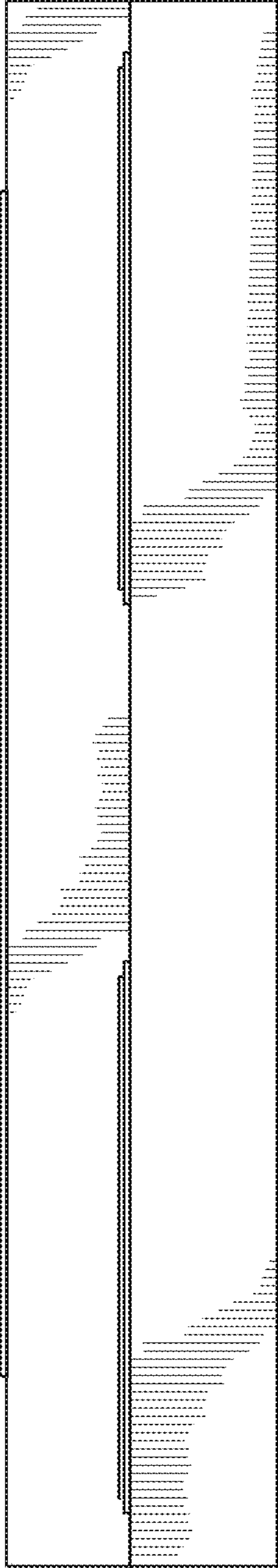


FIG. 11

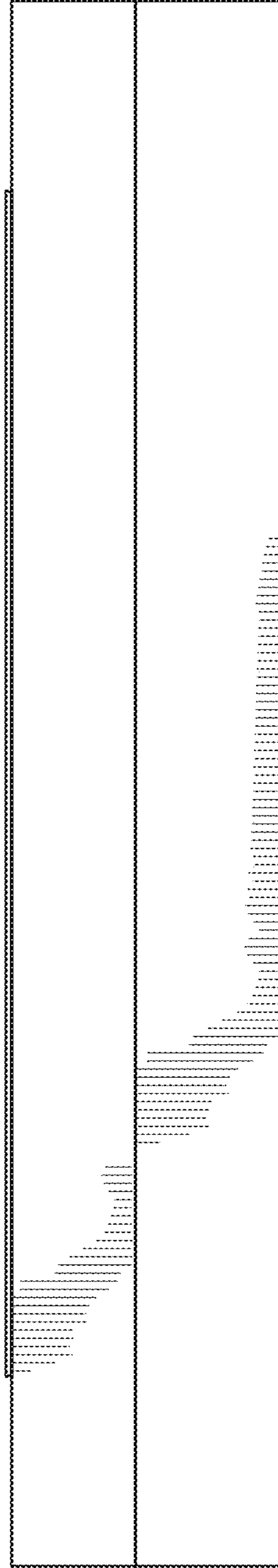


FIG. 12



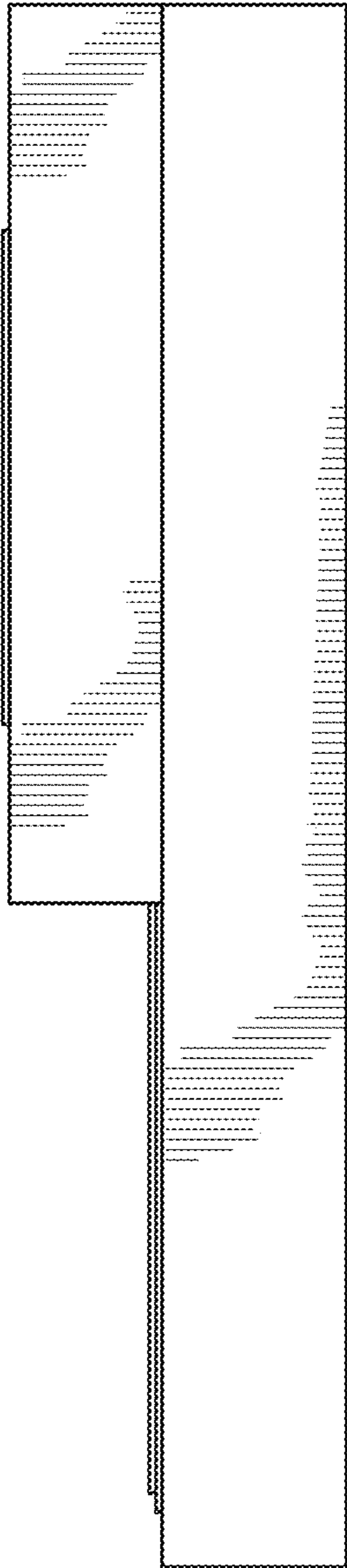


FIG. 13

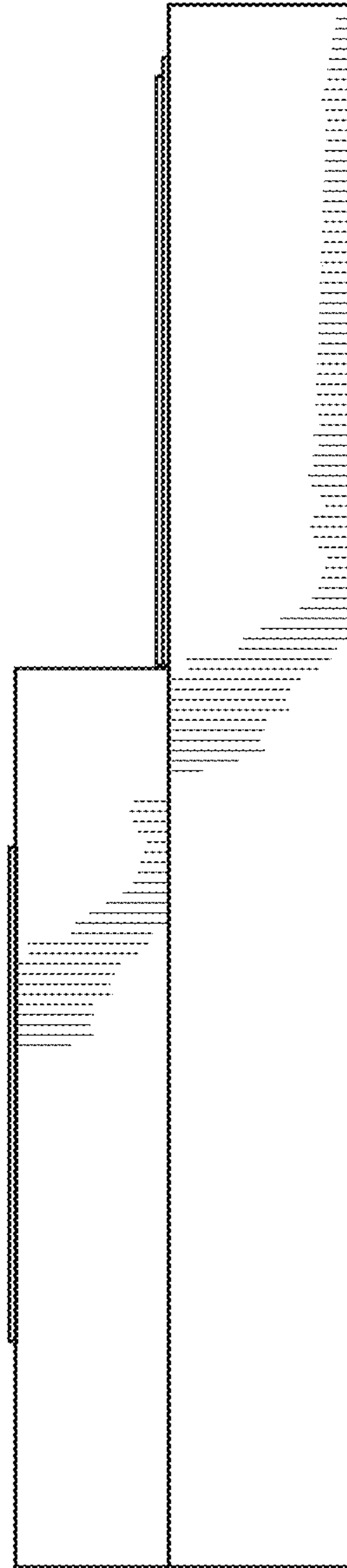


FIG. 14