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(12) **United States Design Patent** (10) **Patent No.:** **US D900,827 S**
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(54) **INTEGRATED CIRCUIT BUILT-IN CARD**
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 (Continued)

(73) Assignee: **KIOXIA CORPORATION**, Tokyo (JP)
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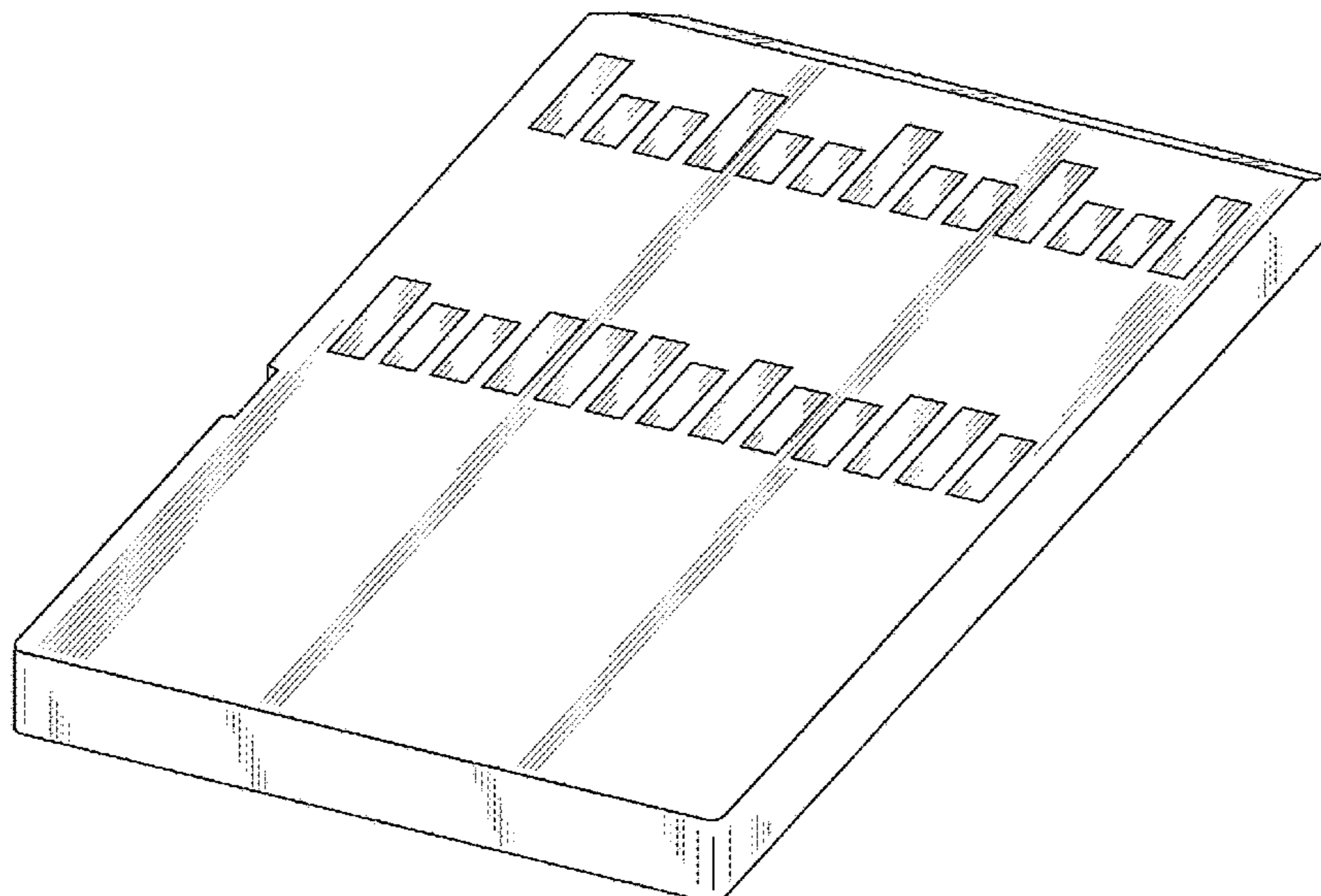
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 (52) **U.S. Cl.**
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 USPC D14/435-438, 478-480, 400, 356;
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 361/737, 679, 752, 736, 796, 797, 729,
 361/730, 725-728, 753, 799-802, 807,
 361/816, 818, 829; 257/678, 679, 693,
 257/E23.064; 439/946, 948, 159, 945,
 439/136-140, 181, 55, 60, 630, 680, 951;
 174/250, 260, 52.1, 52.2; 710/300-303;
 438/121; D21/333, 332; D13/182
 CPC G06K 19/06196; G06K 19/072; G06K
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 G06K 19/0724; G06K 19/0725; G06K
 19/0726; G06K 19/0727; G06K 19/0728;
 G06K 19/073; G06K 19/07309; G06K
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 19/07715; G06K 19/0772; G06K
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(57) **CLAIM**
 The ornamental design for an integrated circuit built-in card, as shown and described.

DESCRIPTION
 FIG. 1 is a front, bottom, right side perspective view of an integrated circuit built-in card, showing our new design; FIG. 2 is a rear, bottom, left side perspective view thereof; FIG. 3 is a front view thereof; FIG. 4 is a top view thereof; FIG. 5 is a right side view thereof; FIG. 6 is a left side view thereof; FIG. 7 is a bottom view thereof; and, FIG. 8 is a rear view thereof.

1 Claim, 6 Drawing Sheets



(58) **Field of Classification Search**

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 2201/10598; H04N 2201/216; H01R
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See application file for complete search history.

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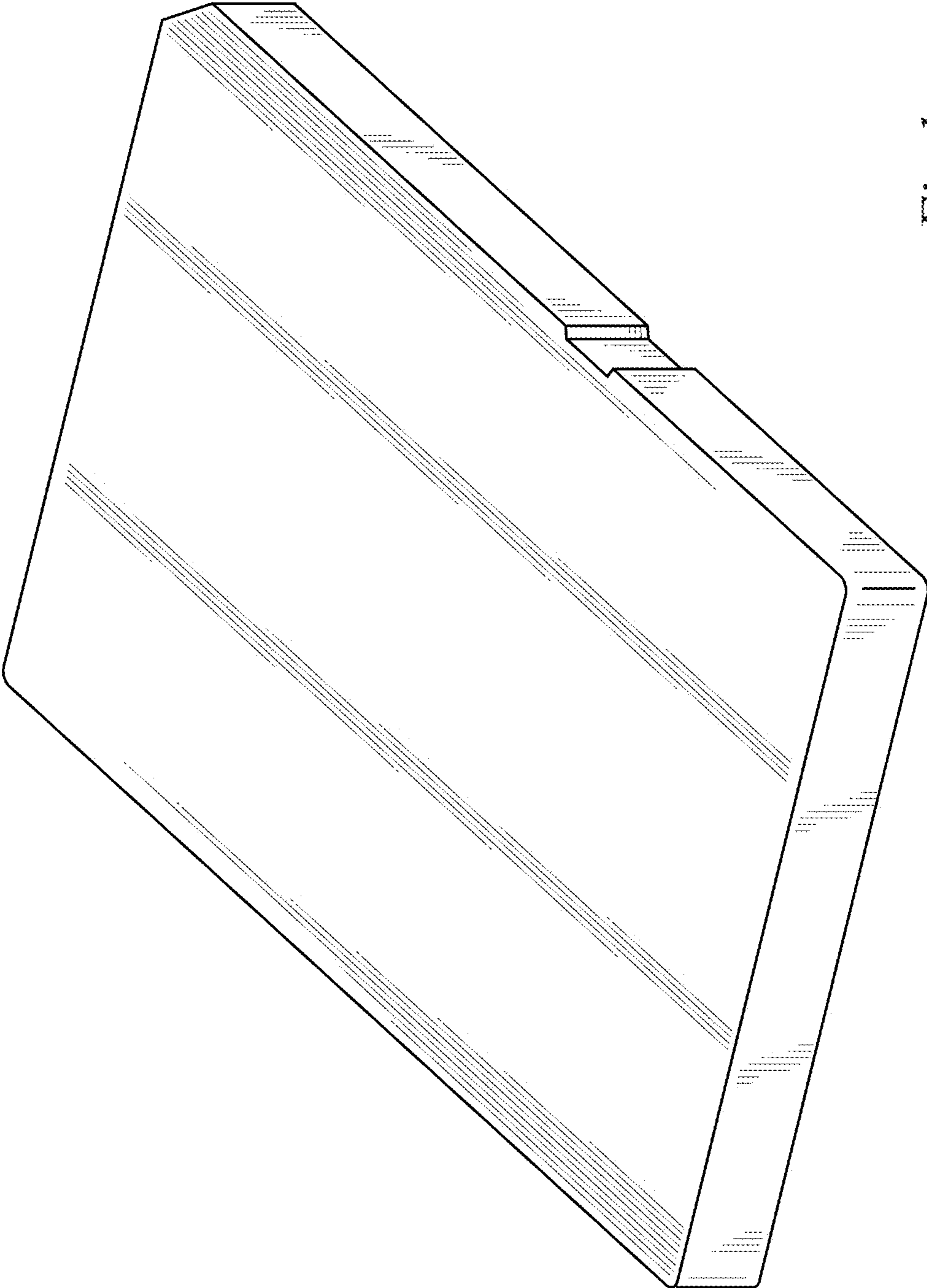


Fig. 1

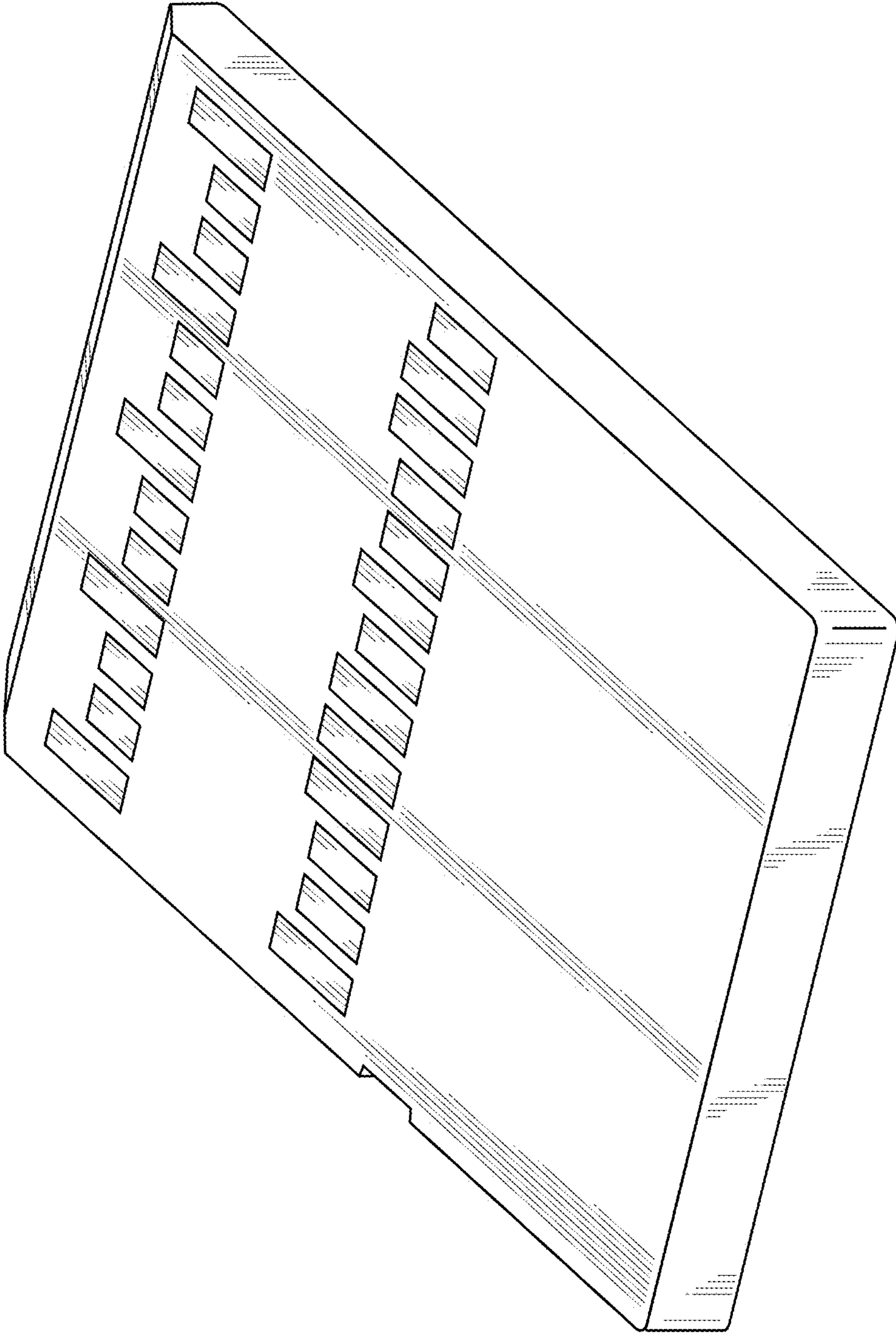


Fig. 2



Fig. 3



Fig. 5

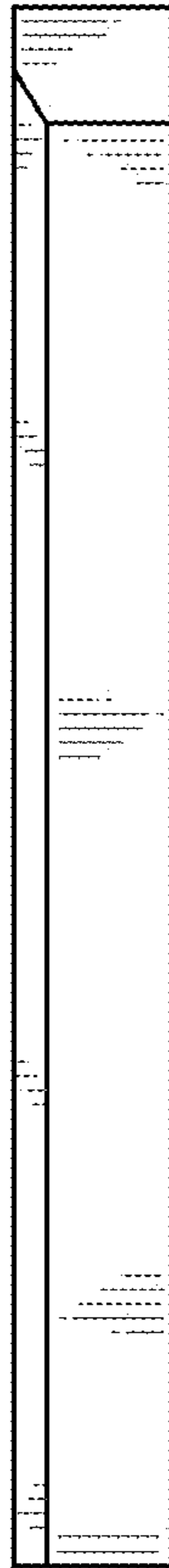


Fig. 4



Fig. 6

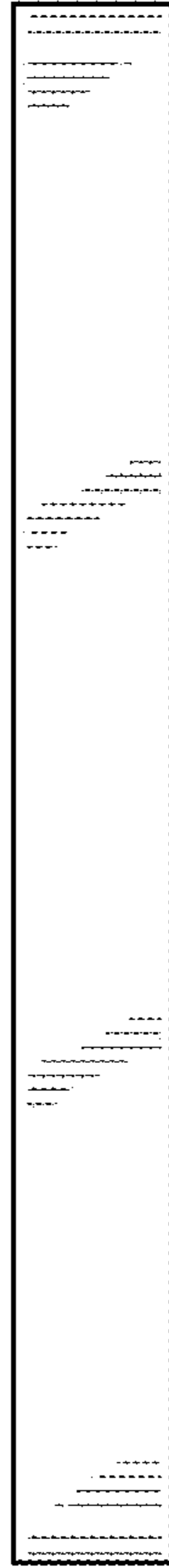


Fig. 7

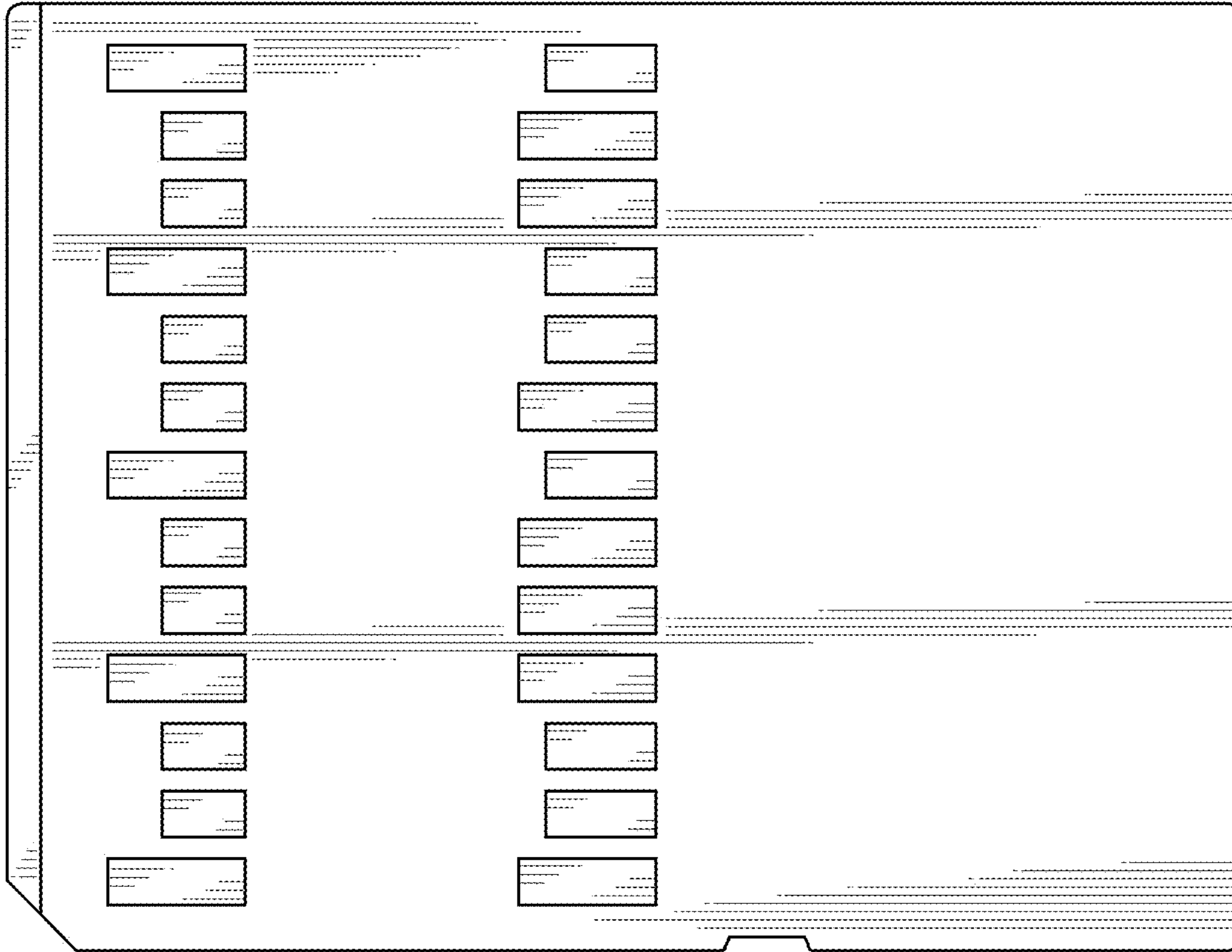


Fig. 8