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(12) **United States Design Patent** (10) **Patent No.:** **US D900,827 S**
Kondo et al. (45) **Date of Patent:** **** Nov. 3, 2020**

(54) **INTEGRATED CIRCUIT BUILT-IN CARD**
(71) Applicant: **KIOXIA CORPORATION**, Tokyo (JP)
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(Continued)

(73) Assignee: **KIOXIA CORPORATION**, Tokyo (JP)
(**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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(52) **U.S. Cl.**
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361/816, 818, 829; 257/678, 679, 693,
257/E23.064; 439/946, 948, 159, 945,
439/136-140, 181, 55, 60, 630, 680, 951;
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438/121; D21/333, 332; D13/182
CPC G06K 19/06196; G06K 19/072; G06K
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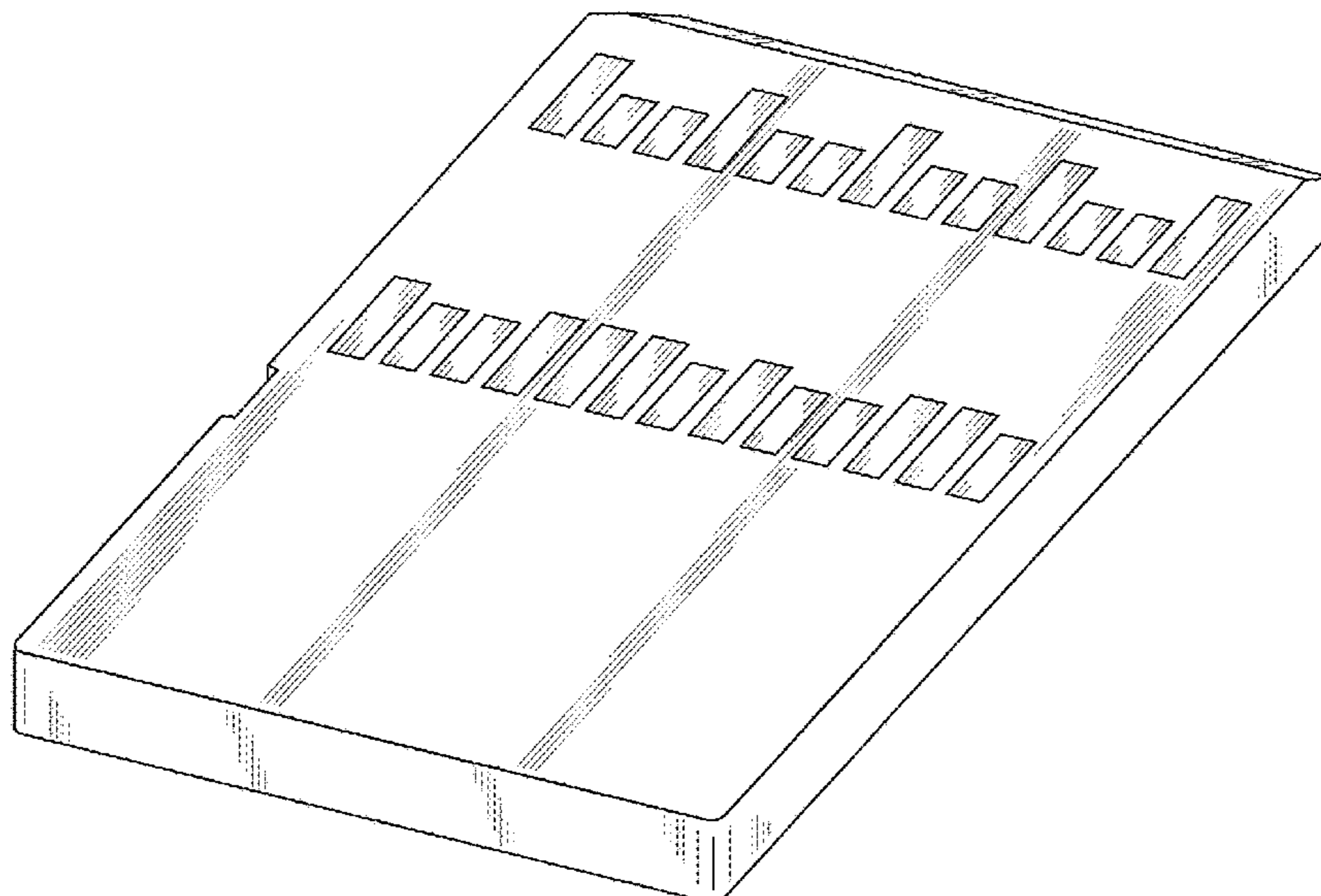
(57) **CLAIM**

The ornamental design for an integrated circuit built-in card, as shown and described.

DESCRIPTION

FIG. 1 is a front, bottom, right side perspective view of an integrated circuit built-in card, showing our new design; FIG. 2 is a rear, bottom, left side perspective view thereof; FIG. 3 is a front view thereof; FIG. 4 is a top view thereof; FIG. 5 is a right side view thereof; FIG. 6 is a left side view thereof; FIG. 7 is a bottom view thereof; and, FIG. 8 is a rear view thereof.

1 Claim, 6 Drawing Sheets



(58) **Field of Classification Search**

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 2201/09745; H05K 2201/09754; H05K
 2201/09763; H05K 2201/09772; H05K
 2201/098; H05K 2201/09818; H05K
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 2201/10212; H05K 2201/1028; H05K
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 2201/10462; H05K 2201/10545; H05K
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 2201/10621; H05K 2201/10636; H05K
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 H05K 3/36; H05K 3/40; H05K 3/4007;
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See application file for complete search history.

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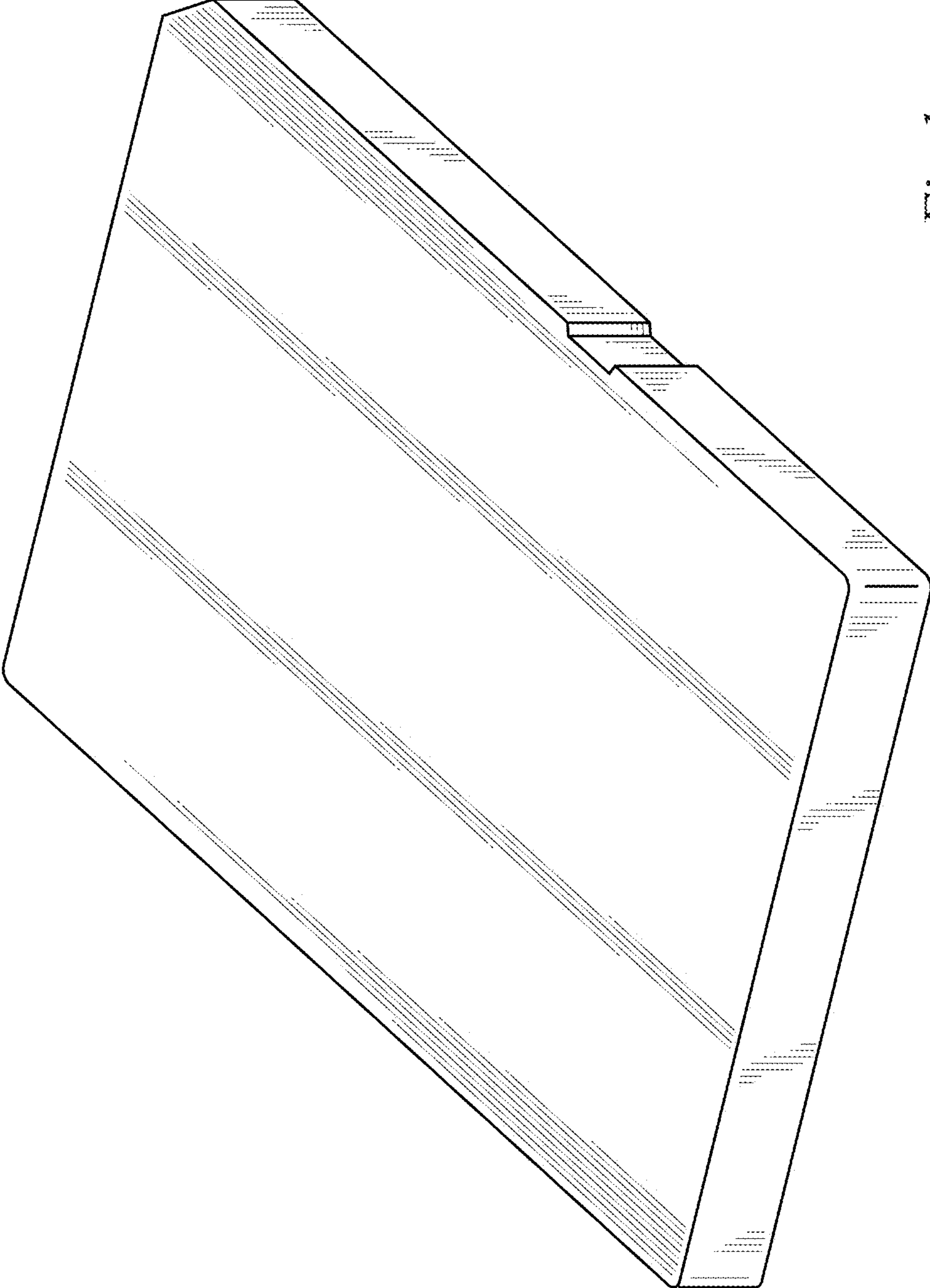


Fig. 1

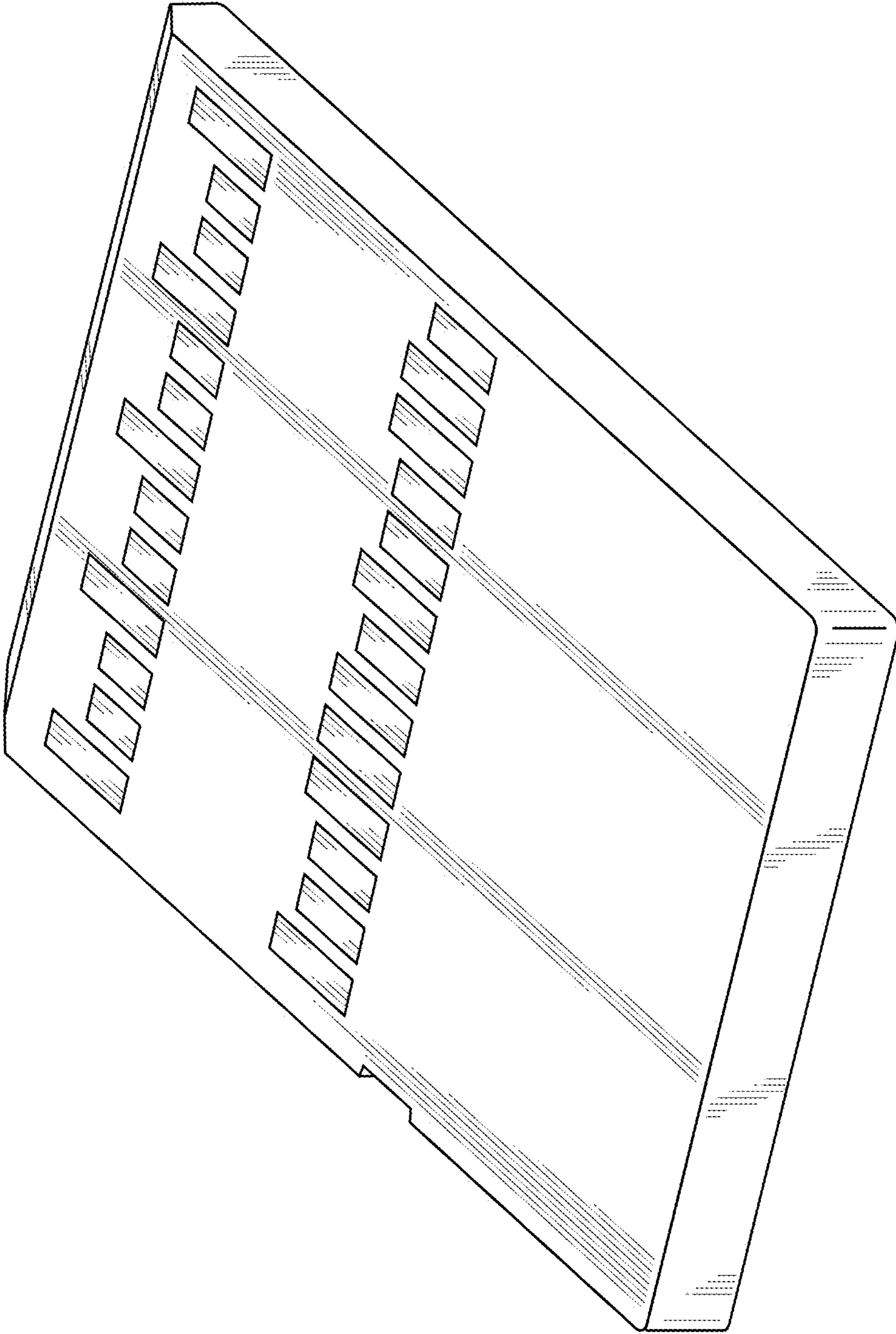


Fig. 2



Fig. 3



Fig. 5

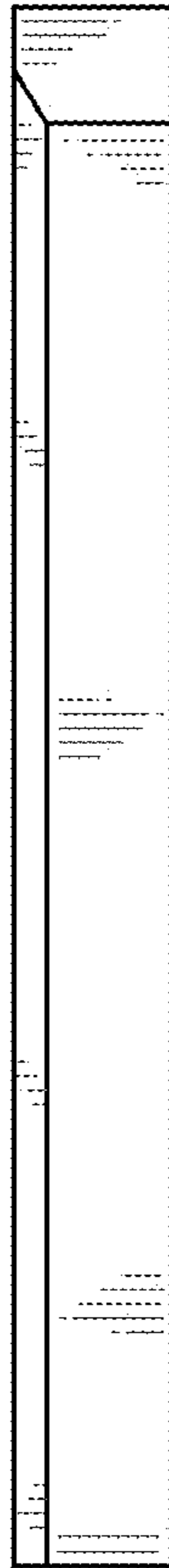


Fig. 4



Fig. 6

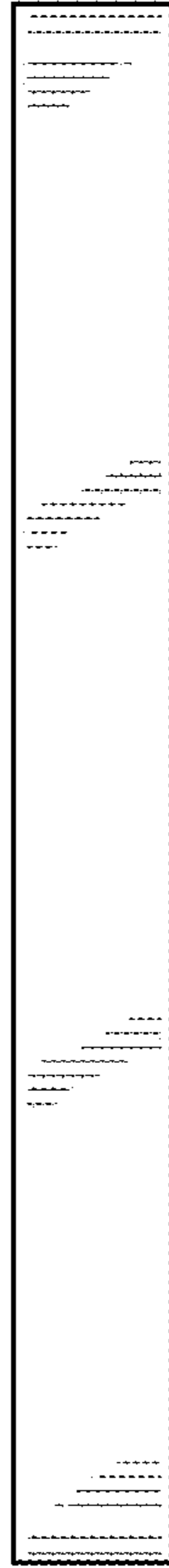


Fig. 7

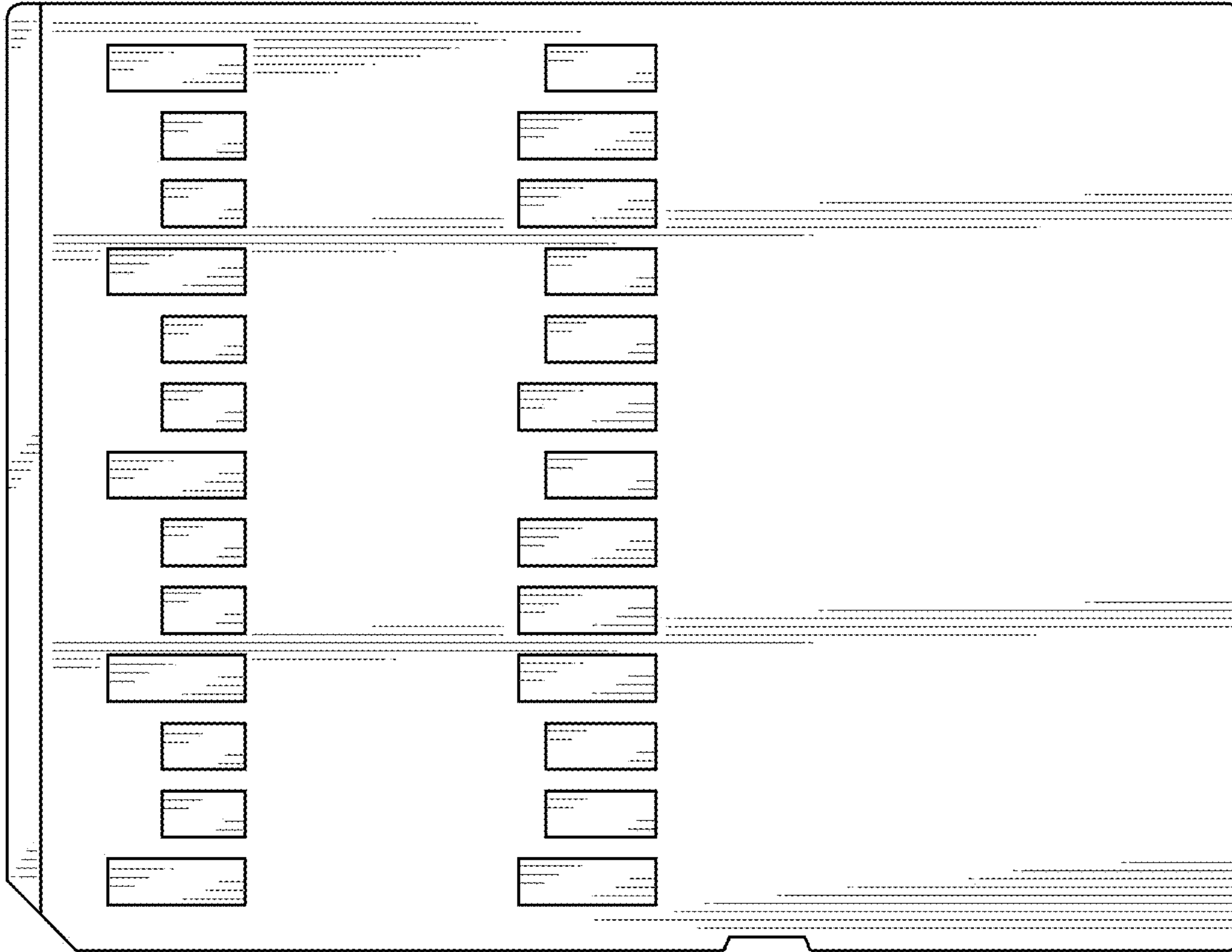


Fig. 8