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(12) **United States Design Patent**
Yoshida et al.

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(45) **Date of Patent:** **** Oct. 6, 2020**

(54) **SEAL MEMBERS FOR USE IN SEMICONDUCTOR PRODUCTION APPARATUSES**

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(73) Assignee: **Valqua, Ltd.**, Tokyo (JP)

(**) Term: **15 Years**

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(80) **Hague Agreement Data**

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Int. Reg. Pub. Date: **Dec. 7, 2018**

(30) **Foreign Application Priority Data**

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Dec. 19, 2017 (JP) 2017-028256

(51) **LOC (12) Cl.** **23-01**

(52) **U.S. Cl.**
USPC **D23/269**
CPC **F16J 15/025** (2013.01)

(58) **Field of Classification Search**
USPC D23/269, 262; D8/382; 277/351–359,
277/504, 562, 563, 572, 361–364, 381,
277/606, 616, 314, 382, 394, 395;
D25/48.2, 122, 164
CPC F16J 15/025; F16J 15/064
See application file for complete search history.

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(57) **CLAIM**

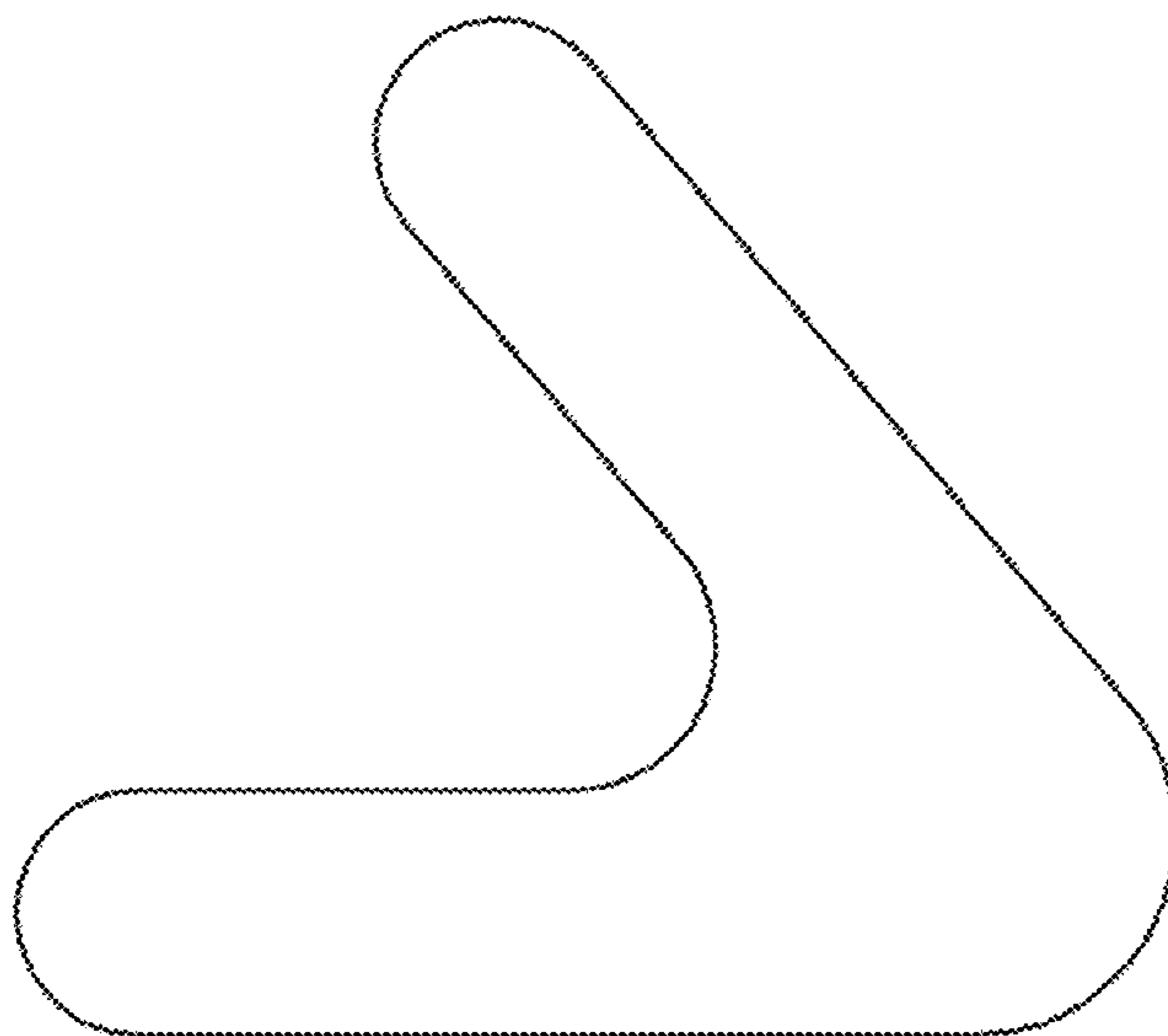
The ornamental design for seal member for use in semiconductor production apparatus, as shown and described.

DESCRIPTION

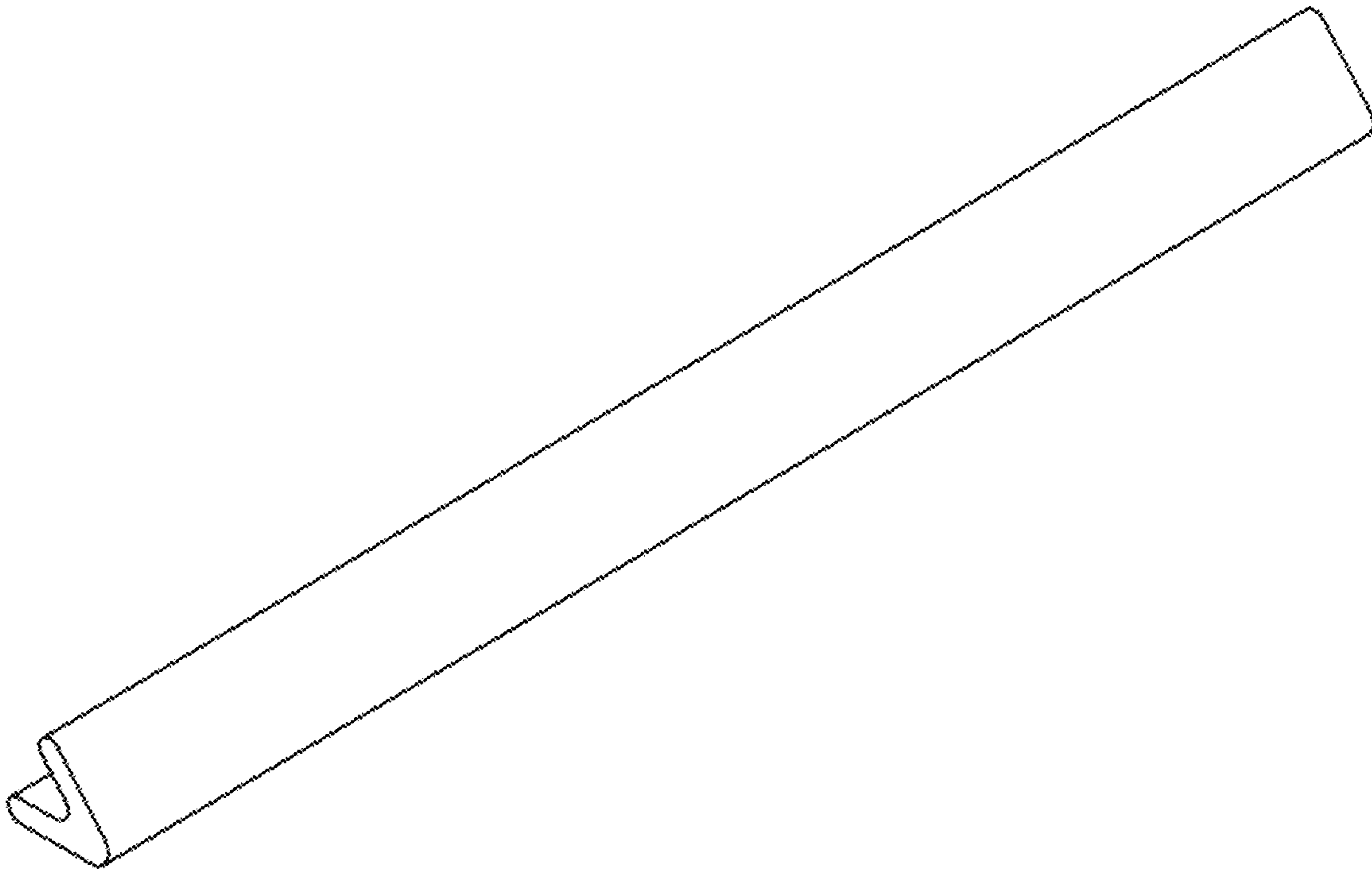
1.1 is a perspective view of a seal member for use in semiconductor production apparatuses, showing our new design;
1.2 is a front elevation view thereof;
1.3 is a rear elevation view thereof;
1.4 is a left side elevation view thereof;
1.5 is a right side elevation view thereof;
1.6 is a top view thereof;
1.7 is a bottom view thereof;
1.8 is an enlarged left side elevation view thereof; and
1.9 is an enlarged left side elevation view thereof in a condition of use.

The broken lines shown in Reproduction **1.9** are for the purposes of illustrating environmental structure and form no part of the claimed design.

1 Claim, 9 Drawing Sheets



1.1



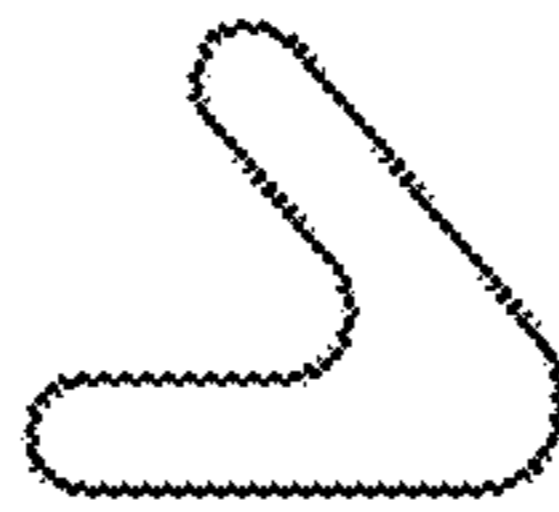
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1.3



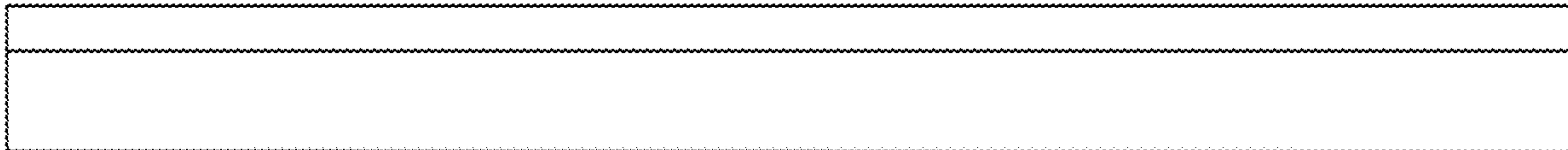
1.4



1.5



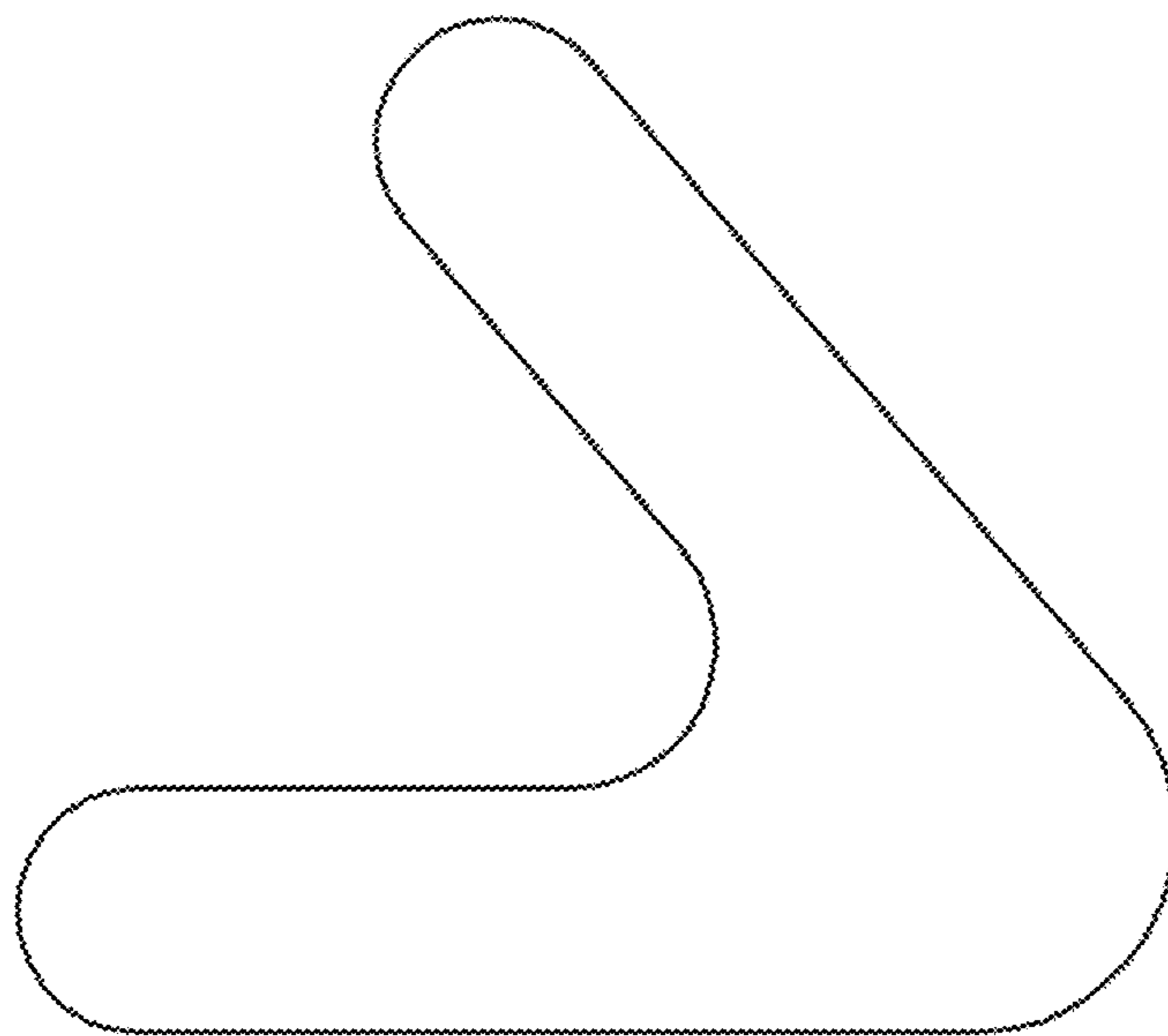
1.6



1.7



1.8



1.9

